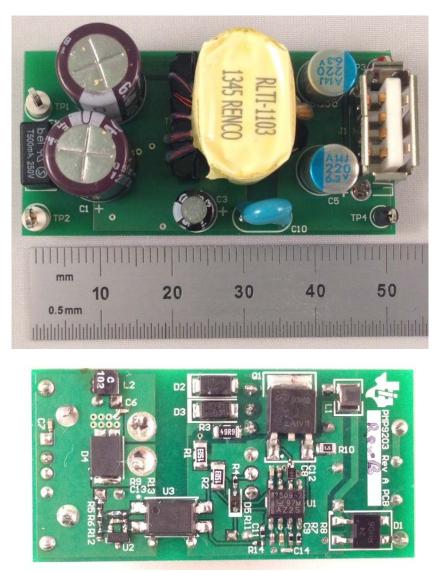


1 Photos

The photograph below shows the PMP9203 Rev B prototype assembly. This circuit was built on a PMP9203 Rev A PCB.

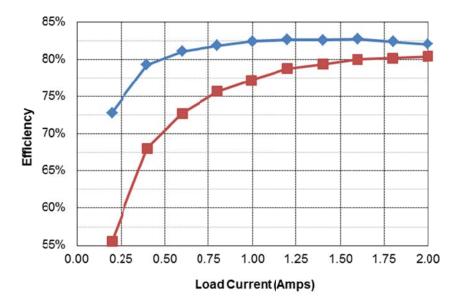


2 Standby Power

With no load attached to the output of the supply, the unit draws 216mW of input power with an 115VAC/60Hz input, and 584mW with a 230VAC/50Hz input.



3 Efficiency



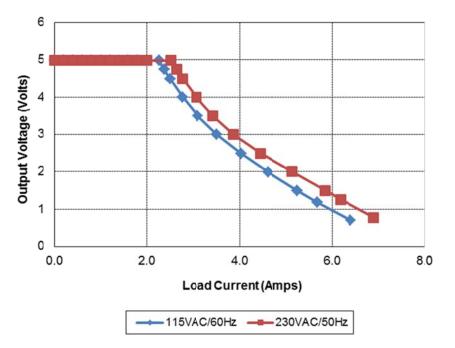
115VAC/6	0Hz							
lout	Vout	Vin	lin	Pin	PF	Pout	Losses	Efficiency
0.000	4.991	115.0	0.0073	0.216		0.00	0.22	0.0%
0.197	4.990	115.0	0.030	1.35	0.38	0.98	0.37	72.8%
0.399	4.990	115.0	0.050	2.51	0.43	1.99	0.52	79.3%
0.600	4.989	115.0	0.069	3.69	0.47	2.99	0.70	81.1%
0.801	4.989	115.0	0.086	4.88	0.49	4.00	0.88	81.9%
1.000	4.989	115.0	0.103	6.05	0.51	4.99	1.06	82.5%
1.200	4.988	115.0	0.119	7.24	0.53	5.99	1.25	82.7%
1.400	4.988	115.0	0.135	8.45	0.54	6.98	1.47	82.6%
1.603	4.998	115.0	0.151	9.68	0.56	8.01	1.67	82.8%
1.801	4.987	115.0	0.167	10.90	0.57	8.98	1.92	82.4%
1.999	4.987	115.0	0.184	12.15	0.58	9.97	2.18	82.0%
230VAC/50Hz								
lout	Vout	Vin	lin	Pin	PF	Pout	Losses	Efficiency
0.000	4.991	229.7	0.0102	0.584		0.00	0.58	0.0%
0.197	4.992	230.0	0.025	1.77	0.31	0.98	0.79	55.6%
0.401	4.991	230.0	0.038	2.94	0.34	2.00	0.94	68.1%
0.599	4.990	230.0	0.049	4.11	0.36	2.99	1.12	72.7%
0.801	4.990	230.0	0.060	5.28	0.38	4.00	1.28	75.7%
0.992	4.989	230.0	0.070	6.41	0.40	4.95	1.46	77.2%
1.200	4.989	230.0	0.080	7.60	0.41	5.99	1.61	78.8%
1.402	4.988	230.0	0.090	8.81	0.42	6.99	1.82	79.4%
1.600	4.987	230.0	0.100	9.97	0.44	7.98	1.99	80.0%
1.800	4.987	230.0	0.109	11.19	0.45	8.98	2.21	80.2%
2.000	4.986	230.0	0.119	12.40	0.46	9.97	2.43	80.4%

	-230VAC/50Hz
--	--------------



4 Power Limit

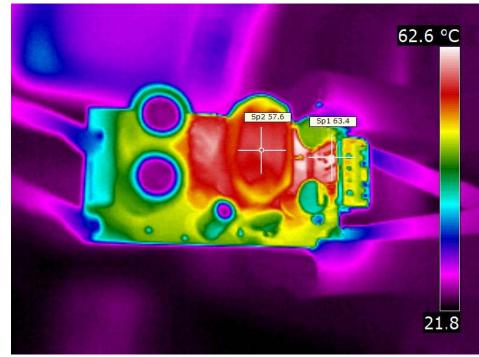
A plot of the output voltage versus load current is shown below.



5 Thermal Images

The ambient temperature was 25°C. The output was loaded with 2A.

5.1 115VAC/60Hz Input

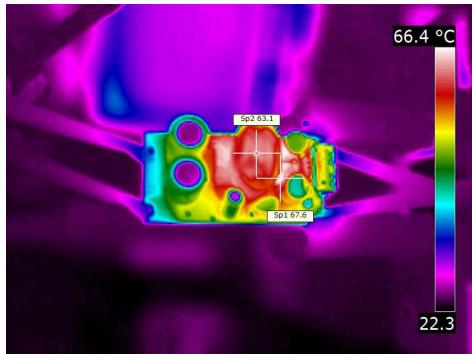


11/25/2013 PMP9203 Rev B Test Results



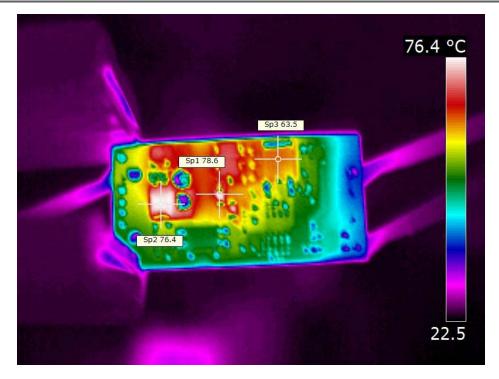


5.2 230VAC/50Hz Input



11/25/2013 PMP9203 Rev B Test Results

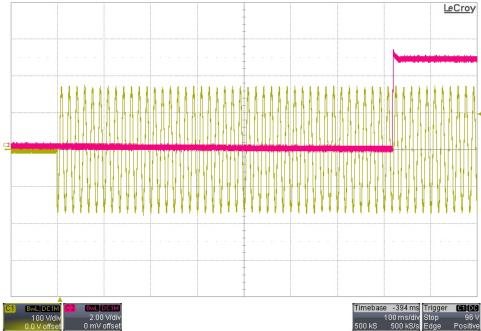




6 Startup

Channel 1 shows the AC input voltage. Channel 2 shows the output voltage.



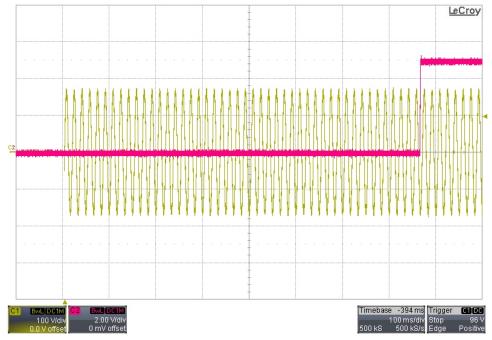




6.2 230VAC/50Hz Startup – 0A Load

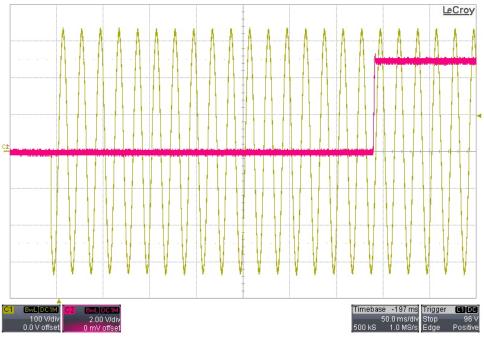


6.3 115VAC/60Hz Startup – 2.5Ω Load





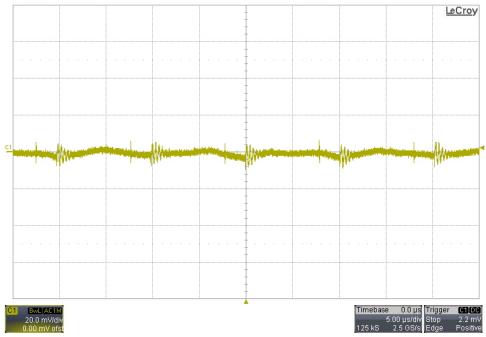
6.4 230VAC/50Hz Startup – 2.5Ω Load



7 Output Ripple Voltage

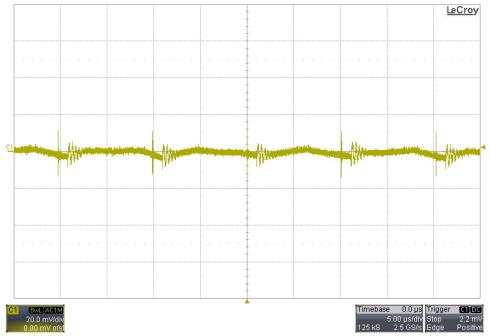
The output was loaded with 2A.

7.1 115VAC/60Hz Output Ripple Voltage



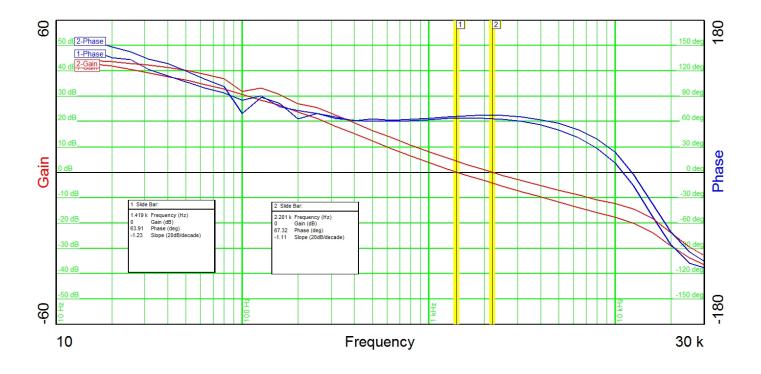


7.2 230VAC/50Hz Output Ripple Voltage



8 Frequency Response

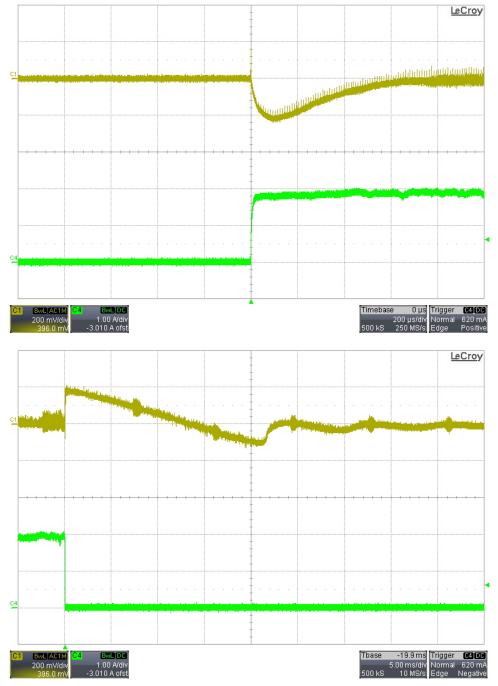
The frequency response of the feedback loop measured at R5 is shown below. For the gain/phase plot #1, the input was set to 115VAC/60Hz. For the gain/phase plot #2, the input was set to 230VAC/50Hz. The output was loaded with 2A.





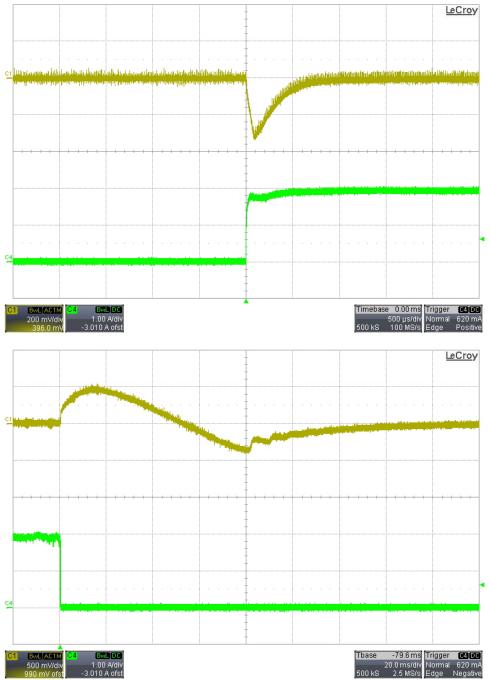
9 Load Transients

9.1 0A to 2A Transient – 115VAC/60Hz Input





9.2 0A to 2A Transient – 230VAC/50Hz Input



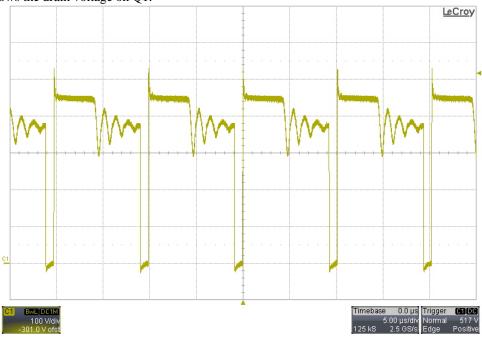


10 Switching Waveforms

The images below show the voltage waveforms on the switching devices within the supply. The input was 265VAC/50Hz. The output was loaded 2A.

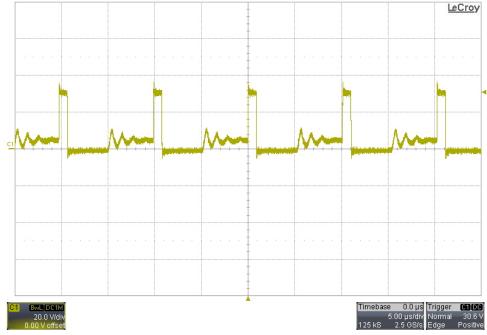
10.1 Primary Waveforms

The image below shows the drain voltage on Q1.



10.2 Secondary Waveforms

The image below shows the voltage on the cathode of D4.



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