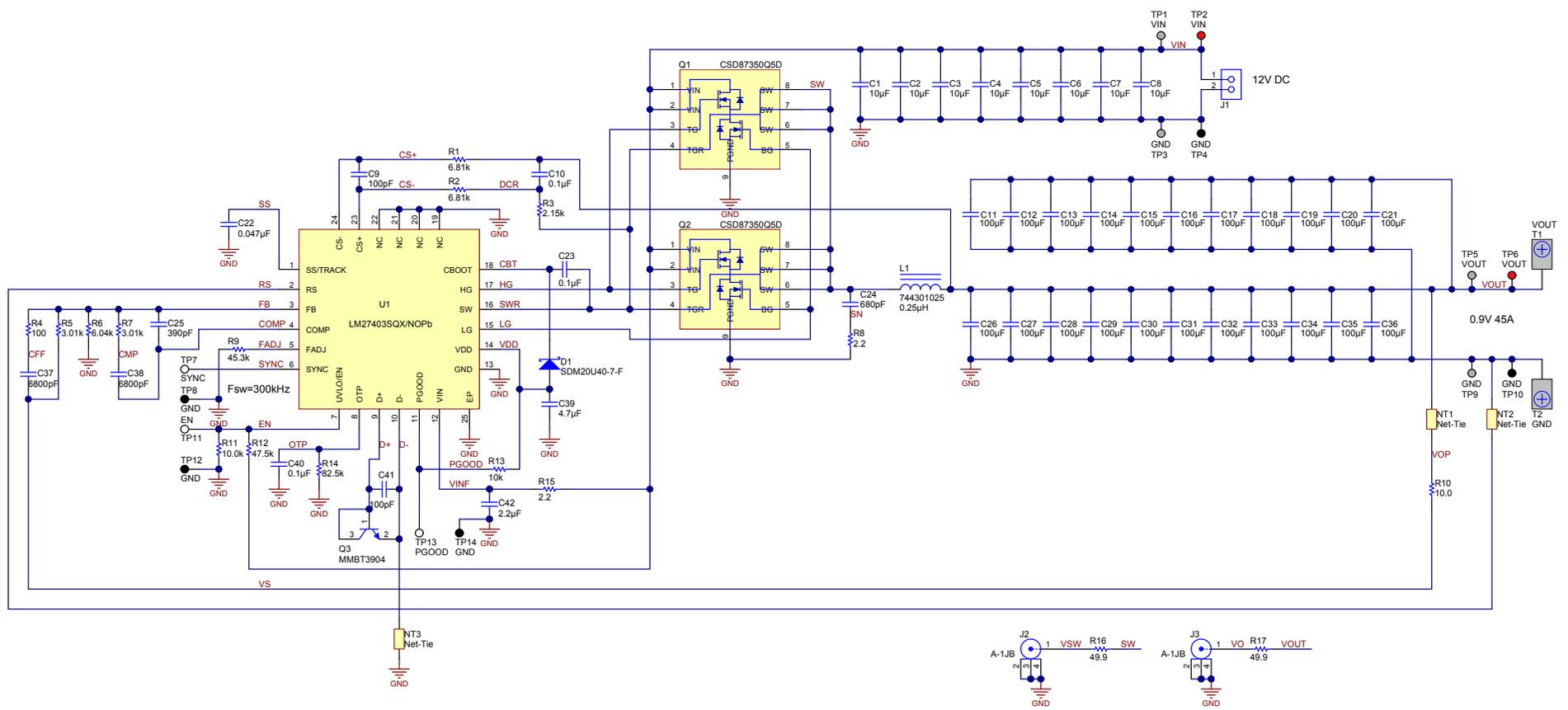


Revision History	
Revision	Notes
A	Initial design 4/10/2013
B	Add GND to D-, adjust current limit and compensation 4/24/2013



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Designed for: Public Release		Mod. Date: 4/25/2013	
Project Title: LM27403 Buck		Number: PMP7935 Rev: B	
Sheet Title: LM27403 Buck		SVN Rev: Not in version control	
Assembly Variant: [No Variations]		Drawn By: Robert Sheehan	
File: PMP7935_REV B_Sht1.SchDoc		Engineer: Robert Sheehan	
Contact: http://www.ti.com/support		Sheet: 1 of 2	
		Size: B	
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PCB Number: PMP7935
PCB Rev: B

PCB
LOGO
Texas Instruments



LBL1
PCB Label
Size: 0.65" x 0.20"

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2., unless otherwise specified.

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Designed for: Public Release		Mod. Date: 4/25/2013	
Project Title: LM27403 Buck			
Number: PMP7935	Rev: B	Sheet Title: Hardware	
SVN Rev: Not in version control		Assembly Variant: [No Variations]	Sheet 2 of 2
Drawn By: Robert Sheehan		File: PMP7935_REV B_Sht2.SchDoc	Size: B
Engineer: Robert Sheehan		Contact: http://www.ti.com/support	

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