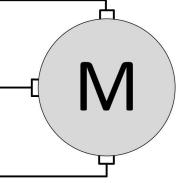


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Revision History						
Rev	ECN #	Approved Date	Approved by	Notes		
N/A	N/A	N/A	N/A	N/A		



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А		J1	$\frac{Vin C1 50V 47\mu F}{2}$ $\frac{1}{2}$ $\frac{1}{4}$ $\frac{1}{FGOUT}$ $\frac{1}{1}$ $\frac{1}{1}$ $\frac{1}{1}$ $\frac{1}{1}$ $\frac{1}{1}$ $\frac{1}{1}$ $\frac{1}{1}$	J2 4 3 V3 4 3 SBU 2 SBU 1 GND	NTDIO VTCLK C2 1000pF			
		SDA R7 100 SCL R8 100 50V 50V C9 C10 1000pF 1000p	GND 		TRI0- 22.0/TB1.1/CO P2.0/TB1.1/CO P3.0/TB0CLK/XIUT TB0CLK/XIUT TB0CLK/XIUT TB0CLK/XIUT TAB0CLK/XIUT TAB0CLK/XIUT TAB0CLK/XIUT TAB0CLK/XIUT TB0CLK/XIUT			
В								
		J3 3 VSP_PW 2 VSP 1 VSP_AD	<u>N</u>	$\begin{array}{c} Vin \\ \hline \\ $	VREG 6 SW 4 47μ H 0.11A 10V C13 10 μ F V 19 V 19			
с		SCL SDA GND G	3V3 R2 R4 F 4.7k 4.7k 4.7k 4.7k	R3 JV3 SPEED 13 SPEED	201 0 18 17 0 0 0 0 0 0 0 0 0 0 0 0 0			
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	PCB Number: TIDA-01585	PCB LOGO	PCB LOGO	PCB LOGO			
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		Pb-Free Symbol	ESD Susceptible	CAUTION. READ USER GUIDE BEFORE USE			
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	<u>ZZ1</u>						

ZZ1 Label Assembly Note This Assembly Note is for PCB labels only

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ZZ2 Assembly Note These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

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ZZ4 Assembly Note These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: ChangeMe! TID #: 01585 Number: TIDA-01585 SVN Rev: Version cont Drawn By: Engineer: Richard Wang

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