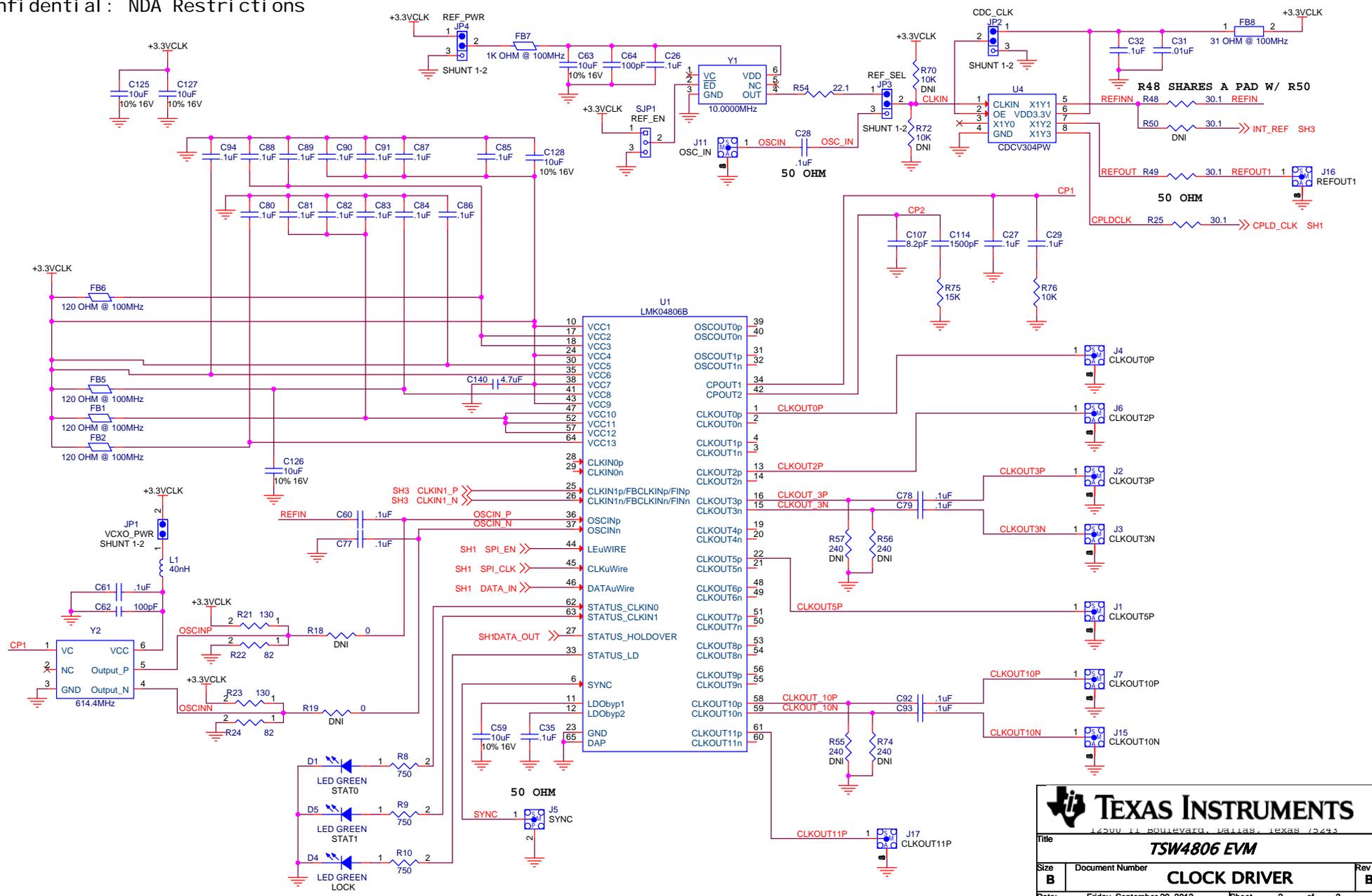


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Title: **TSW4806 EVM**

Size: B	Document Number: SERIAL INTERFACE	Rev: B
Engineer: J. SRTON	Date: Friday, September 20, 2013	Sheet 1 of 3
PCB Designer: JV SMITH		

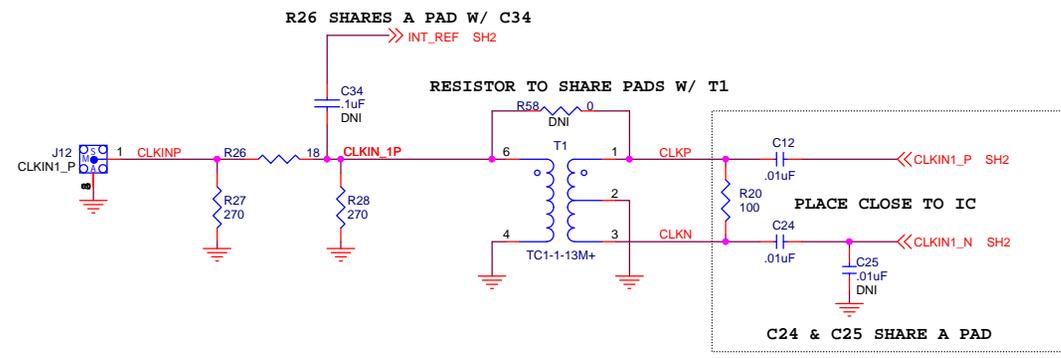
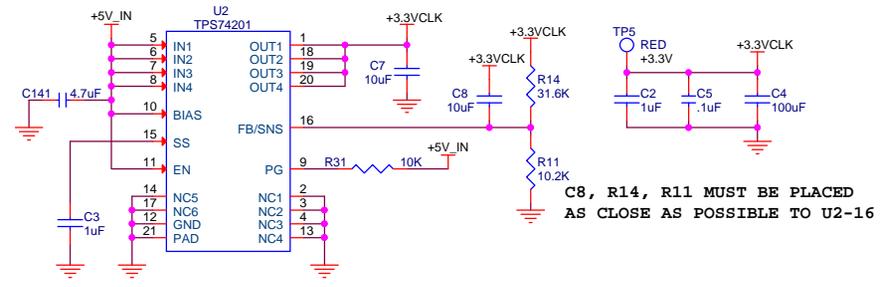
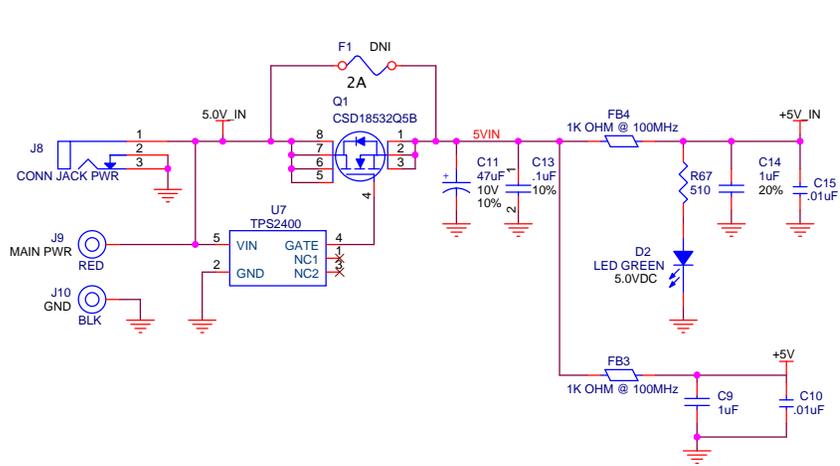


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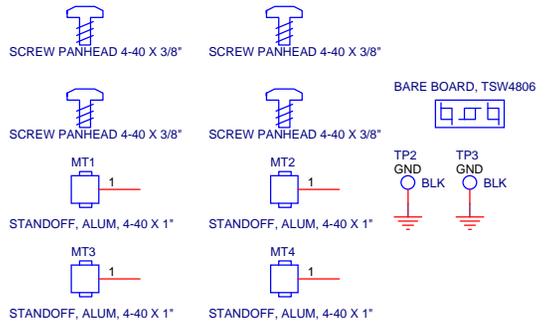
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