


AR1XXEVM-012

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REV	REVISIONS
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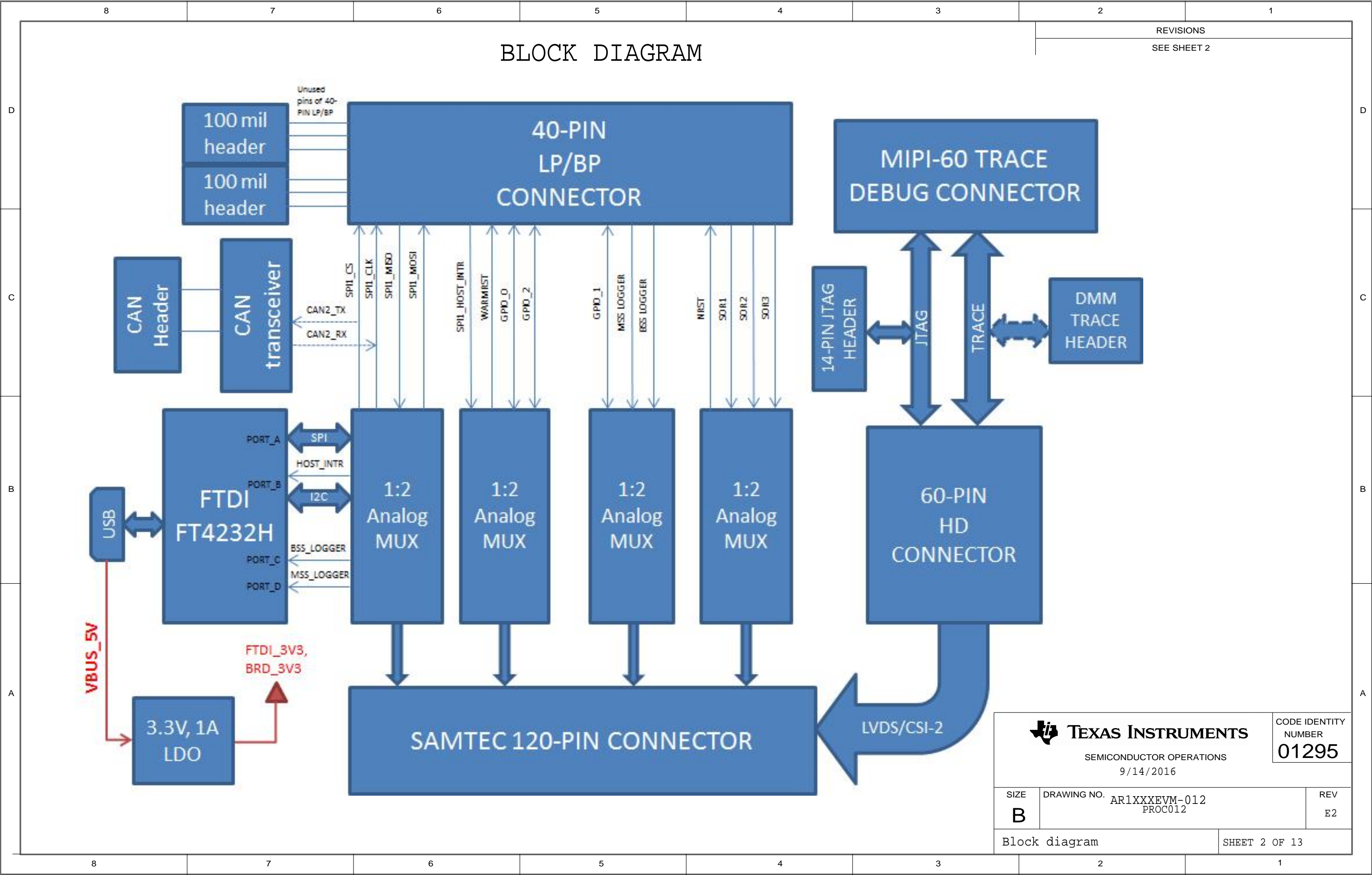
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CHECKER: CHETHAN	DATE 9/14/2016	TITLE: <div>AR1XXXEVM-012</div>				
ENGINEER: VIVEK	DATE 9/14/2016					
APPROVED: ANIL	DATE 9/14/2016					
RELEASED: 9/14/2016	DATE 9/14/2016	SCALE N	SIZE B	PROC012	REV E2	SHEET 1 OF 13

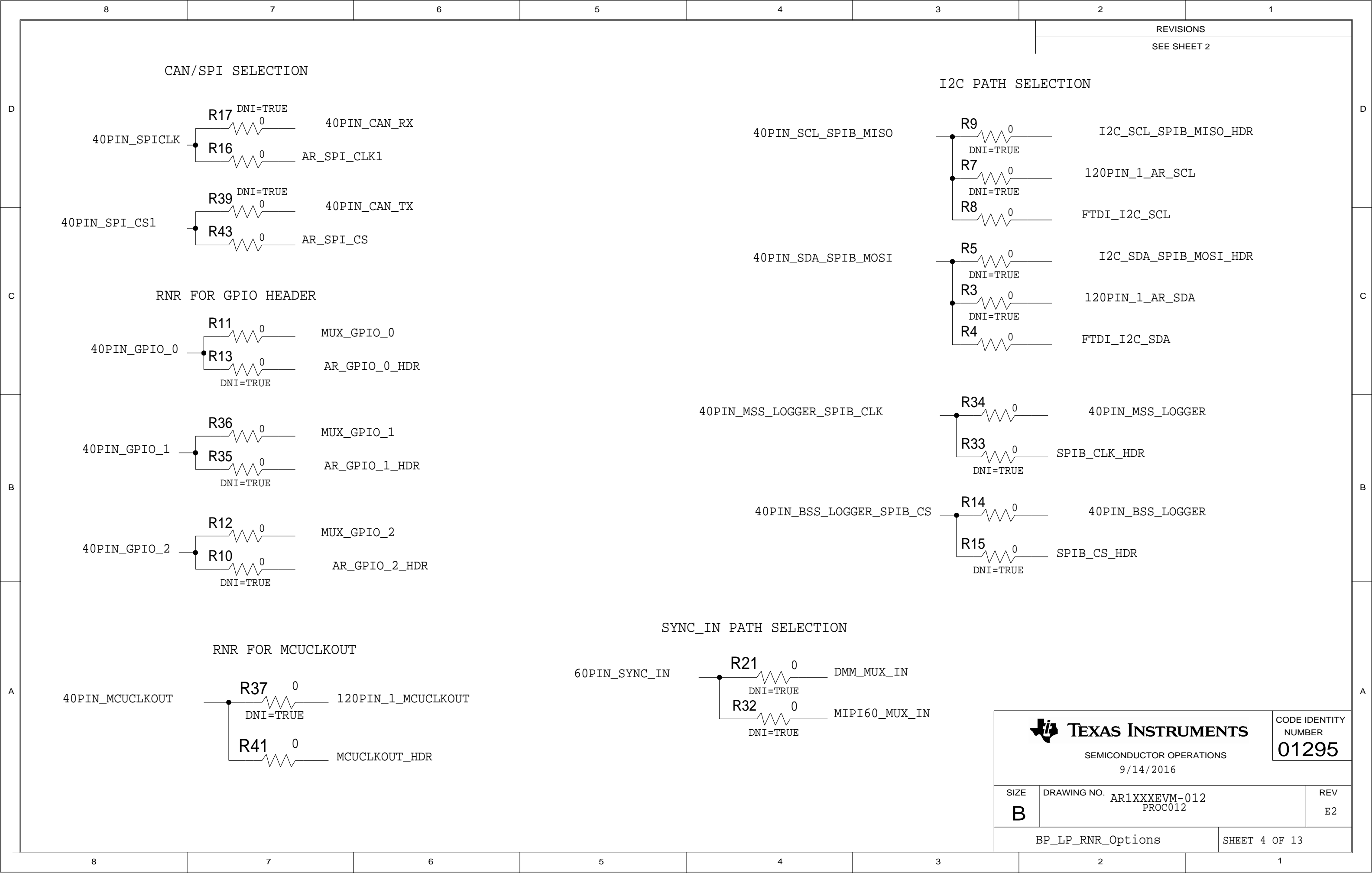
TEXAS INSTRUMENTS AND/OR ITS LICENSORS DO NOT WARRANT THE ACCURACY OR COMPLETENESS OF THIS SPECIFICATION OR ANY INFORMATION CONTAINED THEREIN. TEXAS INSTRUMENTS AND/OR ITS LICENSORS

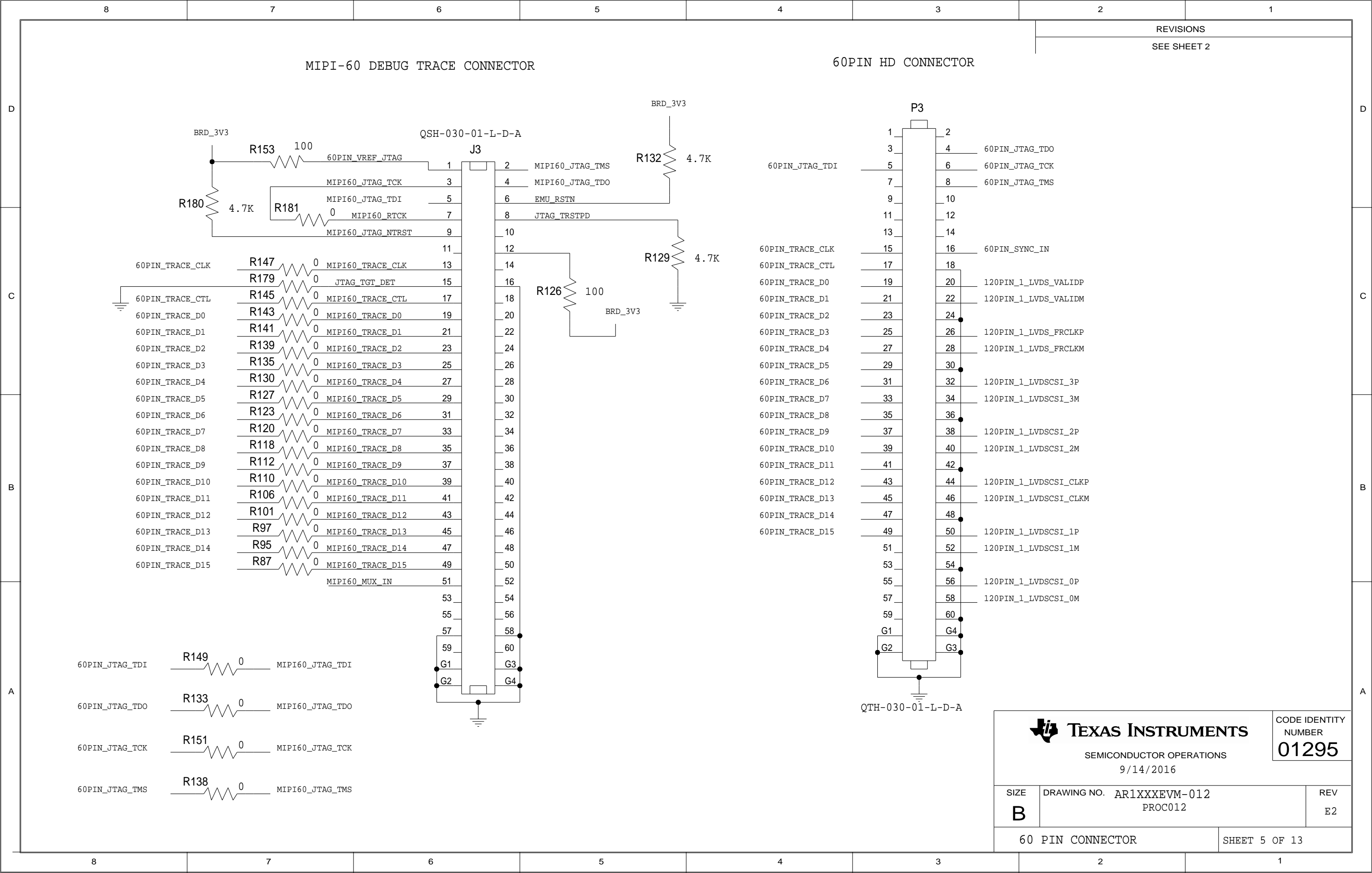
TEXAS INSTRUMENTS AND/OR ITS LICENSORS DO NOT WARRANT THAT THIS DESIGN MEET THE SPECIFICATIONS, WILL BE SUITABLE FOR YOUR APPLICATION OR FIT FOR ANY PARTICULAR PURPOSE, OR WILL OPERATE

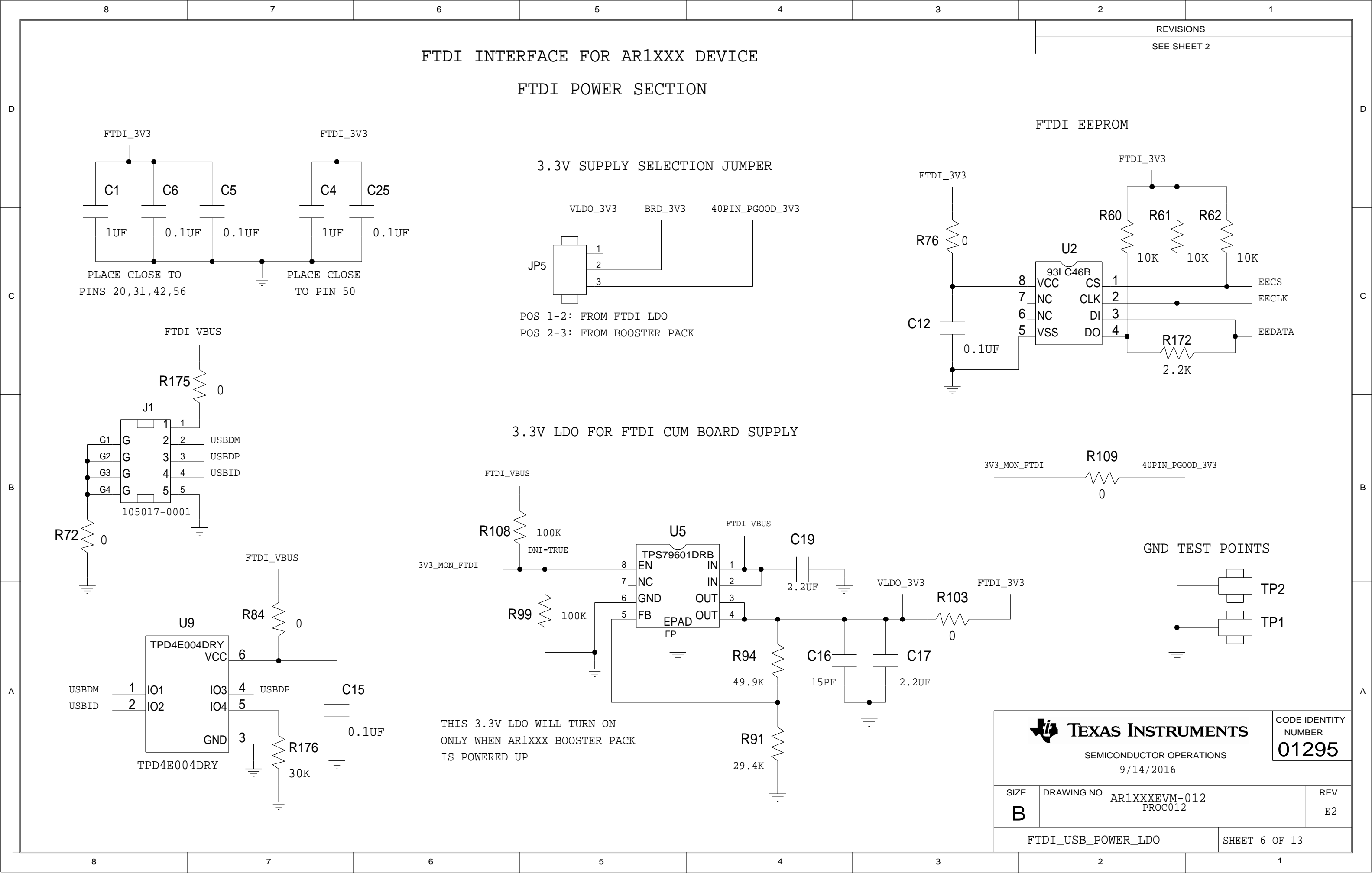
IN AN IMPLEMENTATION. TEXAS INSTRUMENTS AND/OR ITS LICENSORS DO NOT WARRANT THAT THE DESIGN IS PRODUCTION WORTHY. YOU SHOULD COMPLETELY VALIDATE AND TEST YOUR DESIGN IMPLEMENTATION TO

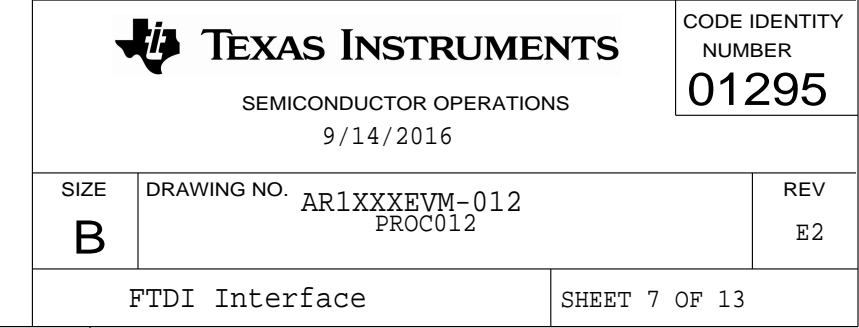
CONFIRM THE SYSTEM FUNCTIONALITY FOR YOUR APPLICATION.

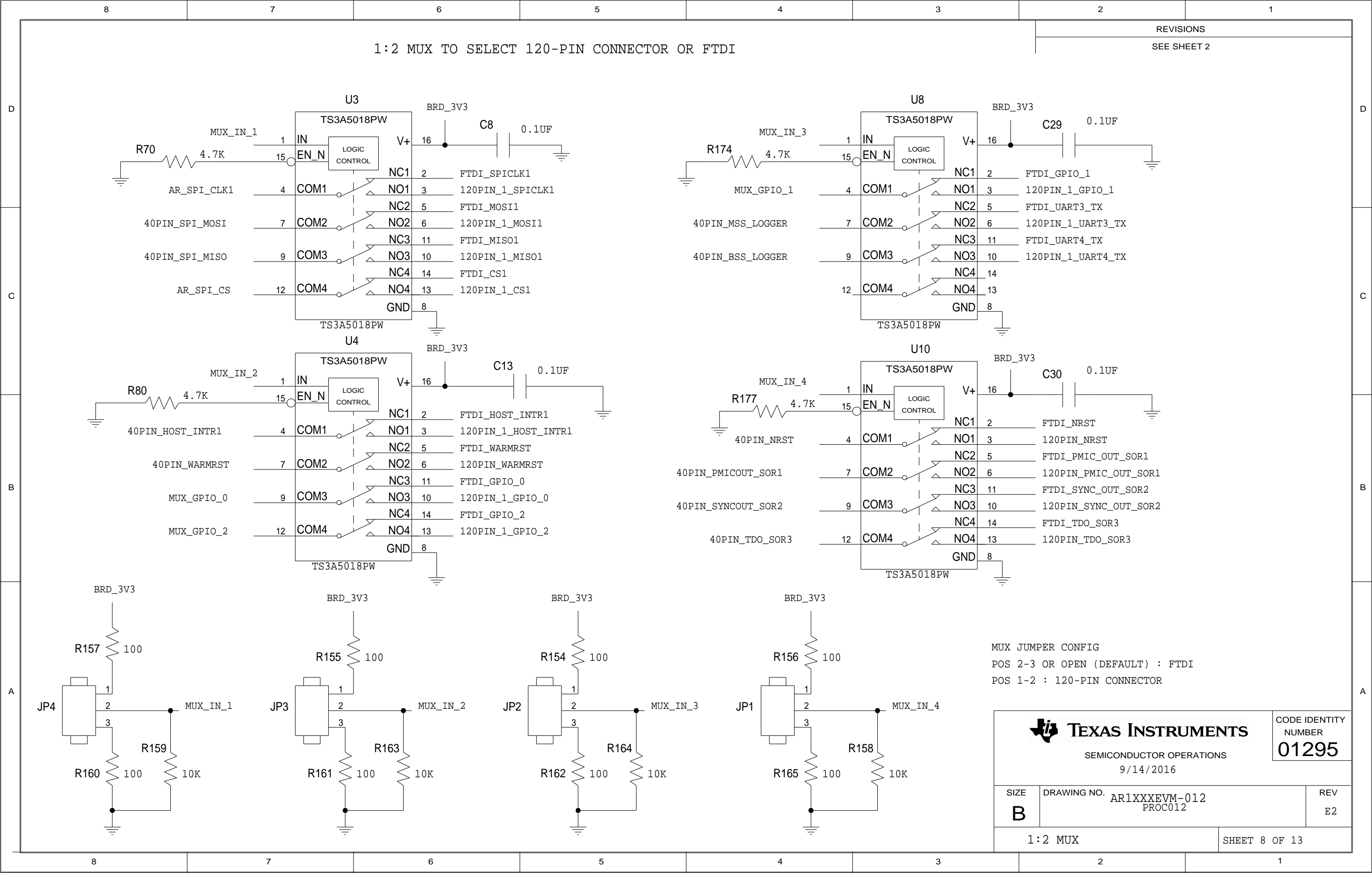








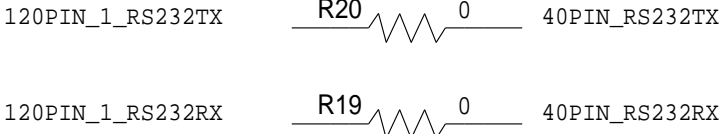
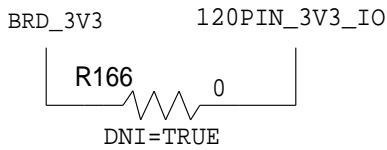
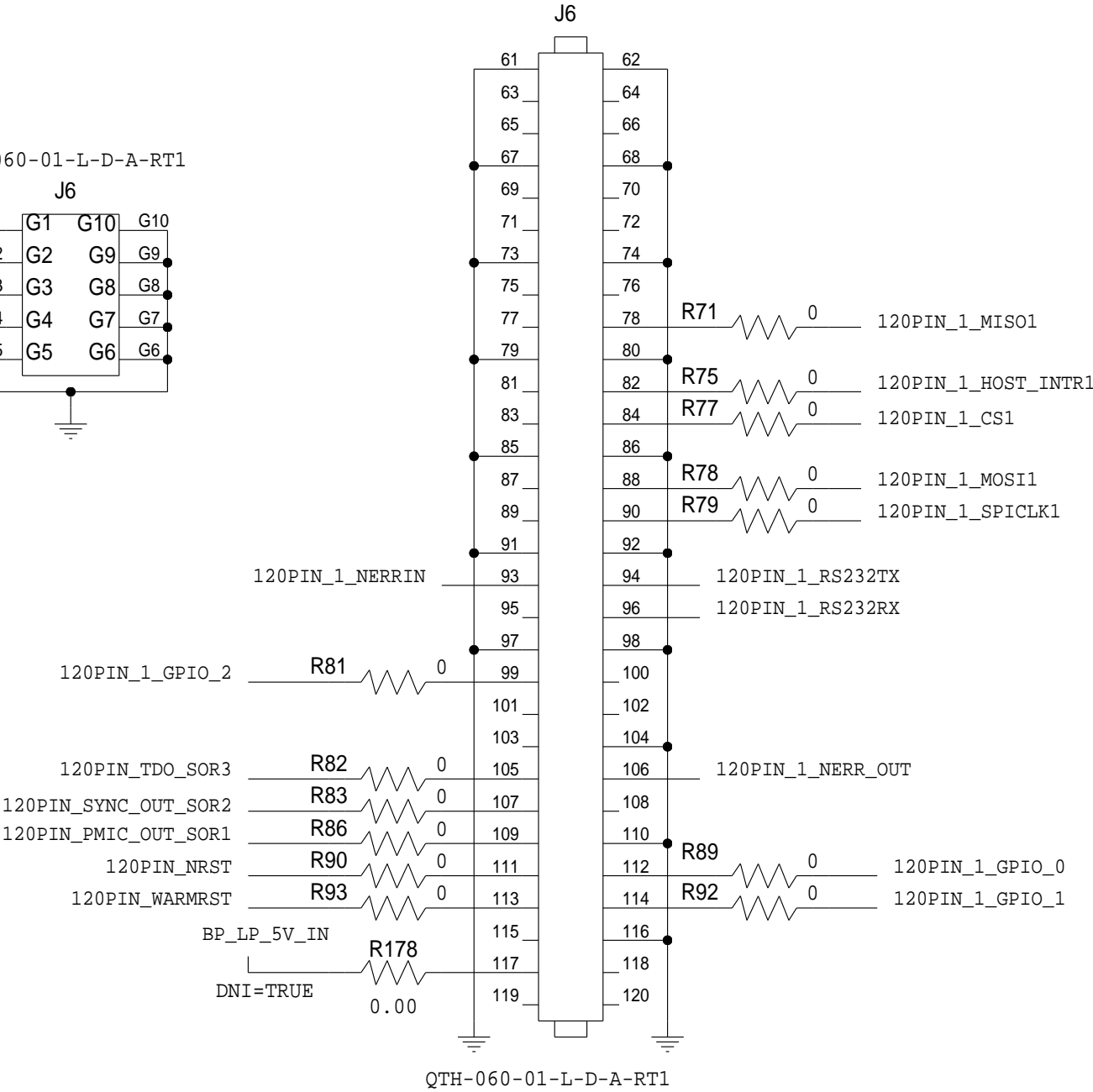
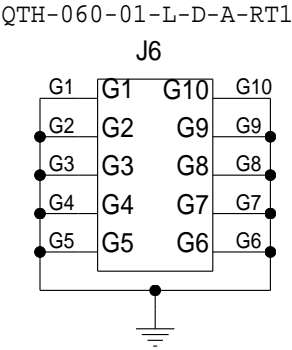
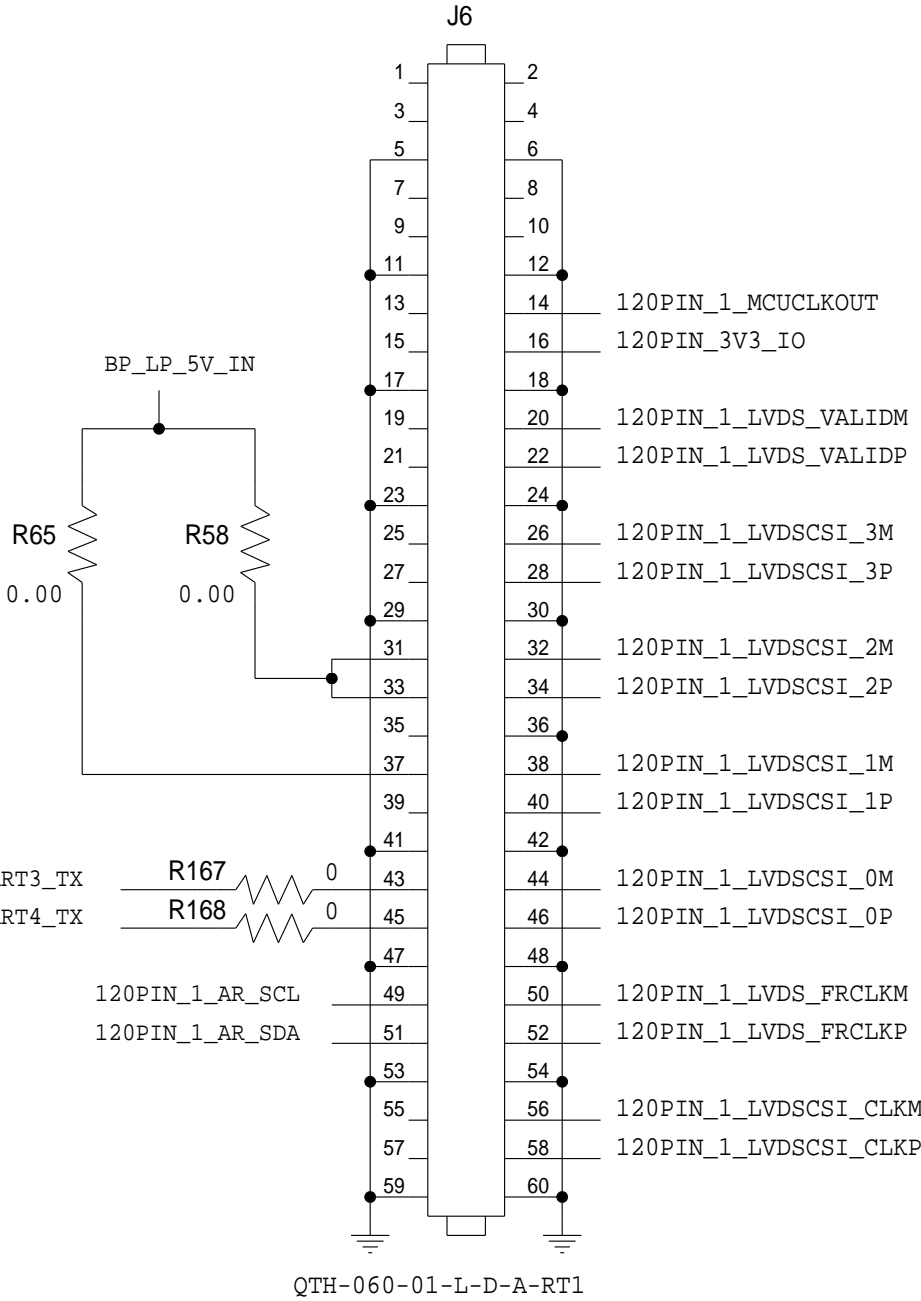




120 PIN EXTERNAL HOST PROCESSOR/TSW1400 CONNECTOR

REVISIONS

SEE SHEET 2



TEXAS INSTRUMENTS

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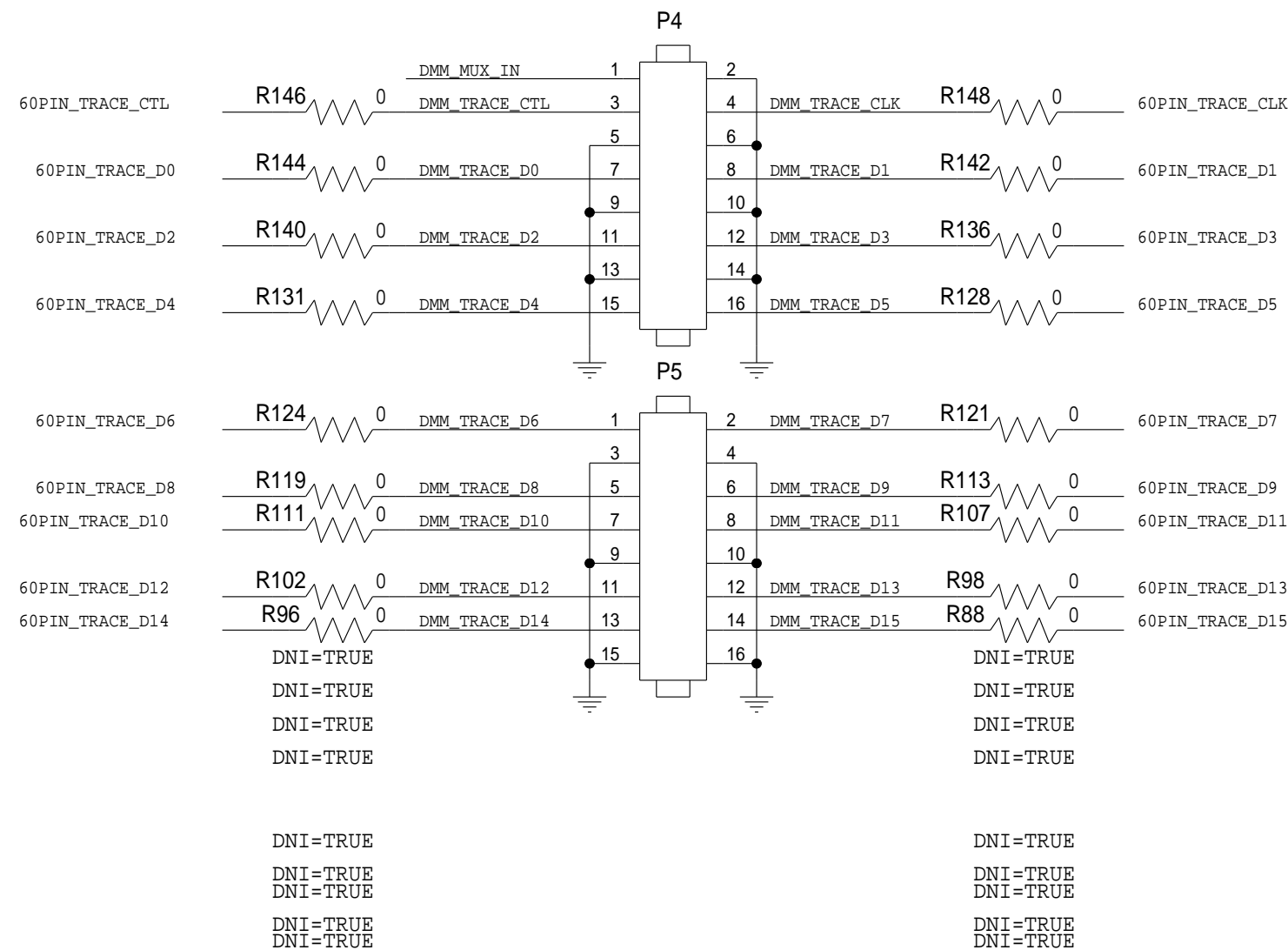
120 Pin connector

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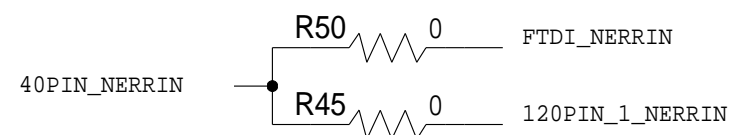
REVISIONS

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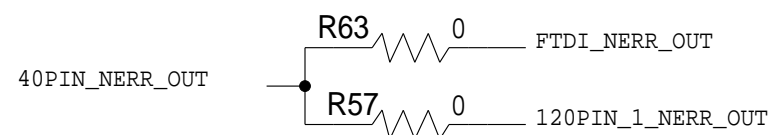
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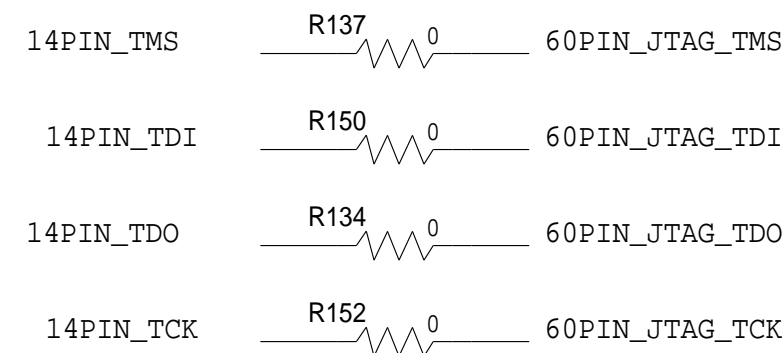
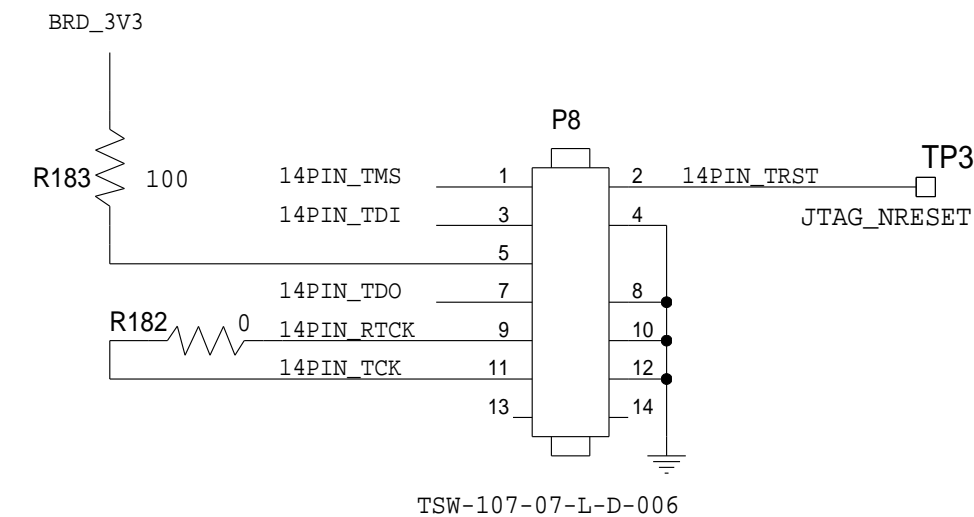
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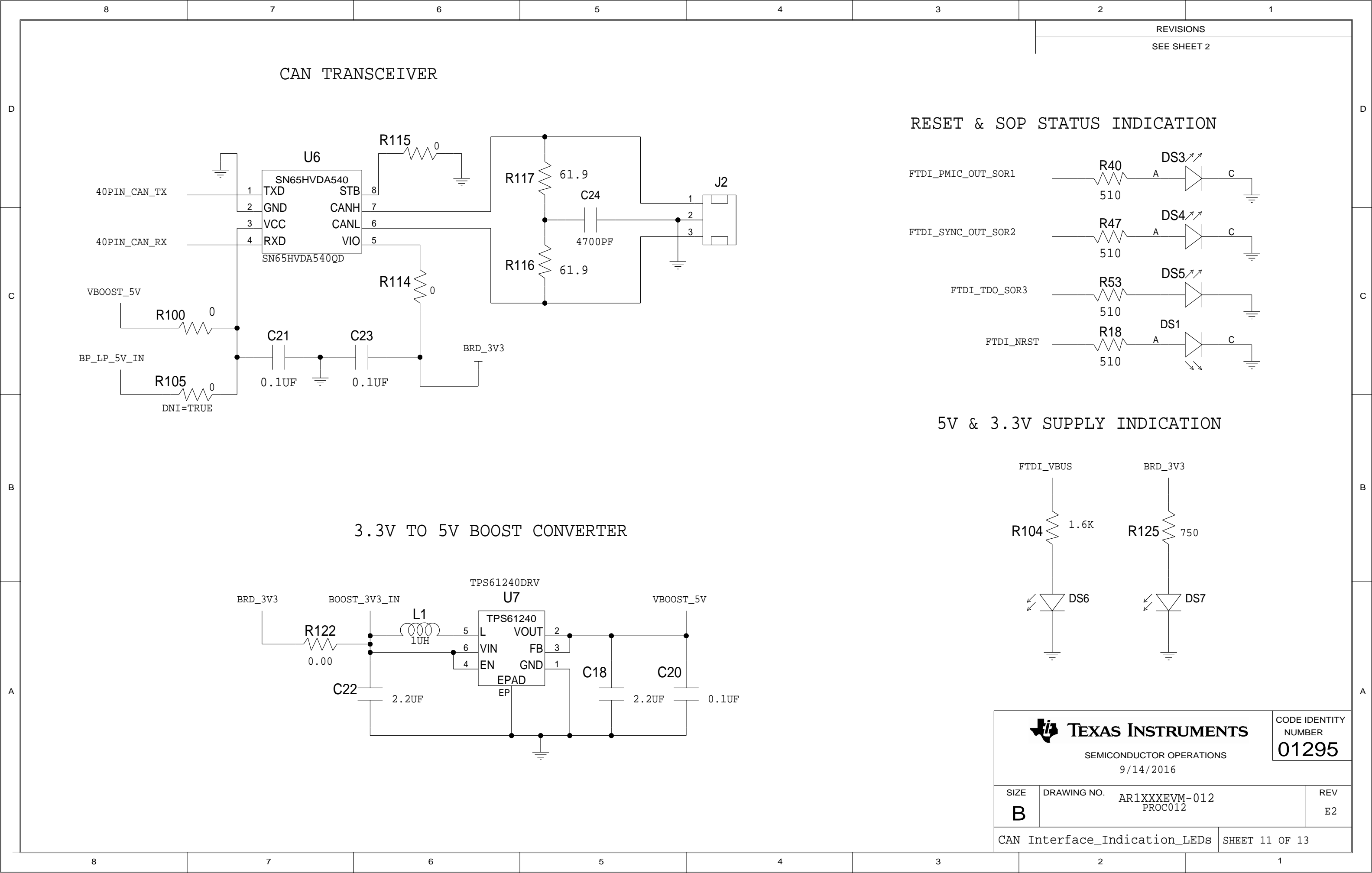
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14-PIN JTAG DEBUG CONNECTOR



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6

5

4

3

2

1

REVISIONS

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○ FID1

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○ FID3

○ FID6

PCB LOGO

PCB LOGO

PCB LOGO

PCB LOGO

TEXAS INSTRUMENTS

FCC DISCLAIMER

PB-FREE SYMBOL

CAUTION HOT SURFACE
SYMBOL

ASSEMBLY NOTES:

ASSEMBLY MUST BE CLEAN AND FREE FROM FLUX AND ALL CONTAMINANTS.
USE OF NO CLEAN FLUX IS NOT ACCEPTABLE

ASSEMBLIES MUST COMPLY WITH WORKMANSHIP STANDARDS IPC-A-610 CLASS 2,
UNLESS OTHERWISE SPECIFIED

COMPONENTS MARKED "DNI=TRUE" WILL NOT BE ASSEMBLED



TEXAS INSTRUMENTS

SEMICONDUCTOR OPERATIONS
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01295

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B

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-------------	-------------------------

REV

E2

Hardware

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8

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4

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2

↑

8		7		6		5		4		3		2		1	
												REVISIONS			
												SEE SHEET 2			
REVISION HISTORY															
CHANGES IN REV-E2															
(1) ISSUE WITH ORIENTATION OF CONNECTORS P1 & P2 HAVE BEEN FIXED IN BOARD LAYOUT															
(2) SILKSCREEN MARKINGS 'FTDI' AND 120-PIN CONNECTOR FOR MUX SELECTOR JUMPERS JP1-JP4 HAVE BEEN CORRECTED															
(3) SYNC_IN SIGNAL PATH CHANGED FROM "J4.9 --> U1.21, P6.9" TO "P3.16 --> P4.1, J3.51"															

 TEXAS INSTRUMENTS SEMICONDUCTOR OPERATIONS 9/14/2016		CODE IDENTITY NUMBER 01295	
		SIZE B	DRAWING NO. AR1XXEVM-012 PROC012
REVISION HISTORY		REV E2	
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