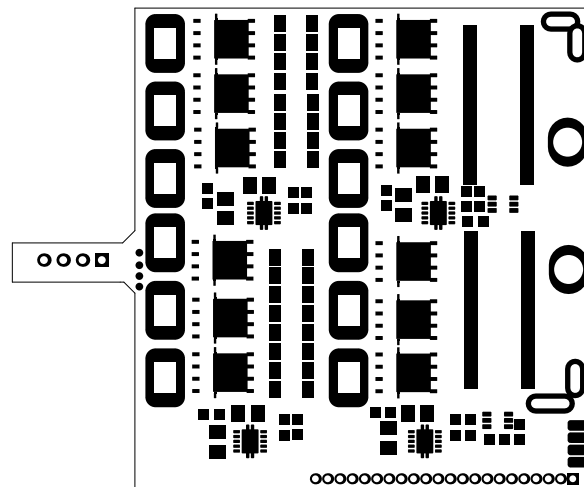
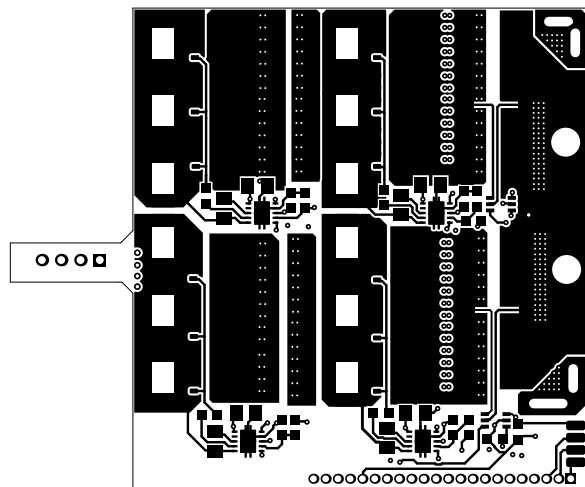


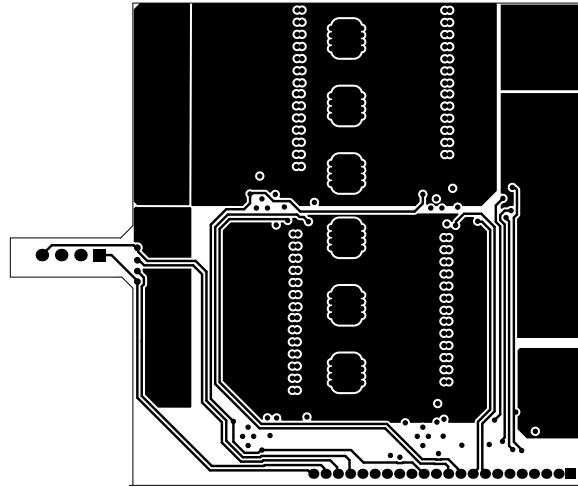
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41017	REV: E1	SUN REV: Not in version control
LAYER NAME = Top Overlay	TID #: N/A		
PLOT NAME = Top Overlay	GENERATED : 5/23/2023 4:14:46 PM		TEXAS INSTRUMENTS



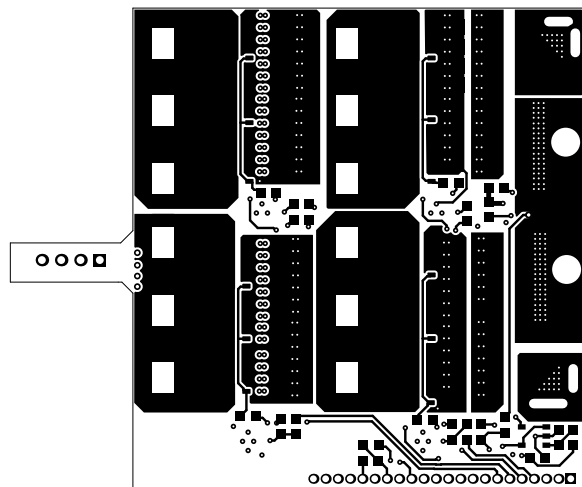
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41017	REV: E1	SVN REV: Not in version control
LAYER NAME = Top Solder	TID #: N/A		
PLOT NAME = Top Solder Mask	GENERATED : 5/23/2023 4:14:46 PM		TEXAS INSTRUMENTS



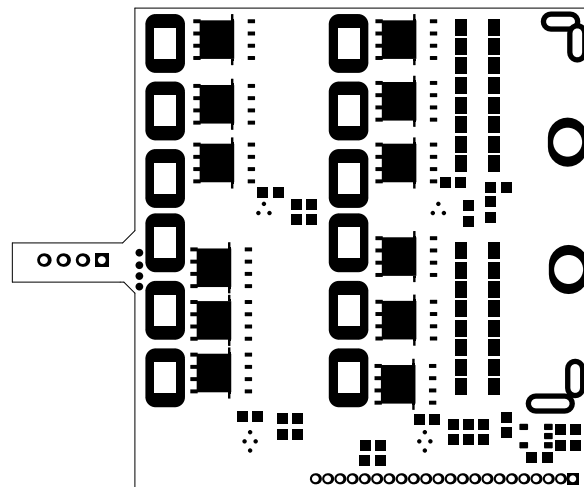
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41017	REV: E1	SVN REV: Not in version control
LAYER NAME = Top Layer	TID #: N/A		
PLOT NAME = Top Layer	GENERATED : 5/23/2023 4:14:47 PM		TEXAS INSTRUMENTS



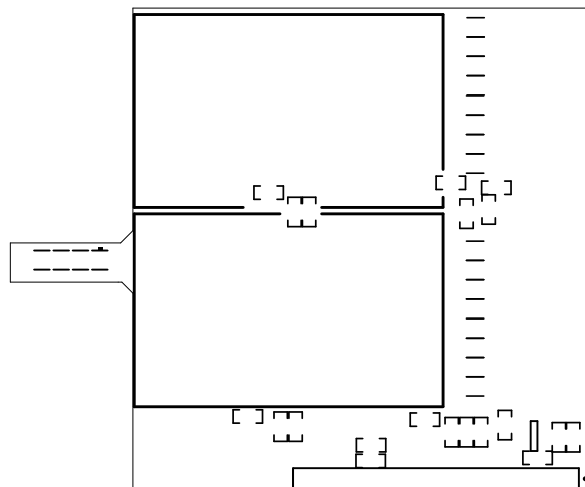
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41017	REV: E1	SVN REV: Not in version control
LAYER NAME =	TID #: N/A		
PLOT NAME = Signal 1	GENERATED : 5/23/2023 4:14:47 PM		TEXAS INSTRUMENTS



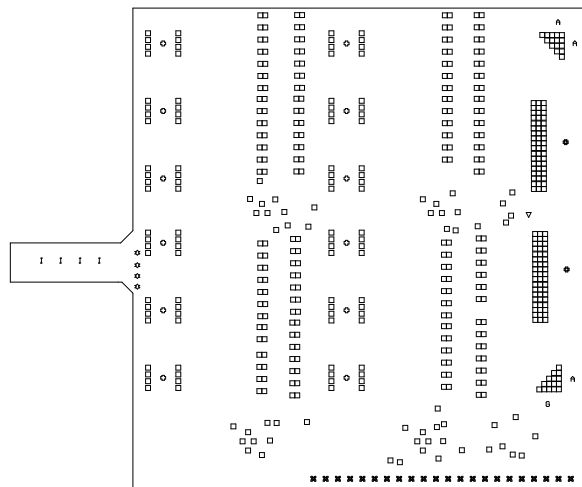
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41017	REV: E1	SUN REV: Not in version control
LAYER NAME = Bottom Layer	TID #: N/A		
PLOT NAME = Bottom Layer	GENERATED : 5/23/2023 4:14:48 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41017	REV: E1	SUN REV: Not in version control
LAYER NAME = Bottom Solder	TID #: N/A		
PLOT NAME = Bottom Solder Mask	GENERATED : 5/23/2023 4:14:48 PM		TEXAS INSTRUMENTS



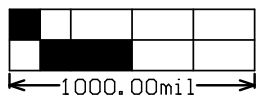
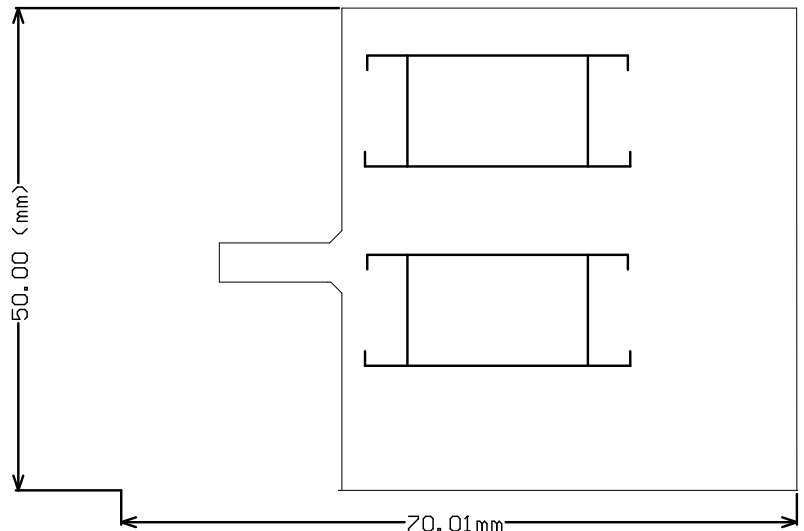
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41017	REV: E1	SUN REV: Not in version control
LAYER NAME = Bottom Overlay	TID #: N/A		
PLOT NAME = Bottom Overlay	GENERATED : 5/23/2023 4:14:48 PM		TEXAS INSTRUMENTS



Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
□	504	8.00mil (0.203mm)	PTH	Round	Top Layer - Bottom Layer	+/-1.97mil
⚙	4	13.78mil (0.350mm)	PTH	Round	Top Layer - Bottom Layer	
▽	1	14.00mil (0.356mm)	PTH	Round	Top Layer - Bottom Layer	
✖	22	27.56mil (0.700mm)	PTH	Round	Top Layer - Bottom Layer	
I	4	31.89mil (0.810mm)	PTH	Round	Top Layer - Bottom Layer	
⊕	2	118.11mil (3.000mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	12	86.61mil (2.200mm)	PTH	Rectangle	Top Layer - Bottom Layer	
A	3	39.37mil (1.000mm)	PTH	Slot	Top Layer - Bottom Layer	
G	1	39.37mil (1.000mm)	PTH	Slot	Top Layer - Bottom Layer	
553 Total						

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41017	REV: E1	SUN REV: Not in version control
LAYER NAME = Drill Drawing	TID #: N/A		
PLOT NAME = Drill Drawing	GENERATED : 5/23/2023 4:14:49 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41017	REV: E1	SVN REV: Not in version control
LAYER NAME = M2 Board Dimensions	TID #: N/A		
PLOT NAME = Board Dimensions	GENERATED : 5/23/2023 4:14:50 PM		TEXAS INSTRUMENTS