

A

B

C

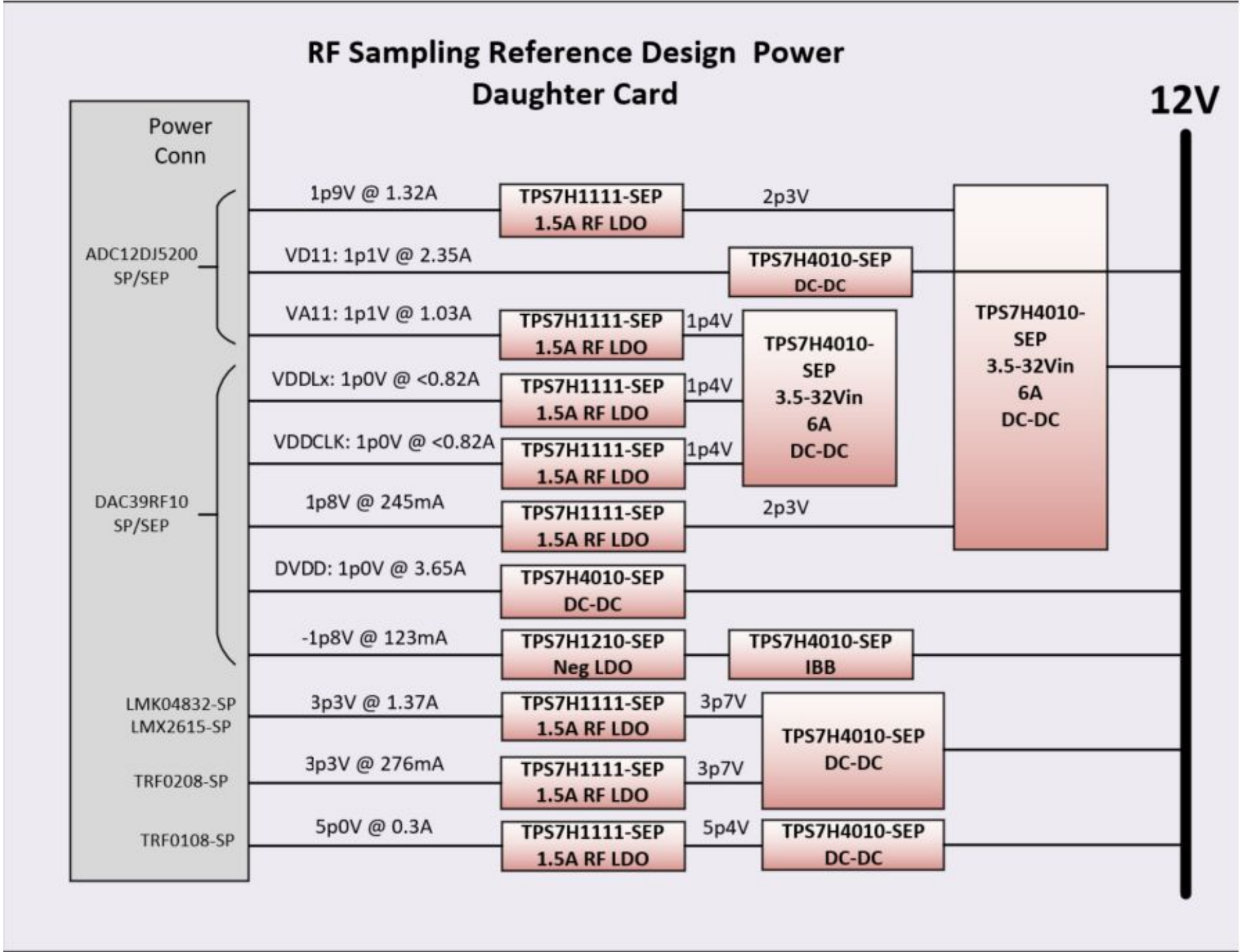
D

A

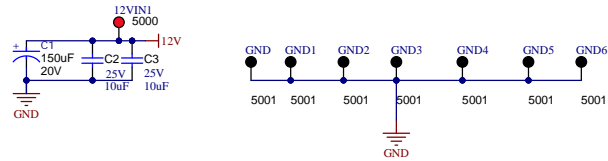
B

C

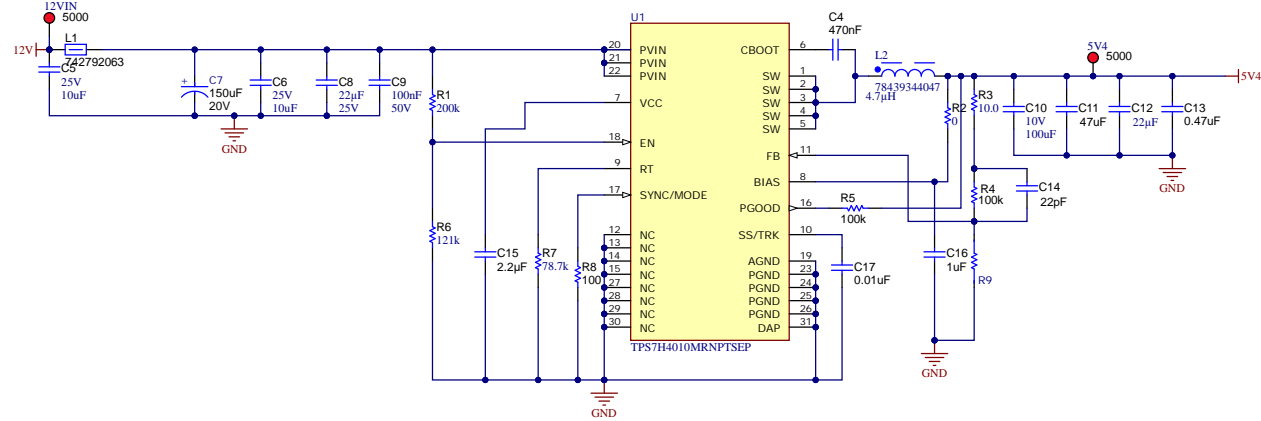
D



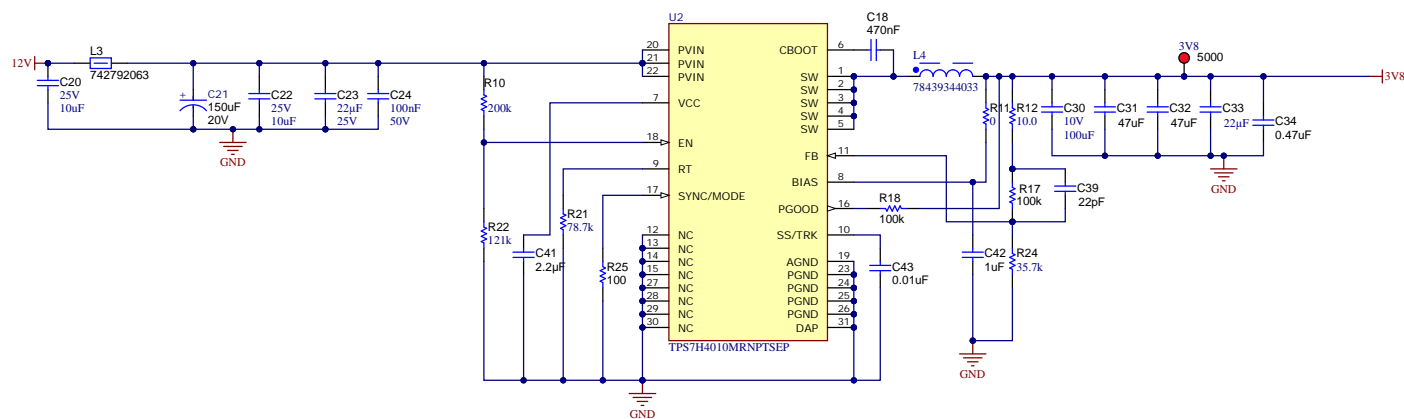
- Tier 1 Sensitivity: DAC sensitive rail
 - Pins 2,4,6,8: 1p0V_DAC_VDDL
 - Pins 10, 12, 14,16: 1p0V_DAC_VDDCLK10
- Tier 2 Sensitivity: RF stuff
 - Pins 29, 31: 5p0V_RF
 - Pins 9, 11: 3.3V_RF
 - Pins 1,3,5,7: 3.3V_LMKX
 - Pins 37,39: N1p8V_DAC_VEEA
- Tier 3 Sensitivity: Analog Stuff
 - Pins 33, 35, 37,39: ADC_1p1V_ANA
 - Pins 26, 28, 30, 32: ADC_1p9V_ANA
 - Pins 26, 28,30, 32: DAC_1p8V
- Tier 4: Digital Stuff / Utility
 - Pins 2, 4, 6, 8: ADC_1p1V_DIG
 - Pins 1,3,5,7,9,11,13: DVDD_DAC (1p0V)
 - Pins 13, 15, 17,19: 3.3V_Utility
 - Pins 25, 27: 5p0V_Utility



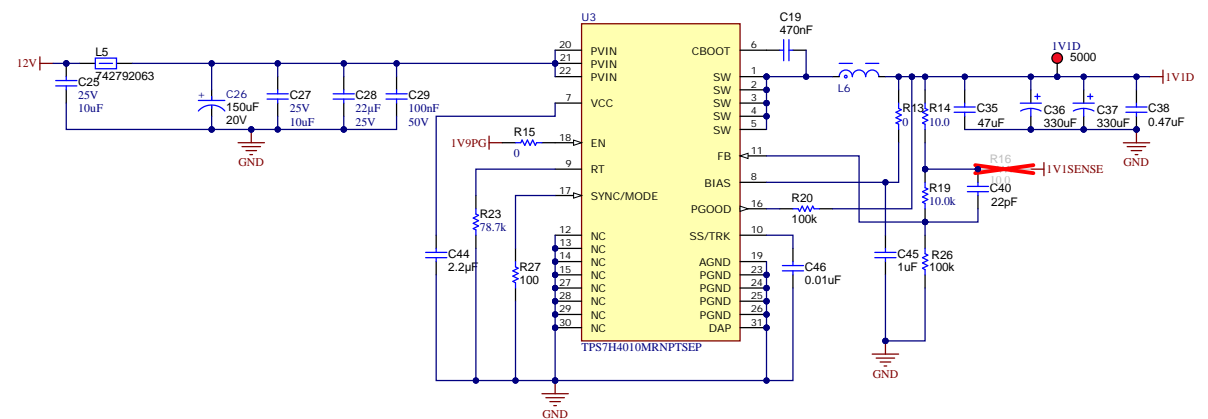
12V to 5V4 @ 1.5A



12V to 3V8 @ 1.65A



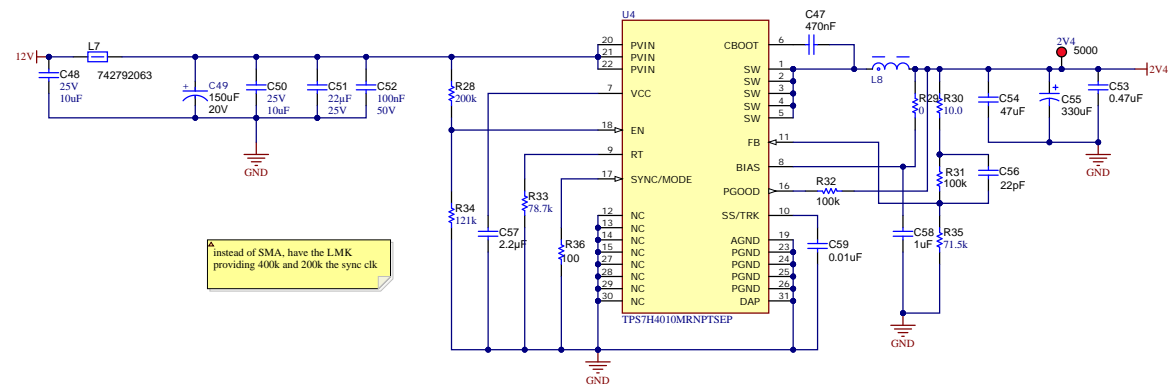
12V to 1V1 @ 2.35A



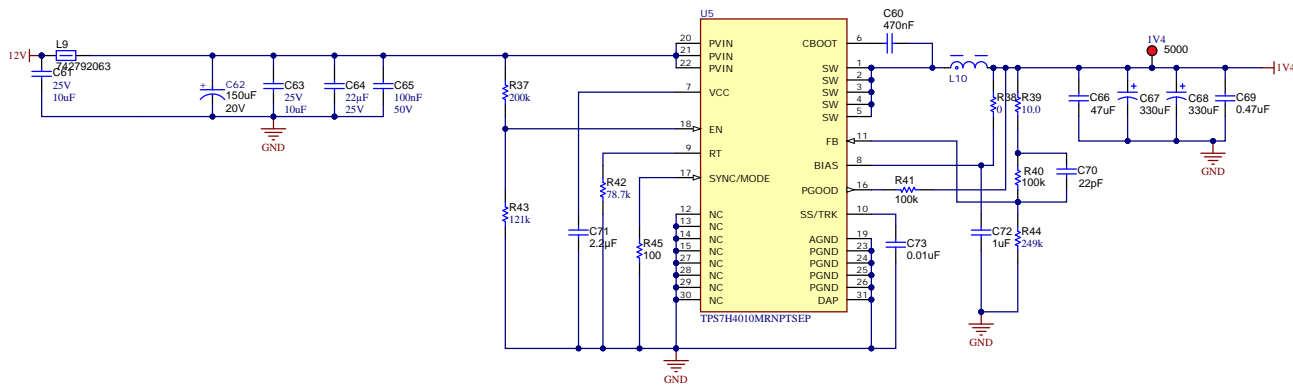
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Orderable: ChangeMe In variant	Designed for: Public Release	Mod. Date: 9/14/2023	
TID F: TIDA-010274	Project Title: Power Daughter Card - SEP		
Number: TIDA-010274 Rev: A	Sheet Title:		
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 6	
Drawn By:	File: Buck_5V4_3V8_1V1.SchDoc	Size: A2	http://www.ti.com
Engineer: RJH	Contact: http://www.ti.com/support		© Texas Instruments 2023

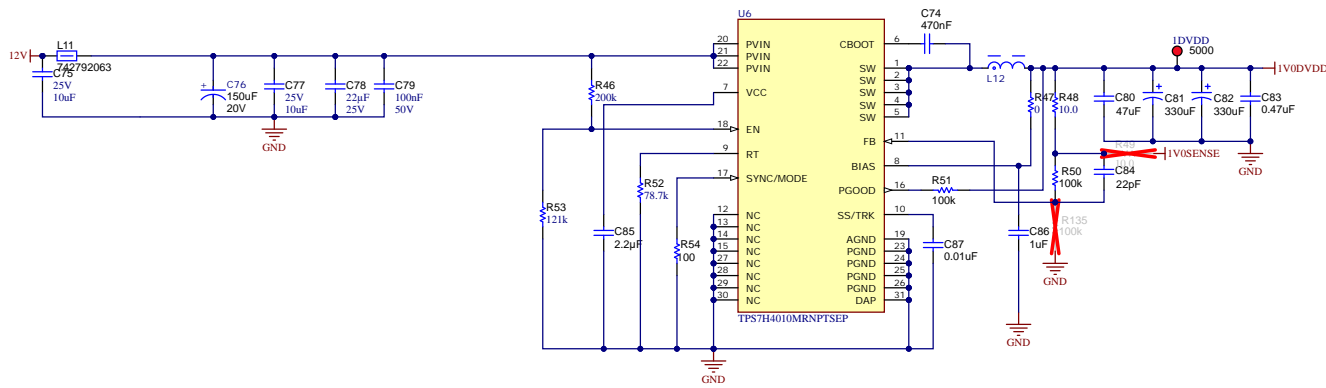
12V to 2.4V Buck @ 1.5A



12V to 1.4V @ 5A



12V to 1V0 @ 3.65A

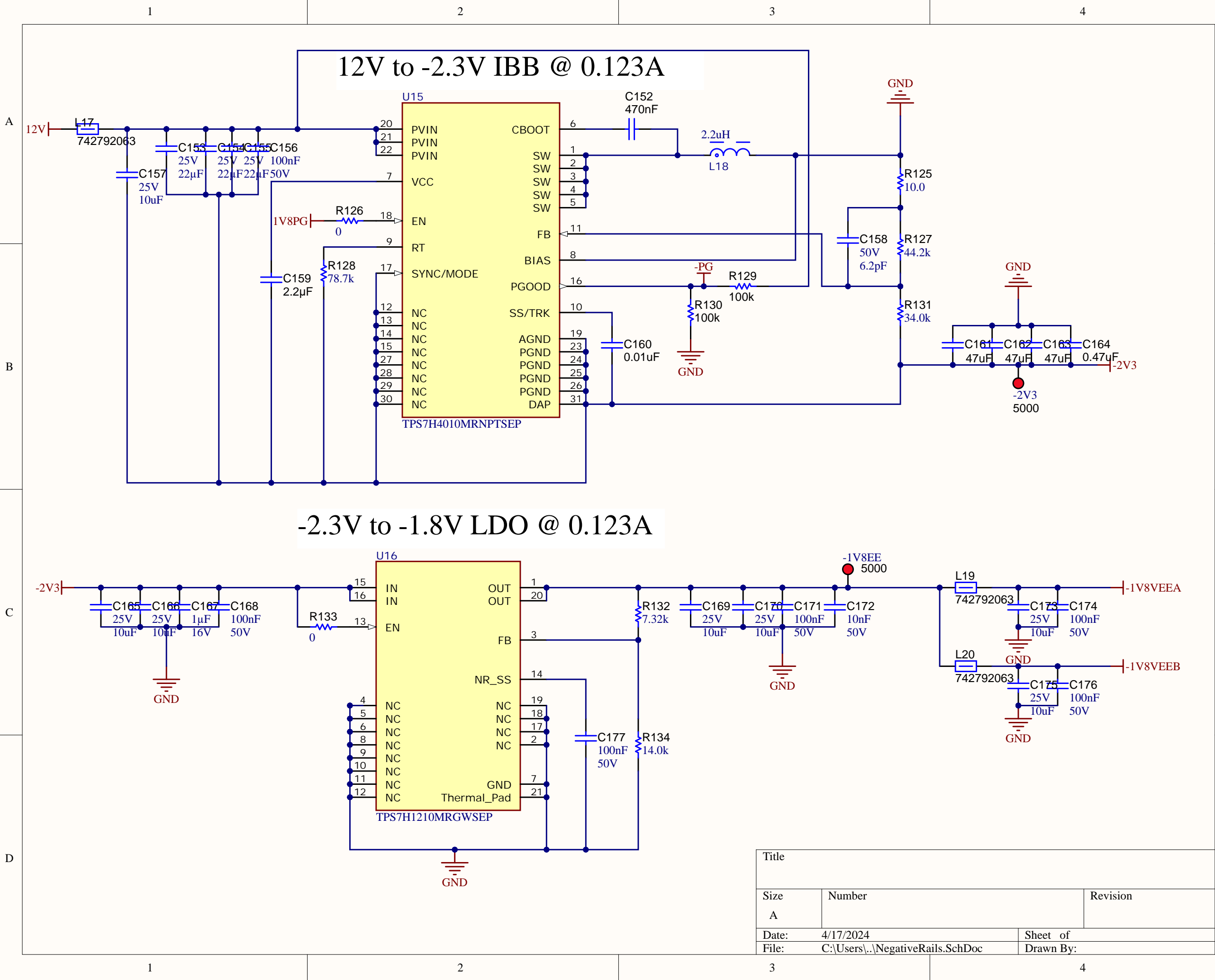


[illegible]

The schematic diagram illustrates the internal circuitry of the TPS7H111PWPSF regulator. Key components include:

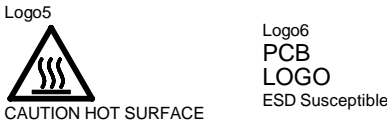
- Input/Output:** A 1V4 input and a 1V16 output (5000) are shown.
- Capacitors:** C102 (25V, 200µF), C103 (100µF, 10V), C104 (6.3V, 100µF), C105 (6.3V, 100µF), C112 (25V, 4.7µF), and C114 (4.7µF, 25V).
- Resistors:** R74 (0), R82 (11.0k), R85 (11.0k), R88 (10.0), R75 (10.0k), R76 (5.0k), R78 (5.0k), and R83 (5.0k).
- Regulator Pins:** IN, IN-, CLM, SS_SET, REF, BIAS, NC, and GND.
- Thermal Pad:** A thermal pad is connected to the GND pin.

[illegible][illegible]



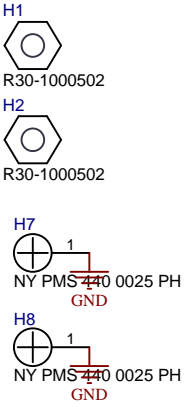
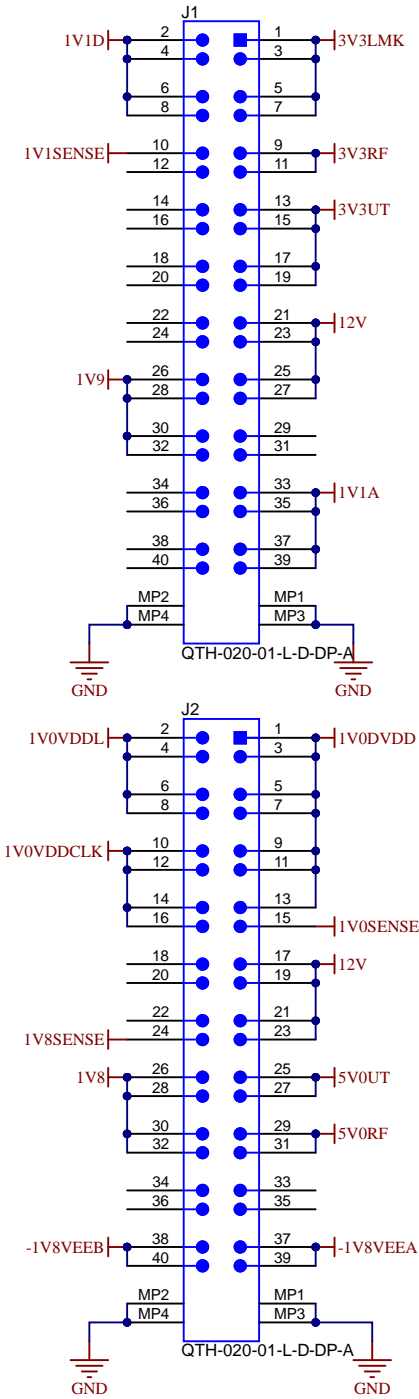


PCB Number: TIDA-010274
PCB Rev: A



LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!



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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 9/13/2023
TID #: TIDA-010274	Project Title: Power Daughter Card - SEP	
Number: TIDA-010274	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 6 of 6
Drawn By:	File: Hardware.SchDoc	Size: B
Engineer: RJH	Contact: http://www.ti.com/support	



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