

# AGENDA Tech Day with Silica

Silverstone, November 3rd 2009

	Track 1	Track 2	Track 3
8:30-9:30	Registration & Exhibits		
<b>Session 1</b> 9:30-10:45	High Speed (to 1GB/s) Dataconverters (ADC & DAC) plus Transformer vs. Op-Amp Input, Interleaving & Averaging	Industrial Isolation : The Technology, Comparisons & Products	Avoid Electrical Stress on your Op-Amps
10:45-11:15	Break & Exhibits		
<b>Session 2</b> 11:15-12:30	Processors for Computation intensive Industrial Applications	PoE Plus : The new Standard for Ethernet Power	Delta Sigma Converters & How to exploit ADC Architecture
12:30-14:00	Lunch & Exhibits		
<b>Session 3</b> 14:00-15:15	TI Stellaris M3 Cortex MCUs	Motor Control solutions with 32-bit MCUs	Powering Intel ATOM : Controllers, FETs plus the Requirements and Tools
15:15-15:45	Break & Exhibits		
<b>Session 4</b> 15:45-17:00	New MSP430 Devices for Industrial Applications	High Temperature (200C) & Hi Reliability Products for Avionics/Military/Space/Down hole Applications	Digital Power for High Density Multi-Rail point-of-load Applications

