Tech Day Irvine, CA – March 19, 2009

Time	Session	Power Management	Signal Chain	Signal Chain 2	Embedded Processing
8 to 9 a.m.	**Registration**				
9 to 10 a.m.	1	Component Selection, Layout and Thermal Design Consideration for DC/DC Converters	Op-Amp Stability Analysis and Fixes	Circuit Isolation Techniques and Implementations	Essential Concepts in SoC System Design with OMAP™ and DaVinci™
10 to 10:15 a.m.	**Break**				
10:15 to 11:15 a.m.	2	UCD92xx Digital Controller Solutions – You Don't Have To Write Code	Designing with High-Speed DACs and Solving the Analog Interface	Getting Started with 900-MHz and 2.4-GHz Products and Protocols	Exploring Windows Embedded CE 6.0
11:15 to 11:30 a.m.	**Break**				
11:30 a.m. to 12:30 p.m.	3	PWM and Light-Load PFM Mode Operation of DC-DC Converters	Designing Mixed Signal Systems with Noise Reduction Techniques in Mind	Practical Techniques for Thermal Management in Portable Products	Video Fundamentals and Future Codec Directions
12:30 to 1:30 p.m.	**Lunch**				
1:30 to 2:30 p.m.	4	Design Tool for DC-DC Converter Design	Tools for Evaluating Precision Analog-to-Digital Converters	High-Speed Op-Amp Design Considerations	Leveraging Ultra-Low-Power Best Practices
2:30 to 2:45 p.m.	**Break**				
2:45 to 3:45 p.m.	5	Rechargeable Batteries and Their Optimized Chargers	Tackling EMI and RFI at the Board and System Level	Circuit Sensitivity with Emphasis on Analog Filters	Introduction to Code Composer Studio™ v4.0
3:45 to 4:45 p.m.	**Reception**				

Texas Instruments