



Technology Day Kansas City – December 3, 2009

Time	Session	Portable Design Considerations	Low-Power Wireless	Signal-Chain Solutions
8 to 9 a.m.	Registration			
9 to 10 a.m.	1	Introduction to DC/DC Converter Topologies for Portable Applications	Low-Power RF Basics and Modulation Techniques	Op Amp Stability and Fixes
10 to 10:30 a.m.	Break			
10:30 to 11:30 a.m.	2	Stability, Transient Response and Noise of Portable DC/DC Converters	Adapting TI LPW Reference Designs	Circuit Isolation Techniques and Implementations
11:30 a.m. to 12:30 p.m.	Lunch/ Keynote/Exhibits			
12:30 to 1:30 p.m.	3	Li-Ion Battery Characteristics, Trends and Fuel Gauge, and Cell Balance in Multicell Battery Packs	Improving the Range of Your LPW Design	Sensors and the Analog Interface
1:30 to 1:45 p.m.	Break			
1:45 to 2:45 p.m.	4	Designing Power Drivers for Solid State Lighting (LED's)	Energy Harvesting <i>Presented by Cymbet</i>	Circuit Sensitivity with Emphasis on Analog Filters
2:45 to 3 p.m.	Break			
3 to 4 p.m.	5	Choose Your Weapon—Selecting an Optimal MOSFET	Antennae Fundamentals for Low-Power Wireless Designs	Approaches to Multi-Channel, High-Resolution Data Acquisition