



Tech Day San Jose, CA – April 1 & 2, 2009

Wednesday April 1

Time	Session	Low-Power Wireless	Interface & Clocks	Signal Chain 1	Signal Chain 2	Power Management	Embedded Processors Workshop	High-Performance Processors	Low-Power Processors and Microcontrollers
8 to 9 a.m.		Registration							
9 to 10 a.m.	1	Getting Started with the TI Low-Power RF Portfolio	Circuit Isolation Techniques and Implementations	High-Performance Differential ADC Input Interface Design	Designing Mixed-Signal Systems with Noise Reduction Techniques in Mind	The Magic of Multiphase – A Comparison of a 2-Phase DC/DC Power Supply versus a Similar Single-Phase Controller Highlighting the Trade-Offs and Challenges of Each Design	Understanding 32-Bit MCU Peripherals Advanced Capability in Embedded Systems using the Piccolo™ MCU ControlSTICK (Part 1)	Getting to Faster Performance with Serial Rapid IO™	Breaking the mW/MHz Mindset: How to Navigate TI's Processor Portfolio
10 to 10:30 a.m.		Break							
10:30 to 11:30 a.m.	2	Fundamentals of Antenna Design <i>by LS Research</i>	Interfacing Between LVPECL, VML, CML and LVDS Levels	Clocking to Maximize High-Speed Signal-Chain Performance	Optimizing Your SAR-ADC Design	Battery Fundamentals	Understanding 32-Bit MCU Peripherals Advanced Capability in Embedded Systems using the Piccolo MCU ControlSTICK (Part 2)	Understanding Virtualization for TI's C64x™ Devices	Understanding the Elements of On-Chip Power
11:30 a.m. to 1:30 p.m.		Lunch							
1:30 to 2:30 p.m.	3	RF Software System Design	Advantage and Flexibility of Programmable Clock Drivers	Designing with High-Speed DACs and Solving the Analog Interface	Optimize Your Delta Sigma – Mux System with Low-Latency Strategies	Power-Supply Layout Considerations	Hands-On OMAP™-L1x Boot-Camp (Part 1)	Power and Thermal Considerations for TI's DM6467	CC430: MCUs for Space-Constrained, Ultra-Low-Power, Wireless Applications
2:30 to 2:45 p.m.		Break							
2:45 to 3:45 p.m.	4	RF4CE and the RF-Based Remote Control Market	Networking Clock Solutions – LAN/WAN Clocking Techniques	High-Speed Amplifier Design Considerations	Advanced TINA SPICE Simulation	Digital-Power UCD Buck Converter	Hands-On OMAP-L1x Boot-Camp (Part 2)	Tutorial on the New Code Composer Studio™ v4.0 and Software Libraries – Get Coding in Record Time	Integrated USB Connectivity with MSP430 MCUs
3:45 to 4 p.m.		Break							
4 to 5 p.m.	5	Techniques for a Compliant RF Design <i>by LS Research</i>	Tackling EMI and RFI at the Board and System Level	High-Speed Data Converter Tools	Evaluating Analog-to-Digital Converters with ADCPro™	Meeting Battery Power Management Design Challenges	Hands-On OMAP-L1x Boot-Camp (Part 3)	Tips and Tricks for Increasing Performance with TI's Multi-Core Embedded Processors	Implementing Digital Motor Control with High-Performance Low-Cost MCUs
5 to 6:30 p.m.		Reception							

Exhibit Hall Hours



Tech Day San Jose, CA – April 1 & 2, 2009

Thursday April 2

Time	Session	Low-Power Wireless	Interface & Clocks	Signal Chain 1	Signal Chain 2	Power Management	Embedded Processors Workshops	Video Processors	Application Processors and Microcontrollers
8 to 9 a.m.		Registration							
9 to 10 a.m.	1	Getting Started with the TI Low-Power RF Portfolio	Circuit Isolation Techniques and Implementations	High-Performance Differential ADC Input Interface Design	Designing Mixed-Signal Systems with Noise Reduction Techniques in Mind	The Magic of Multiphase – A Comparison of a 2-Phase DC/DC Power Supply versus a Similar Single-Phase Controller Highlighting the Trade-Offs and Challenges of Each Design	MSP430F5xx Hands-On Workshop (Part 1)	The Ins and Outs of TI's Video Interfaces	10 Reasons You Should Use OMAP 3 in Your Next Design
10 to 10:30 a.m.		Break							
10:30 to 11:30 a.m.	2	Fundamentals of Antenna Design <i>by LS Research</i>	Interfacing Between LVPECL, VML, CML and LVDS Levels	Clocking to Maximize High-Speed Signal-Chain Performance	Optimizing Your SAR-ADC Design	Battery Fundamentals	MSP430F5xx Hands-On Workshop (Part 2)	Implementing High-Definition Codecs Using DM6467	High-Speed Board Design Considerations for OMAP
11:30 a.m. to 1:30 p.m.		Lunch							
1:30 to 2:30 p.m.	3	RF Software System Design	Advantage and Flexibility of Programmable Clock Drivers	Designing with High-Speed DACs and Solving the Analog Interface	Optimize Your Delta Sigma – Mux System with Low Latency Strategies	Power-Supply Layout Considerations	Hands-On OMAP Lab (Part 1)	Image Pipe (IPIPE) Overview	Implementing Smart and Flexible Power-Line Modem for AMR/AMI and Industrial Applications on TMS320F28x 32-Bit Controllers
2:30 to 2:45 p.m.		Break							
2:45 to 3:45 p.m.	4	RF4CE and the RF-Based Remote Control Market	Networking Clock Solutions – LAN/WAN Clocking Techniques	High-Speed Amplifier Design Considerations	Advanced TINA SPICE Simulation	Digital Power UCD Buck Converter	Hands-On OMAP Lab (Part 2)	HD On-Screen Display Using DM6467 and DM6437	FRAM: The Future of Embedded Memory for Microcontrollers
3:45 to 4 p.m.		Break							
4 to 5 p.m.	5	Techniques for a Compliant RF Design <i>by LS Research</i>	Tackling EMI and RFI at the Board and System Level	High-Speed Data Converter Tools	Evaluating Analog-to-Digital Converters with ADCPro™	Meeting Battery Power Management Design Challenges	Hands-On OMAP Lab (Part 3)	Multi-Channel Video Aggregation for Digital Media Products	C2000™ MCU Digital Power Solutions: AC/DC and DC/DC

Exhibit Hall Hours

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