



Tech Day Washington D.C. – June 4, 2009

Time	Session	Power	Low-Power RF	Signal Chain	Embedded Processing
8:30 to 9:30 a.m.	Registration				
9:30 to 10:30 a.m.	1	SwitcherPro™ Tool	RF Basics	High-Speed Amplifier Design Considerations	Overview of TI's OMAP™ Devices
10:30 to 10:45 a.m.	Break				
10:45 to 11:45 a.m.	2	Preventing Battery System Failures in Portable Devices <i>by MircoPower</i>	Low-Power RF Protocol Overview	Solving Common Design Issues in High-Speed Data Converters	Choosing the Right Video Processor
11:45 a.m. to 1 p.m.	Lunch				
1 to 2 p.m.	3	Buck-Boost Converters for Portable Systems	RF Hardware System Design	Compliance by Design <i>by LS Research</i>	Introduction to Stellaris® ARM Cortex™-M3 MCUs
2 to 2:15 p.m.	Break				
2:15 to 3:15 p.m.	4	Design Consideration in Selecting the Switching Frequency of Power Controllers	CC430: MCUs for Space Constrained, Ultra-Low-Power, Wireless Applications	Clocking to Maximize High-Speed Signal-Chain Performance	Getting Started with Low-Power Floating-Point Processors
3:15 to 3:30 p.m.	Break				
3:30 to 4:30 p.m.	5	UCD92xx Digital Controller Solutions – You Don't Have To Write Code	Precision Analog, High-Res ADC/DAC and Complementary OPA	The Complex IF Transmitter	Introduction to Code Composer Studio™ v4.0

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