# Analog Engineer's Circuit Non-Inverting Op Amp with Non-Inverting Positive Reference Voltage Circuit

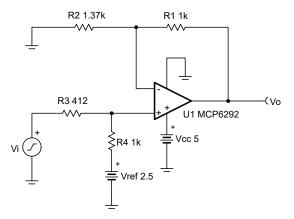


# **Design Goals**

Input		Output		Supply		
V <sub>iMin</sub>	V <sub>iMax</sub>	V <sub>oMin</sub>	V <sub>oMax</sub>	V <sub>cc</sub>	V <sub>ee</sub>	V <sub>ref</sub>
-1 V	3 V	0.05 V	4.95 V	5 V	0 V	2.5 V

#### **Design Description**

This design uses a non-inverting amplifier with a non-inverting positive reference to translate an input signal of -1 V to 3 V to an output voltage of 0.05 V to 4.95 V. This circuit can be used to translate a sensor output voltage with a positive slope and negative offset to a usable ADC input voltage range.



## **Design Notes**

- 1. Use op amp linear output operating range. Usually specified under A<sub>OL</sub> test conditions.
- 2. Check op amp input common mode voltage range.
- 3. V<sub>ref</sub> output must be low impedance.
- 4. Input impedance of the circuit is equal to the sum of  $R_3$  and  $R_4$ .
- Choose low-value resistors to use in the feedback. It is recommended to use resistor values less than 100 kΩ. Using high-value resistors can degrade the phase margin of the amplifier and introduce additional noise in the circuit.
- 6. The cutoff frequency of the circuit is dependent on the gain bandwidth product (GBP) of the amplifier.
- 7. Adding a capacitor in parallel with R<sub>1</sub> will improve stability of the circuit if high-value resistors are used.



## **Design Steps**

$$V_{o} = V_{i} \times \left(\frac{R_{4}}{R_{3} + R_{4}}\right) \left(\frac{R_{1} + R_{2}}{R_{2}}\right) + V_{ref} \times \left(\frac{R_{3}}{R_{3} + R_{4}}\right) \left(\frac{R_{1} + R_{2}}{R_{2}}\right)$$

1. Calculate the gain of the input voltage to produce the desired output swing.

$$\begin{aligned} G_{input} &= \left(\frac{R_4}{R_3 + R_4}\right) \left(\frac{R_1 + R_2}{R_2}\right) \\ V_{o\_max} - V_{o\_min} &= \left(V_{i\_max} - V_{i\_min}\right) \left(\frac{R_4}{R_3 + R_4}\right) \left(\frac{R_1 + R_2}{R_2}\right) \\ \frac{V_{o\_max} - V_{o\_min}}{V_{i\_max} - V_{i\_min}} &= \left(\frac{R_4}{R_3 + R_4}\right) \left(\frac{R_1 + R_2}{R_2}\right) \\ \frac{4.95V - 0.05V}{3V - (-1V)} &= \left(\frac{R_4}{R_3 + R_4}\right) \left(\frac{R_1 + R_2}{R_2}\right) \\ 1.225V &= \left(\frac{R_4}{R_3 + R_4}\right) \left(\frac{R_1 + R_2}{R_2}\right) \end{aligned}$$

2. Select a value for R<sub>1</sub> and R<sub>4</sub> and insert the values into the previous equation. The other two resistor values must be solved using a system of equations. The proper output swing and offset voltage cannot be calculated if more than two variables are selected.

$$R_1 = R_4 = 1 \quad k\Omega$$
  
1.225V =  $\left(\frac{1 \quad k\Omega}{R_3 + 1 \quad k\Omega}\right) \left(\frac{1 \quad k\Omega + R_2}{R_2}\right)$ 

3. Solve the previous equation for  $R_3$  in terms of  $R_2$ .

$$R_3 = \frac{1 \quad M\Omega + (1 \quad k\Omega \times R_2)}{1.225 \times R_2} - 1 \quad k\Omega$$

4. Select any point along the transfer function within the linear output range of the amplifier to set the proper offset voltage at the output (for example, the minimum input and output voltage).

$$\begin{split} &V_{o\_min} = V_{i\_min} \times \left(\frac{R_4}{R_3 + R_4}\right) \left(\frac{R_1 + R_2}{R_2}\right) + V_{ref} \times \left(\frac{R_3}{R_3 + R_4}\right) \left(\frac{R_1 + R_2}{R_2}\right) \\ &0.05V = \ -1 \quad V \times \left(\frac{1 \quad k\Omega}{R_3 + 1 \quad k\Omega}\right) \left(\frac{1 \quad k\Omega + R_2}{R_2}\right) + 2.5V \times \left(\frac{R_3}{R_3 + 1 \quad k\Omega}\right) \left(\frac{1 \quad k\Omega + R_2}{R_2}\right) \end{split}$$

5. Insert  $R_3$  into the equation from step 1 and solve for  $R_2$ .

$$\begin{array}{ll} 0.05\mathrm{V}=\ -1 & \mathrm{V} \times \left( \frac{1 & \mathrm{k\Omega}}{1 & \mathrm{k\Omega} + 1 & \mathrm{k\Omega} \times \mathrm{R}_2} - 1 & \mathrm{k\Omega} + 1 & \mathrm{k\Omega} \right) \left( \frac{1 & \mathrm{k\Omega} + \mathrm{R}_2}{\mathrm{R}_2} \right) + 2.5\mathrm{V} \times \left( \frac{\frac{1 & \mathrm{M\Omega} + 1 & \mathrm{k\Omega} \times \mathrm{R}_2}{1.225 \times \mathrm{R}_2} - 1 & \mathrm{k\Omega}}{\frac{1 & \mathrm{M\Omega} + 1 & \mathrm{k\Omega} \times \mathrm{R}_2}{1.225 \times \mathrm{R}_2} - 1 & \mathrm{k\Omega} + 1 & \mathrm{k\Omega} \right) \\ \left( \frac{1 & \mathrm{k\Omega} + \mathrm{R}_2}{\mathrm{R}_2} \right) \\ R_2 = 1360.5\mathrm{\Omega} & \approx 1370\mathrm{\Omega} \end{array}$$

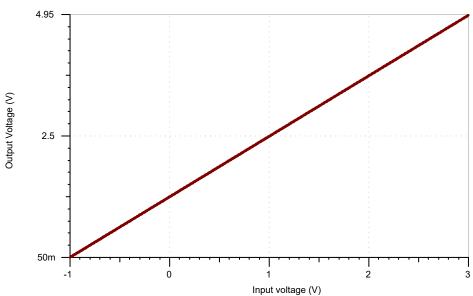
6. Insert  $R_2$  into the equation from step 1 to solve for  $R_3$ .

$$\begin{split} R_3 &= \frac{1 \quad M\Omega + 1 \quad k\Omega \times (1370\Omega)}{1.225 \times (1370\Omega)} - 1 \quad k\Omega \\ R_3 &= 412.18\Omega \approx 412\Omega \end{split}$$

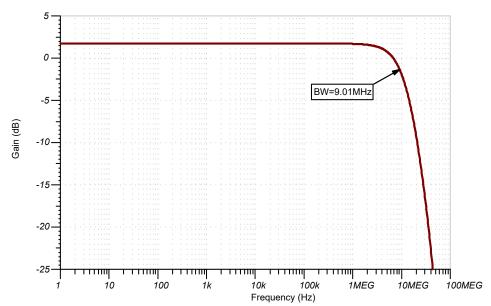


## **Design Simulations**

# **DC Simulation Results**



## **AC Simulation Results**





#### **Design References**

See Analog Engineer's Circuit Cookbooks for TI's comprehensive circuit library.

See the circuit SPICE simulation file SBOC513.

See Designing Gain and Offset in Thirty Seconds.

#### **Design Featured Op Amp**

MCP6292				
V <sub>ss</sub>	2.4 V to 5.5 V			
V <sub>inCM</sub>	Rail-to-rail			
V <sub>out</sub>	Rail-to-rail			
V <sub>os</sub>	0.3 mV			
Ι <sub>q</sub>	600 µA			
I <sub>b</sub>	1 pA			
UGBW	10 MHz			
SR	6.5 V/µs			
#Channels	1, 2, and 4			
MCI	P6292			

## **Design Alternate Op Amp**

OPA388				
V <sub>ss</sub>	2.5 V to 5.5 V			
V <sub>inCM</sub>	Rail-to-rail			
V <sub>out</sub>	Rail-to-rail			
V <sub>os</sub>	0.25 µV			
Ιq	1.9 mA			
۱ <sub>b</sub>	30 pA			
UGBW	10 MHz			
SR	5 V/µs			
#Channels	1, 2, and 4			
OPA388				

## **Revision History**

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NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

#### Changes from February 1, 2018 to February 4, 2019

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