20-bit, 1-MSPS Isolator Optimized Data Acquisition Reference Design

Maximizing SNR and Sample Rate

Designed for:

Public Release

Assembly Variant: ADS8900B

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**ANALOG FRONT END**

**REFERENCE GENERATION**

**COMMON MODE GENERATION**

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Deleting anything else from this page may result in your EVM submission being rejected (until you add them back).

Update the Label Text in the Label Table as needed for each Assembly Variant.

You can delete this note too.

**Label Table**

<table>
<thead>
<tr>
<th>Variant</th>
<th>Label Text</th>
</tr>
</thead>
<tbody>
<tr>
<td>001</td>
<td>ChangeMe!</td>
</tr>
<tr>
<td>002</td>
<td>ChangeMe!</td>
</tr>
</tbody>
</table>

**Assembly Notes**

**ZZ1**
These assemblies are ESD sensitive, ESD precautions shall be observed.

**ZZ2**
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

**ZZ3**
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
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