Battery tester reference design for multiphase high-precision 0.5 to 100-A battery formation

Designed for:
Public Release

Assembly Variant:
001

Contact:
http://www.ti.com/support

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Revision History

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TIDA-01041

http://www.ti.com
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Version control disabled

SVN Rev: TIDA-01041

Orderable: N/A

PCB Label:

Label Table

<table>
<thead>
<tr>
<th>Variant</th>
<th>Label Text</th>
</tr>
</thead>
<tbody>
<tr>
<td>001</td>
<td>001</td>
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<tr>
<td>002</td>
<td>002</td>
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</tbody>
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Label Note: No extra labels for PCB labels only.

Assembly Note: These assemblies are ESD sensitive, ESD precautions shall be observed.

Assembly Note: These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

Assembly Note: These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
Enable Jumpers

Optional Ground Jumpers

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Optical Ground Jumpers
Pull OPT pin high to enable 4 phase operation.
Enable Jumper:
- Connect EN1 to GND
- Connect EN2 to VCC

Optional Ground Jumper:
- Connect OPT to AGND

Direction Control:
- Connect VIN to V5.0_Control
- Connect VOUT to DNP
- Connect SW1 to LO1
- Connect SW2 to HO1

Enable Jumper:
- Pull OPT pin high to enable 4 phase operation

Direction Control:
- Set the direction using the control pins

V5.0_Control
- Use for control signals

Enable Jumper:
- Connect EN1 to GND
- Connect EN2 to VCC

Direction Control:
- Set the direction using the control pins
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Analog Loop Control

Mode:  

- DIR=0, EN=1
  - MOSFET turn off
- Buck Mode:  
  - DIR=1, EN=1
  - MOSFET turn off

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