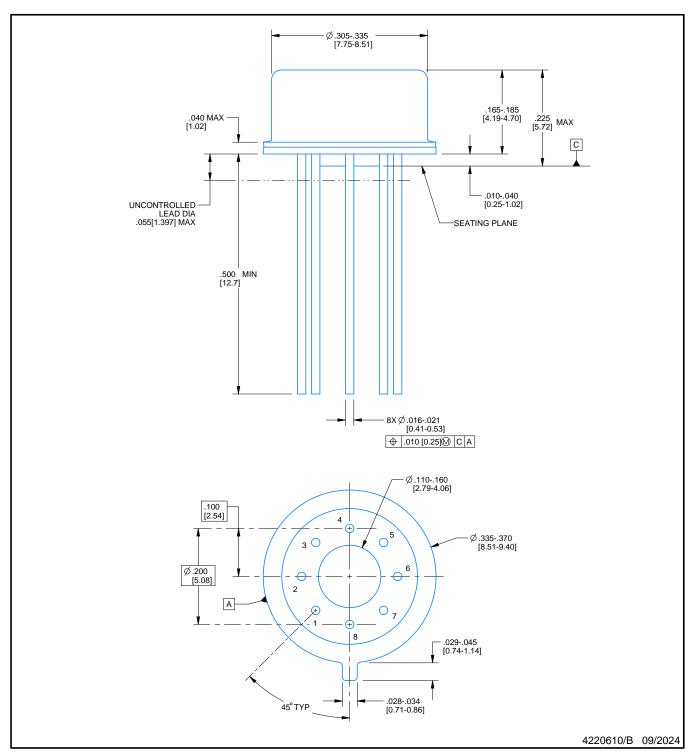
TRANSISTOR OUTLINE



## NOTES:

- 1. All linear dimensions are in inches [millimeters]. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.

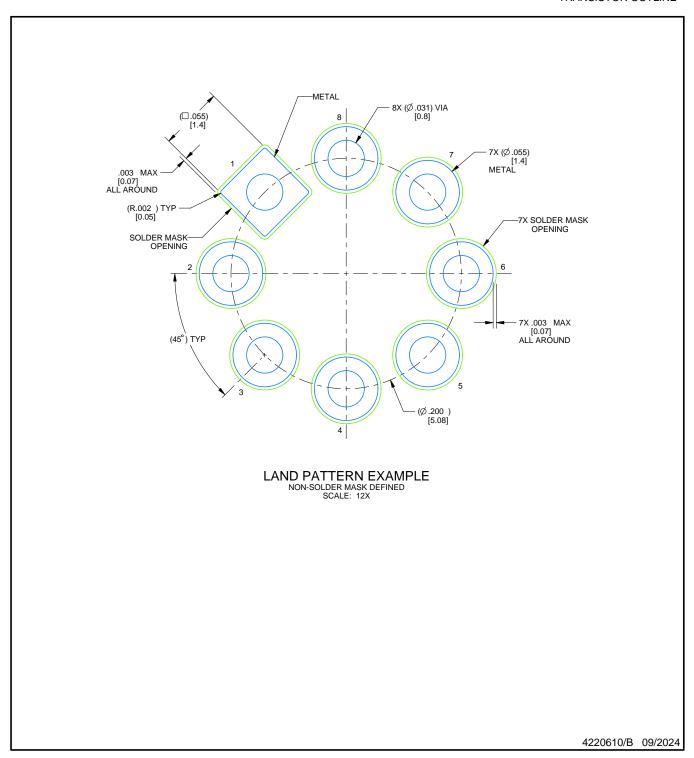
  2. This drawing is subject to change without notice.

  3. Pin numbers shown for reference only. Numbers may not be marked on package.

- 4. Reference JEDEC registration MO-002/TO-99.



TRANSISTOR OUTLINE



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