Temperature Sensing PTC Circuit With MSP430™ Smart Analog Combo



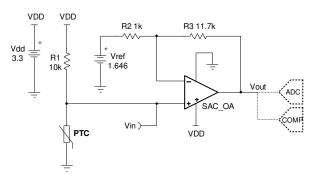
Design Goals

Temperature		Output voltage		Supply		
T _{Min}	T _{Max}	V _{outMin}	V _{outMax}	V _{dd}	V _{ee}	V _{ref}
0°C	50°C	0.15 V	3.15 V	3.3 V	0 V	1.646 V

Design Description

Some MSP430™ microcontrollers (MCUs) contain configurable integrated signal chain elements such as opamps, DACs, and programmable gain stages. These elements make up a peripheral called the Smart Analog Combo (SAC). For information on the different types of SACs and how to leverage their configurable analog signal chain capabilities, visit MSP430 MCUs Smart Analog Combo Training. To get started with your design, download the Temperature Sensing PTC Circuit Design Files.

This temperature sensing circuit uses a resistor in series with a positive-temperature-coefficient (PTC) thermistor to form a voltage divider, which produces an output voltage that is linear over temperature. The circuit uses the MSP430FR2311 SAC_L1 op-amp in a noninverting amplifier configuration with inverting reference to offset and amplify the signal, which helps to use the full ADC resolution and increase measurement accuracy. (Note: The MSP430FR2355 features four SAC_L3 peripherals which each contain a built-in DAC and PGA, providing a single-chip solution for generating Vref and measuring the thermistor circuit.) The output of the integrated SAC op-amp can be sampled directly by the on-board ADC or monitored by the on-board comparator for further processing inside the MCU.



Design Notes

- The connection, V_{in}, is a positive temperature coefficient output voltage. To measure the output voltage of a negative-temperature-coefficient (NTC) thermistor, switch the position of R₁ and the PTC resistor.
- Vref can be generated by the integrated SAC_L3 DACs in the MSP430FR2355 or a voltage divider. If a
 voltage divider is used, the equivalent resistance of the voltage divider affects the gain of the circuit.
- Using high-value resistors can degrade the phase margin of the amplifier and introduce additional noise in the circuit. It is recommended to use resistor values around 10 k Ω or less.
- If the solution is implemented using the MSP430FR2311, the SAC_L1 op-amp is configured in general purpose mode to measure the thermistor circuit.
- If the solution is implemented using the MSP430FR2355, one SAC_L3 peripheral is configured in DAC mode to generate the reference voltage and another is configured in general purpose mode to measure the thermistor circuit.



Design Steps

$$V_{out} = V_{dd} \times \frac{R_{PTC}}{R_{PTC} + R_1} \times \frac{R_2 + R_3}{R_2} - \frac{R_3}{R_2} \times V_{ref}$$
 (1)

1. Calculate the value of R₁ to produce a linear output voltage. Use the minimum and maximum values of the PTC to obtain a range of values for R₁.

$$\begin{split} R_{PTC_Max} &= R_{PTC} \underset{@50^{\circ}C}{@50^{\circ}C} = 11.611 \text{ k}\Omega \\ R_{PTC_Min} &= R_{PTC} \underset{@0^{\circ}C}{@0^{\circ}C} = 8.525 \text{ k}\Omega \\ R_{1} &= \sqrt{R_{PTC} \underset{@0^{\circ}C}{@0^{\circ}C} \times R_{PTC} \underset{@50^{\circ}C}{@50^{\circ}C}} = \sqrt{8.525 \text{ k}\Omega \times 11.611 \text{ k}\Omega} = 9.95 \text{ k}\Omega \approx 10 \text{ k}\Omega \end{split}$$

2. Calculate the input voltage range.

$$\begin{split} V_{inMin} &= V_{dd} \times \frac{R_{PTC_Min}}{R_{PTC_Min} + R_1} = 3.3 \text{ V} \times \frac{8.525 \text{ k}\Omega}{8.525 \text{ k}\Omega + 10 \text{ k}\Omega} = 1.519 \text{ V} \\ V_{inMax} &= V_{dd} \times \frac{R_{PTC_Max}}{R_{PTC_Max} + R_1} = 3.3 \text{ V} \times \frac{11.611 \text{ k}\Omega}{11.611 \text{ k}\Omega + 10 \text{ k}\Omega} = 1.773 \text{ V} \end{split}$$

Calculate the gain required to produce the maximum output swing.

$$G_{ideal} = \frac{V_{outMax} - V_{outMin}}{V_{inMax} - V_{inMin}} = \frac{3.15 \text{ V} - 0.15 \text{ V}}{1.773 \text{ V} - 1.519 \text{ V}} = 11.811 \frac{\text{V}}{\text{V}}$$
(4)

4. Select R₂ and calculate R₃ to set the gain calculated in Step 3.

$$\begin{aligned} \text{Gain} &= \frac{R_2 + R_3}{R_2} \\ R_2 &= 1 \, k\Omega \\ R_3 &= R_2 \times \left(G_{ideal} - 1 \right) = 1 \, k\Omega \times \left(11.811 - 1 \right) = 10.811 \, k\Omega \end{aligned}$$
 Choose $R_3 = 10.7 \, k\Omega$ (Standard value)

5. Calculate the actual gain based on standard values of R₂ and R₃.

$$G_{\text{actual}} = \frac{R_2 + R_3}{R_2} = \frac{1 \,\text{k}\Omega + 10.7 \,\text{k}\Omega}{1 \,\text{k}\Omega} = 11.7 \,\frac{\text{V}}{\text{V}}$$
 (6)

6. Calculate the output voltage swing based on the actual gain.

$$V_{out_swing} = (V_{inMax} - V_{inMin}) \times G_{actual} = (1.773 \text{ V} - 1.519 \text{ V}) \times 11.7 \frac{V}{V} = 2.9718 \text{V}$$
 (7)

7. Calculate the maximum output voltage when the output voltage is symmetrical around mid-supply.

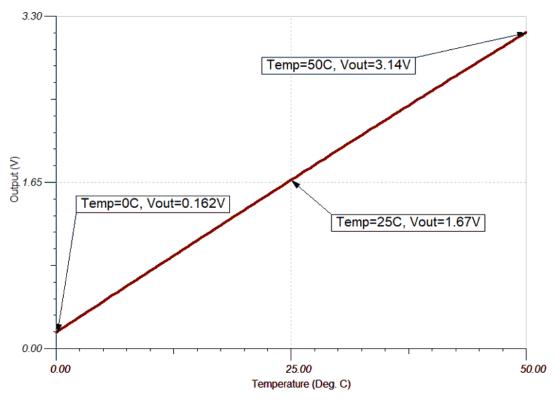
$$V_{outMax} = V_{mid-supply} + \frac{V_{out_swing}}{2} = \frac{V_{dd} - V_{ee}}{2} + \frac{V_{out_swing}}{2} = \frac{3.3 \text{ V} - 0 \text{ V}}{2} + \frac{2.9718 \text{ V}}{2} = 3.136 \text{ V}$$
 (8)

8. Calculate the reference voltage.

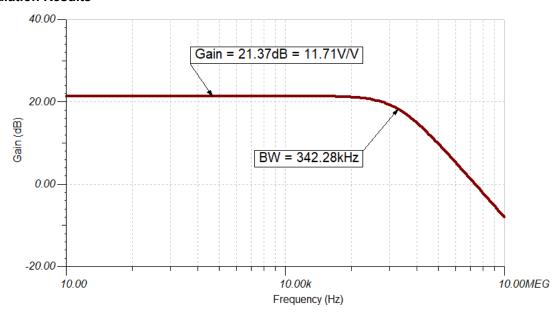
$$\begin{split} V_{outMax} &= V_{inMax} \times G_{actual} - \frac{R_3}{R_2} \times V_{ref} \\ 3.136 \ V &= 1.773 \ V \times 11.7 \ \frac{V}{V} - \frac{10.7 \ k\Omega}{1 \ k\Omega} \times V_{ref} \\ V_{ref} &= \frac{1.773 \ V \times 11.7 \ \frac{V}{V} - 3.136 \ V}{\frac{10.7 \ k\Omega}{1 \ k\Omega}} = 1.646 \ V \end{split}$$

Design Simulations

DC Transfer Results



AC Simulation Results



Target Applications

- Field temperature transmitters
- Thermostats
- Thermometers
- Thermistor probes
- · System temperature monitor

References

- 1. MSP430 MCUs Smart Analog Combo Training
- 2. Analog Engineer's Circuit Cookbooks
- 3. MSP430FR2311 TINA-TI Spice Model
- 4. MSP430 Temp Sense PTC Circuit Code Examples and SPICE Simulation File

Design Featured Op Amp

MSP430FRxx Smart Analog Combo					
	MSP430FR2311 SAC_L1	MSP430FR2355 SAC_L3			
V _{cc}	2.0 V to 3.6 V				
V _{CM}	-0.1 V to V _{CC} + 0.1 V				
V _{out}	Rail-to-rail				
V _{os}	±5 mV				
A _{OL}	100 dB				
	350 μA (high-speed mode)				
Iq	120 μA (low-power mode)				
l _b	50 pA				
UGBW	4 MHz (high-speed mode)	2.8 MHz (high-speed mode)			
OGDW	1.4 MHz (low-power mode)	1 MHz (low-power mode)			
SR	3 V/µs (high-speed mode)				
SK .	1 V/µs (low-power mode)				
Number of channels	1	4			
	MSP430FR2311	MSP430FR2355			

Design Alternate Op Amp

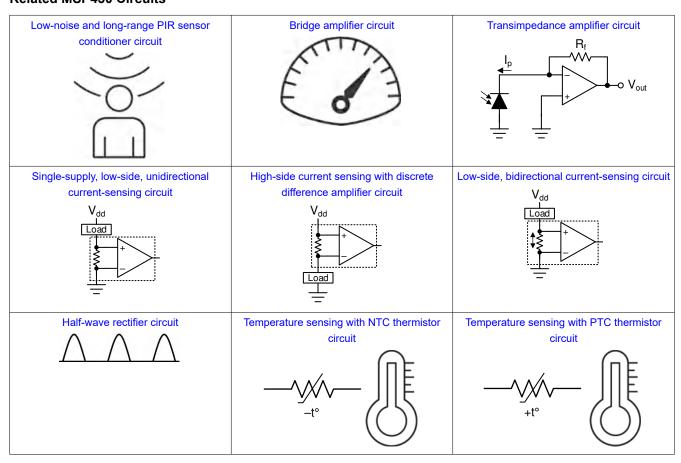
MSP43	MSP430FR2311 Transimpedance Amplifier				
V _{cc}	2.0 V to 3.6 V				
V _{CM}	-0.1 V to V _{CC} /2 V				
V _{out}	Rail-to-rail				
V _{os}	V _{os} ±5 mV				
A _{OL}	100 dB				
	350 μA (high-speed mode)				
I _q	120 μA (low-power mode)				
	5 pA (TSSOP-16 with OA-dedicated pin input)				
I _b	50 pA (TSSOP-20 and VQFN-16)				
HODW	5 MHz (high-speed mode)				
UGBW	1.8 MHz (low-power mode)				
SR	4 V/µs (high-speed mode)				
ЭК	1 V/µs (low-power mode)				
Number of channels	1				
	MSP430FR2311				

www.ti.com Revision History

Design Featured Thermistor

TMP61				
V _{CC}	Up to 5.5 V			
R ₂₅	10 kΩ			
R _{TOL}	1%			
I _{SNS}	400 μΑ			
Operating temperature range	-40°C to 125°C			
TMP61				

Related MSP430 Circuits



Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from	October 19	9, 2019 to	March 6, 2020
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