

Steve Corrigan HPL-D Interface

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Introduction www.ti.com

1 Introduction

This user's guide details the evaluation module (EVM) operation of the ISO721 and ISO721M digital isolators. The same EVM board is used for each device. Configuration requirements are presented as well as user optional I/O loads. This document is intended to aid designers with isolator parameter performance evaluation within a particular system.

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2 Overview

The ISO721 and ISO721M digital isolators have a logic input and output buffer separated by a silicon oxide (SiO₂) insulation barrier. Used with isolated power supplies, these devices prevent noise currents on a data bus or other circuits from entering the local ground and interfering with or damaging sensitive circuitry.

A binary input signal is conditioned, translated to a balanced signal, and then differentiated by the SiO_2 isolation barrier. Across the isolation barrier, a differential comparator receives the logic transition information, then sets or resets a flip-flop and the output circuit accordingly. A periodic update pulse is sent across the barrier to ensure the proper dc level of the output. If this dc-refresh pulse is not received for more than 4 μ s, the input is assumed to be unpowered or not functional, and the fail-safe circuit drives the output to a logic-high state.

CAUTION

Note that although these devices provide galvanic isolation of up to 4000 V, this EVM cannot be used for isolation voltage testing. It is designed for the examination of device operating parameters only and will be damaged if high voltage (> 5.5 V) is applied anywhere in the circuit.

3 Functional Configuration of the ISO721 and ISO721M

The EVM is configured for the pinout displayed in Figure 3-1. The additional I/Os on the EVM are provided for future development.

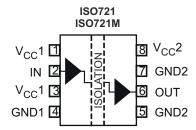


Figure 3-1. The ISO721 and ISO721M Pinout

The ISO721 has TTL input thresholds and a input noise filter that prevents transient pulses of up to 2 ns in duration from being passed to the output of the device.

The ISO721M has a CMOS $V_{CC}/2$ input threshold and does not have the noise filter and the additional propagation delay. These features of the ISO721M also provide for a 0-Mbps to 150-Mbps signaling rate rather than the ISO721's 0-Mbps to 100-Mbps signaling rate.



4 EVM Signal Paths of the ISO721 and ISO721M Isolators

This multifunctional EVM is designed with signal paths shown in Figure 4-1 for the analysis of the ISO721 and ISO721M, as well as future isolator configurations.

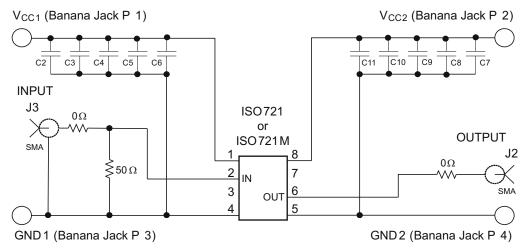


Figure 4-1. The ISO721 and ISO721M EVM Schematic

Table 4-1. EVM Connections

| Connection | Label | Description |
|------------|------------------|-----------------------------------------------|
| J1 | | SMA connector (unused) |
| J2 | | SMA connector to the output pin 6 |
| J3 | | SMA connector to the input pin 2 |
| J4 | | SMA connector (unused) |
| P1 | V _{CC1} | Input power supply banana jack |
| P2 | V _{CC2} | Output power supply banana jack |
| P3 | GND1 | Input power ground connection banana jack |
| P4 | GND2 | Output power ground connection banana jack |
| JMP1 | | 3-pin jumper (unused) |
| JMP2 | | 3-pin jumper (unused) |
| JMP3 | | 3-pin jumper – V _{CC1} , input, GND1 |
| JMP4 | | 3-pin jumper (unused) |



The EVM Configuration www.ti.com

5 The EVM Configuration

The ISO721EVM configuration has an SMA connector J3 set up as the input to pin 2, the IN pin of the ISO721 in Figure 3-1 and Figure 4-1. A $0-\Omega$ input series resistor, R8, is located next to the J3 input connector, and a $50-\Omega$ R5 from the input to ground is located on the bottom of the board.

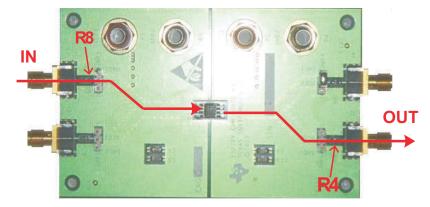


Figure 5-1. ISO721 and ISO721M EVM, Top

The output channel configuration of the ISO721EVM has the OUT pin (pin 6) of Figure 3-1 and Figure 4-1 connected to SMA connector J2 through a 0- Ω series resistor R4.

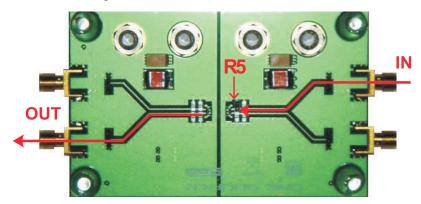


Figure 5-2. ISO721 and ISO721M EVM, Bottom

The pads for R3, C12, and C13 are available on the bottom of the EVM for varied loading conditions if desired by a user.



6 EVM Setup and Operation

This section includes the setup and operation of the EVM for parameter performance evaluation. Typical waveforms are included.

6.1 Overview

The basic setup in Figure 6-1 has the two power supplies required to evaluate isolator performance with 3.3-V on one side and 5-V on the other. If both sides are to be evaluated at the same supply voltage, only one power supply is required and can be used to power both sides of the EVM.

CAUTION

Note that this EVM is for operating parameter performance evaluation only and not designed for isolation voltage testing. Any voltage applied above the 5.5-V maximum recommended operating voltage of the ISO721 will damage the EVM.

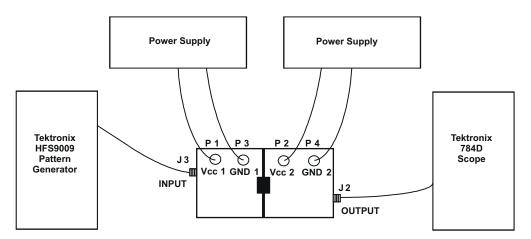


Figure 6-1. Basic EVM Operation

The input to the EVM is a 20-MHz pulse displayed on channel 1 in Figure 6-2. The output of the EVM is channel 2.

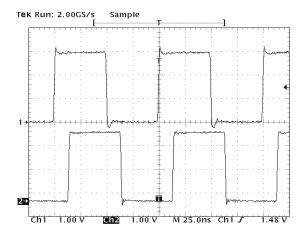


Figure 6-2. Typical Input and Output Waveforms

7 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.



Revision History www.ti.com

| C | hanges from Revision * (January 2006) to Revision A (September 2022) | Page |
|---|----------------------------------------------------------------------|------|
| • | Replaced Capacitor with SiO ₂ | 2 |

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CAUTION

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- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

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Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types lated in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

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Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

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- 2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
- 3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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