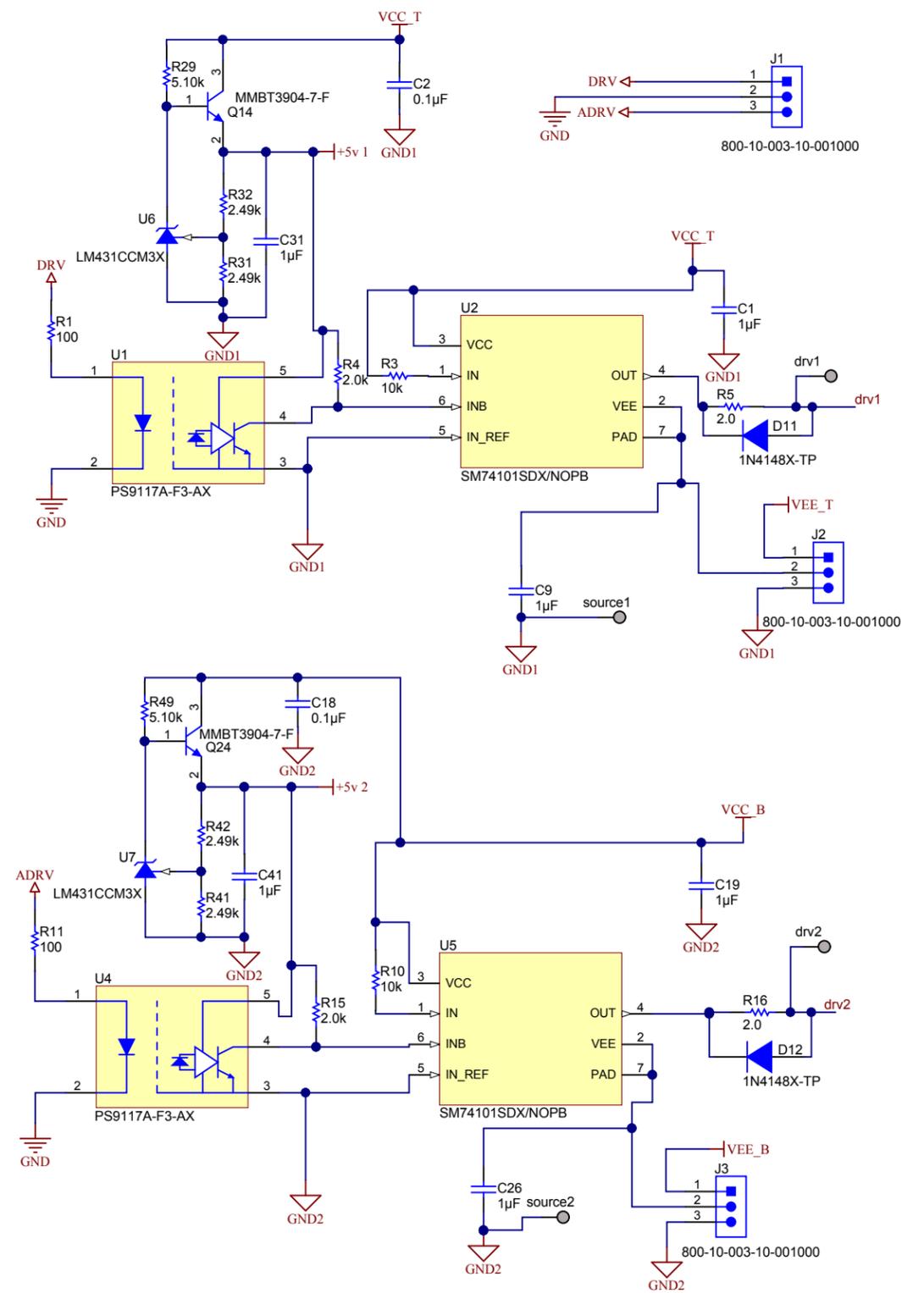
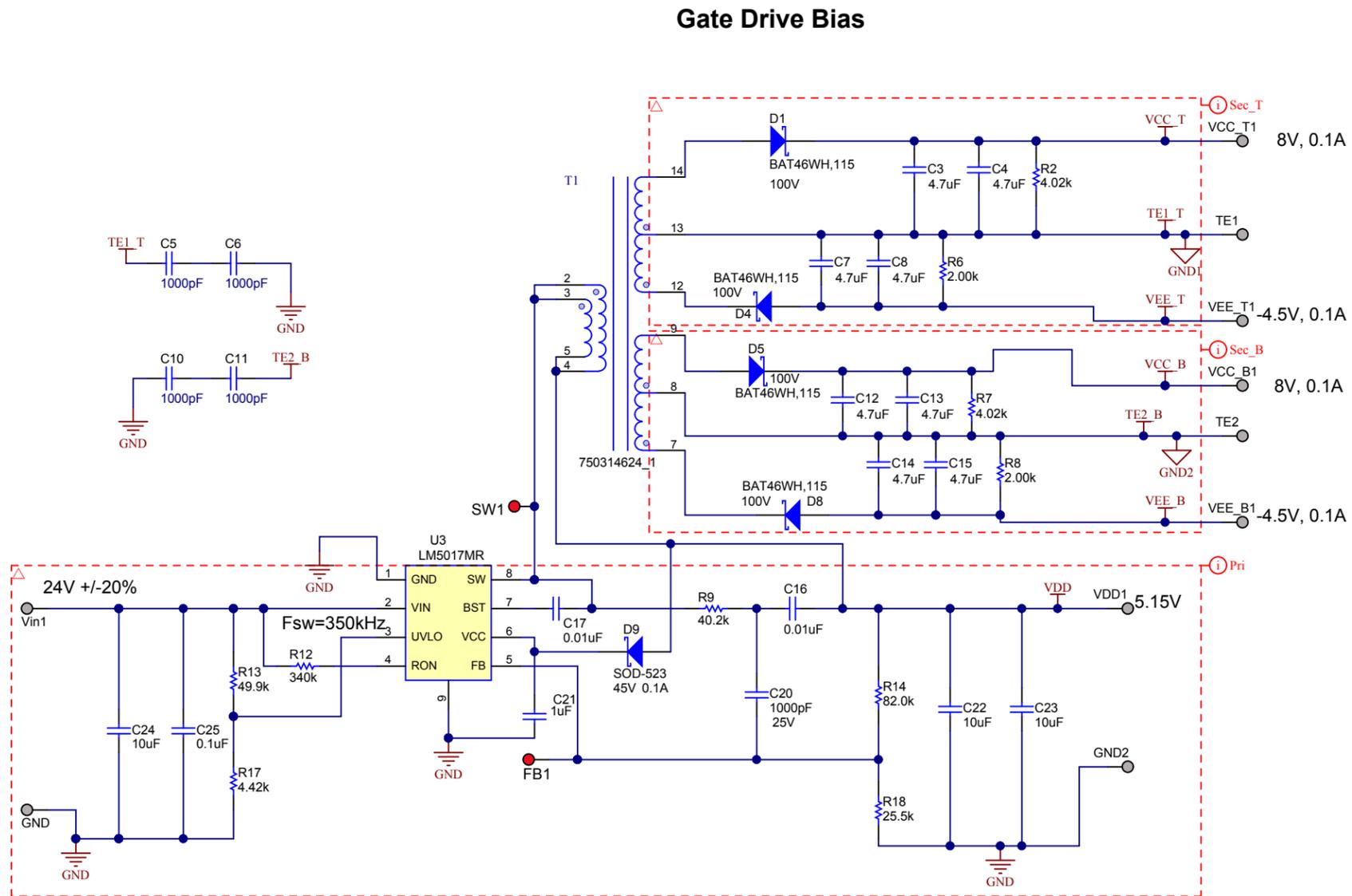


# Gate Drive Bias



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Designed for: Public Release		Mod. Date: 5/22/2015	
Project Title: PMP10645			
Sheet Title:			
Number: PMP10645		Rev: rev2	
SVN Rev: Version control disabled			
Drawn By:		Assembly Variant: [No Variations]	
Engineer: Frank Xiao		File: PMP10645.SchDoc	
		Sheet: 1 of 2	
		Size: B	
		Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	





PCB Number: PMP10645  
PCB Rev: A

PCB  
LOGO  
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PCB  
LOGO  
Pb-Free Symbol

Label Table	
Variant	Label Text
001	ChangeMe!

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