					1
	1	2	3	4	5
A		Rectifier	DC+ Rhs		
		220Vac Mains	DC- RIS DC- W	Voltage follower buffer	
В				OPA2376 Hi-side current sense ADCO	ICU
С			High side and low side current sense voltages are summed together and provided to comparator Vtl TLC372 Vtl TLC372 SV Power sup references	s(REF2033)	
D			DC-UCC28880 and LM404	2.5V DC+ Ploat_ref	
		Texas Instruments and/or its licensors warrant that this design will meet the s licensors do not warrant that the design	do not warrant the accuracy or completeness of this specification or any pecifications, will be suitable for your application or fit for any particular p n is production worthy. You should completely validate and test your des	r information contained therein. Texas Instruments and/or its licensors do purpose, or will operate in an implementation. Texas Instruments and/or sign implementation to confirm the system functionality for your applicatio	Orderable: N/A TID #: TID Number: TIDA-00442 Rev: E SVN Rev: Version control disab its Drawn By: Pawan N on. Engineer: Pawan N
	1	2	3	4	5
				·	

5	6

		Mod. Date: 6/15/2015	_
	Project Title: Ground Fault Protection for 220Vac Powered D		LII TEXA
Rev: E2	Sheet Title: Block Diagram		TEXA INST
ol disabled Assembly Variant: 001		Sheet: 1 of 3	1.01
	File: Sh01_CoverSheet.SchDoc	Size: B	http://ww
	Contact: http://www.ti.com/support		© Texas Instru

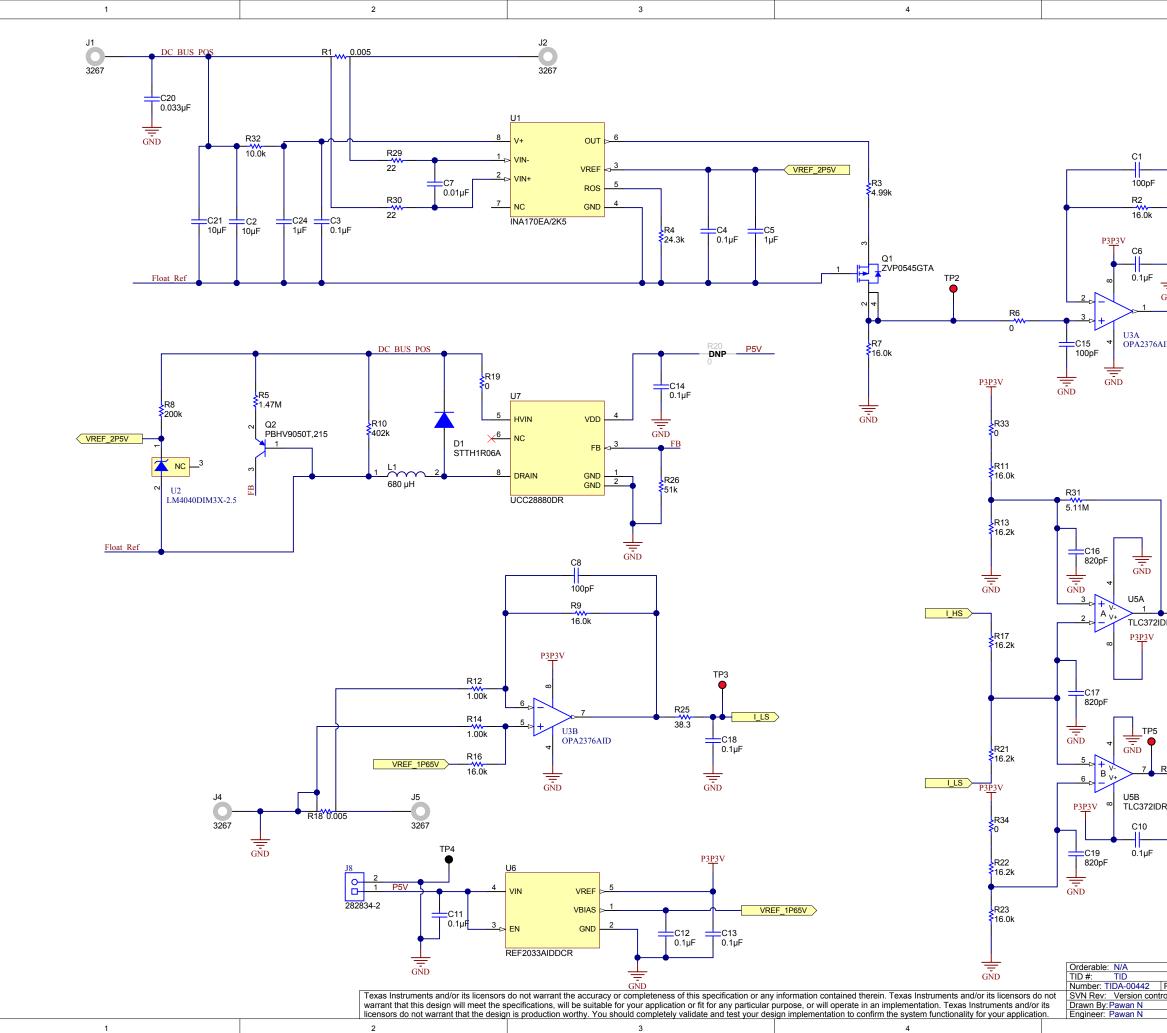


6

в

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D

5		6]
F			A	
76AID	R24 38.3 GND		В	
R27 0 72IDR	R15 ≥15.0k GND_FAUL	GND FAULT	$ \begin{array}{c} 1 \\ 2 \\ 0 \\ 282834-2 \end{array} $ C $ \begin{array}{c} 1 \\ 2 \\ 0 \\ 282834-2 \end{array} $ C $ \begin{array}{c} 1 \\ 2 \\ 0 \\ 282834-2 \end{array} $ C	
2IDR	Designed for: Public Relea Project Title: Ground Fault Sheet Title: Current sense Assembly Variant: 001 File: Sh02 Current sense. Contact: http://www.ti.com	ise Mod. Date: 6/15/2015 Protection for 220Vac Powered Drives and Signal Conditioning Sheet: 2 of 3 SchDoc Size: B /support	D TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments 2015	

 1			2	3	4	
H1	H2	H3	H4			
NY PMS 440 0025 PH						

FID1 FID2 FID3 FID4 FID5 FID6 PCB Number: TIDA-00442 PCB Rev: E2

PCB LOGO Texas Instruments

LBL1
 PCB Label

 Size: 0.65" x 0.20 "

А

в

С

D

	Label Table			
Variant	Label Text			
001	ChangeMe!			
002	ChangeMe!			

ZZ1 Label Assembly Note This Assembly Note is for PCB labels only

1

ZZ2 Assembly Note These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: N/A	Designed for: Public Release N	lod. Date: 6/15/2015	_
TID #: TID	Project Title: Ground Fault Protection for 2	220Vac Powered Drives	LIA TEX
Number: TIDA-00442 Rev: E2	Sheet Title:		
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 3 of 3	11 10
Drawn By: Pawan N	File: Sh03 TID Hardware.SchDoc	Size: B	http://w
Engineer: Pawan N	Contact: http://www.ti.com/support		© Texas Ins
E		6	

				Number: 11
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licensors do not warrant that the desig	n is production worthy. You should completely validate and test your des	ign implementation to confirm the system functionality	for your application.	Engineer: F
2	3	4		

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