



LMH3401IRMS can be used for U1.  
 If LMH3401IRMS used for U1  
 C11, C14, R3 and R4 can be  
 unpopulated(open).

LMH3401IRMS can be used for U2.  
 If LMH3401IRMS used for U2  
 R9 and R10 can be  
 unpopulated(open).

Orderable: 1GHZ-LINEAR-TIAEVM	Designed for: Public Release	Mod. Date: 5/18/2017
TID #:	Project Title: 1GHz LINEAR TIA EVM	
Number: TIDA-01350   Rev: E2	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: 001-1GHz LINEAR TIA EVM	Sheet: 1 of 2
Drawn By: Bob Holtz	File: TIDA-01350_E2_SchDoc	Size: B
Engineer: Loren Siebert	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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1 2 3 4 5 6

A

A

DNP FID1 DNP FID2 DNP FID3 DNP FID4 DNP FID5 DNP FID6

PCB Number: TIDA-01350  
PCB Rev: E2  
PCB LOGO Texas Instruments  
PCB LOGO Pb-Free Symbol  
PCB LOGO FCC disclaimer

B

B

C

C

ZZ1  
Assembly Note  
These assemblies are ESD sensitive, ESD precautions shall be observed.  
ZZ2  
Assembly Note  
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.  
ZZ3  
Assembly Note  
These assemblies must comply with workmanship standards IPC-A-610 Class 2., unless otherwise specified.

D

D

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Orderable: 1GHZ-LINEAR-TIAEVM	Designed for: Public Release	Mod. Date: 5/17/2017
TID #:	Project Title: 1GHZ LINEAR TIA EVM	
Number: TIDA-01350	Rev: E2	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001-1GHZ LINEAR TIA EVM	Sheet: 2 of 2
Drawn By: Bob Holtz	File: TIDA-01350_E2_Hardware.SchDoc	Size: B
Engineer: Loren Siebert	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	



1 2 3 4 5 6

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