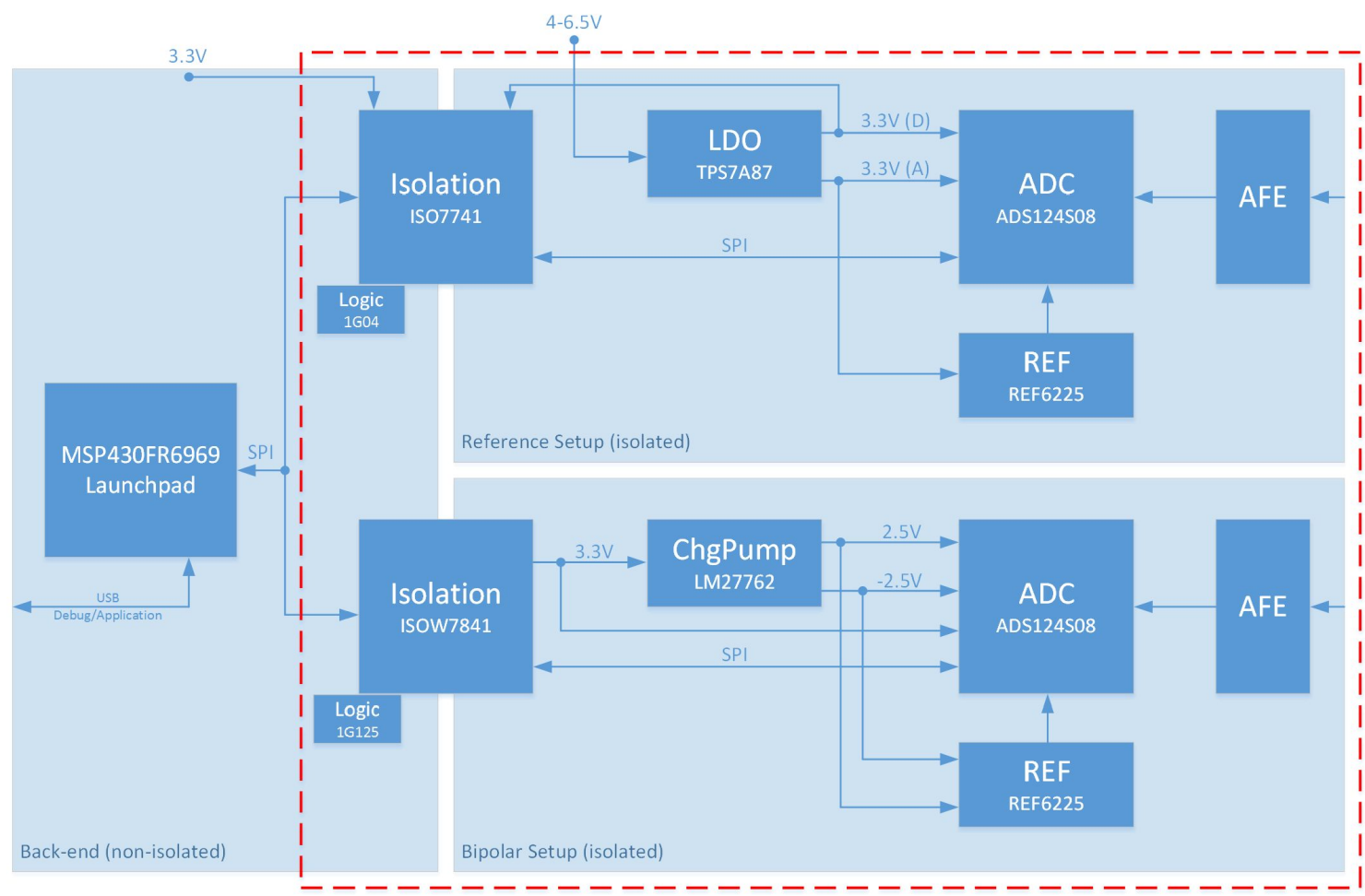


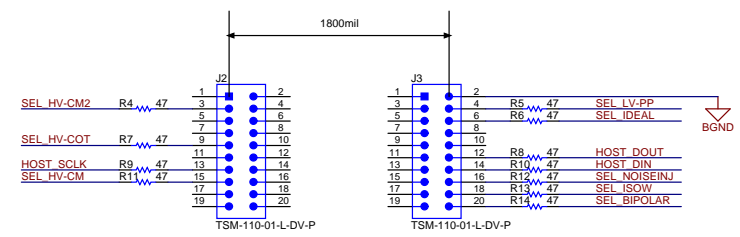
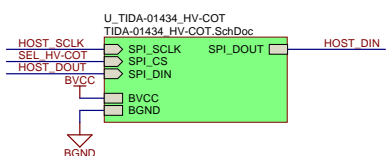
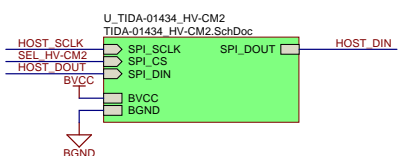
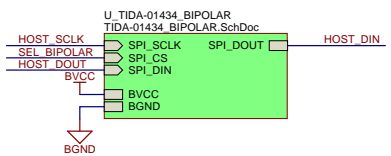
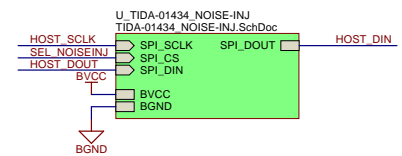
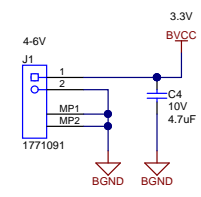
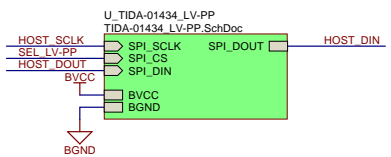
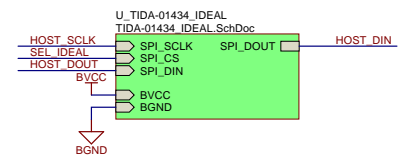
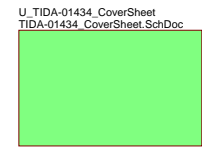
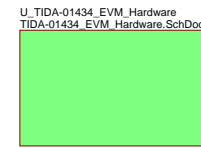
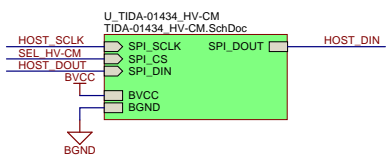
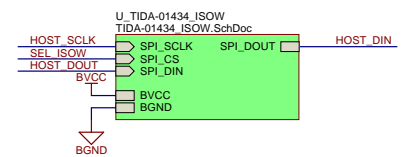
Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: EVM_orderable	Designed for: Public Release	Mod. Date: 10/10/2018
TID #: TIDA-01434	Project Title: TIDA-01434	
Number: TIDA-01434 Rev: E2	Sheet Title:	Sheet: 1 of 3
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	
Drawn By:	File: TIDA-01434_CoverSheet_SchDoc	Size: B
Engineer: Lars Lotzenburger	Contact: http://www.ti.com/support	





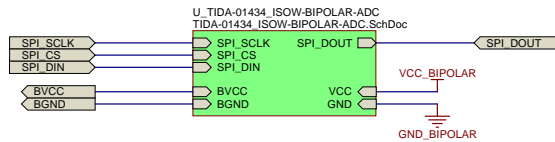
Lauchpad connector FR5969/CC3220 etc.

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: EVM_orderable	Designed for: Public Release	Mod. Date: 9/3/2018
TID #: TIDA-01434	Project Title: TIDA-01434	
Number: TIDA-01434 Rev: E2	Sheet Title:	
SVN Rev. Version control disabled	Assembly Variant: [No Variations]	Sheet: 2 of 3
Drawn By:	File: TIDA-01434_TopLevel.SchDoc	Size: B
Engineer: Lars Lotzenburger	Contact: http://www.ti.com/support	



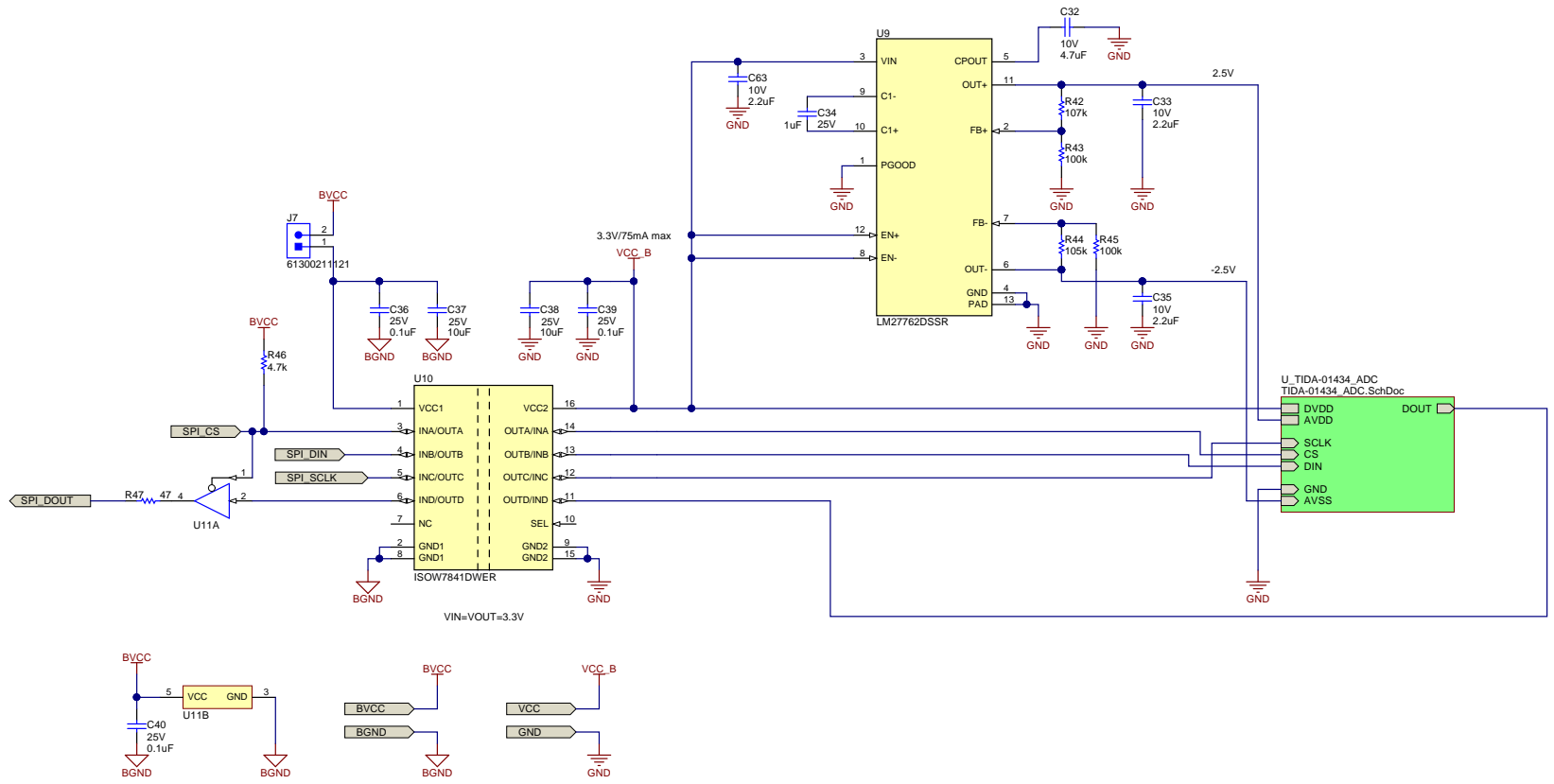
© Texas Instruments 2018



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: EVM_orderable	Designed for: Public Release	Mod. Date: 11/10/2017
TID #: TIDA-01434	Project Title: TIDA-01434	
Number: TIDA-01434 Rev: E2	Sheet Title:	
SVN Rev. Version control disabled	Assembly Variant: [No Variations]	Sheet: 2 of 3
Drawn By:	File: TIDA-01434_BIPOLAR.SchDoc	Size: B
Engineer: Lars Lotzenburger	Contact: http://www.ti.com/support	



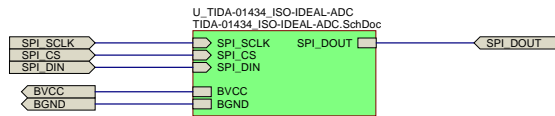


Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: EVM_orderable	Designed for: Public Release	Mod. Date: 12/15/2017
TID #: TIDA-01434	Project Title: TIDA-01434	
Number: TIDA-01434 Rev: E2	Sheet Title:	
SVN Rev. Version control disabled	Assembly Variant: [No Variations]	Sheet: 2 of 3
Drawn By:	File: TIDA-01434_ISOW-BIPOLAR-ADC_SchDoc	Size: B
Engineer: Lars Lotzenburger	Contact: http://www.ti.com/support	http://www.ti.com



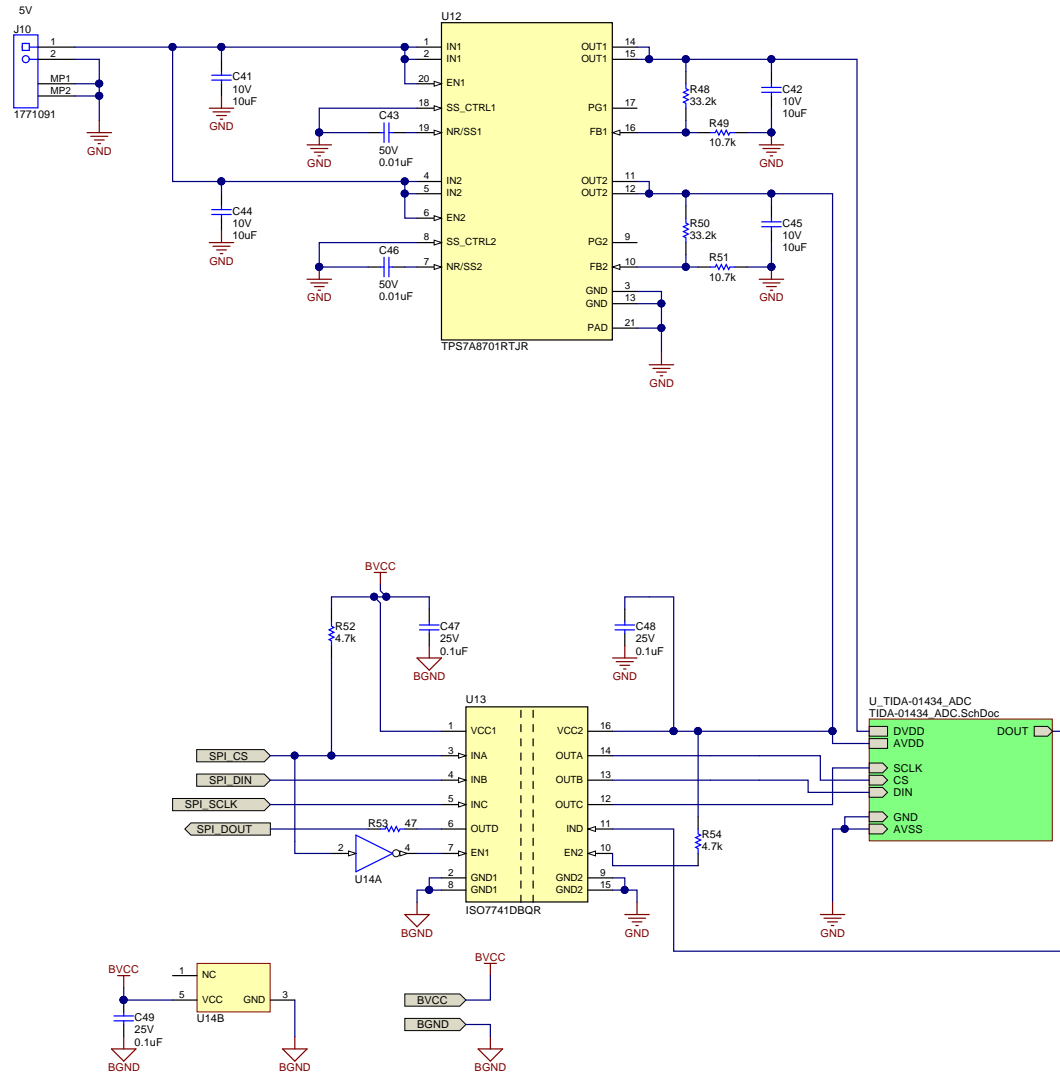
© Texas Instruments 2018



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: EVM_orderable	Designed for: Public Release	Mod. Date: 11/10/2017
TID #: TIDA-01434	Project Title: TIDA-01434	
Number: TIDA-01434 Rev: E2	Sheet Title:	
SVN Rev. Version control disabled	Assembly Variant: [No Variations]	Sheet: 2 of 3
Drawn By:	File: TIDA-01434_IDEAL_SchDoc	Size: B
Engineer: Lars Lotzenburger	Contact: http://www.ti.com/support	



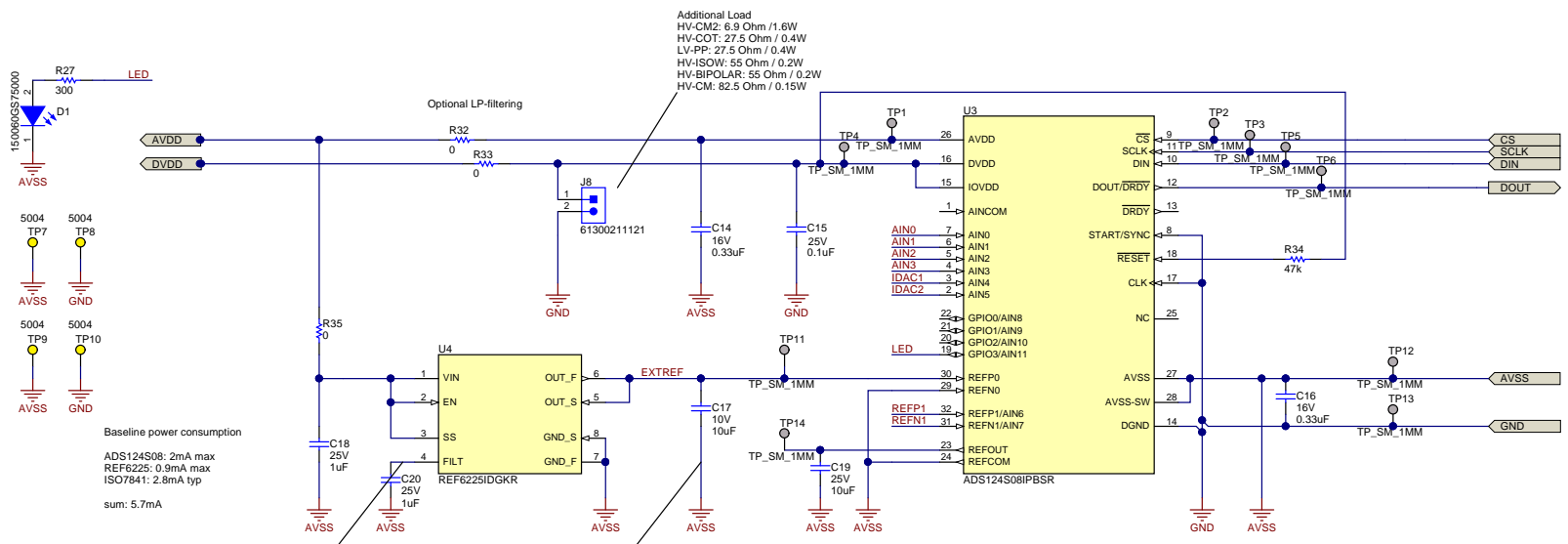
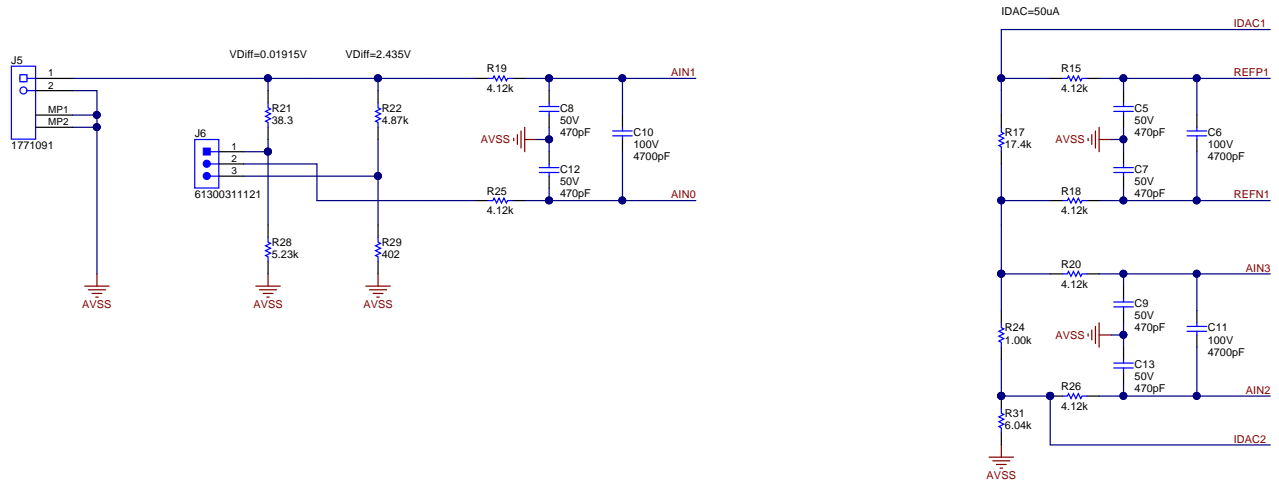


Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: EVM, orderable	Designed for: Public Release	Mod. Date: 11/27/2017
TID #: TIDA-01434	Project Title: TIDA-01434	
Number: TIDA-01434 Rev: E2	Sheet Title:	
SVN Rev. Version control disabled	Assembly Variant: [No Variations]	Sheet: 2 of 3
Drawn By:	File: TIDA-01434_ISO-IDEAL-ADC_SchDoc	Size: B
Engineer: Lars Lotzenburger	Contact: http://www.ti.com/support	



© Texas Instruments 2018



H1 NY PMS 440 0025 PH
 H2 NY PMS 440 0025 PH
 H3 NY PMS 440 0025 PH
 H4 NY PMS 440 0025 PH

H5 1902C
 H6 1902C
 H7 1902C
 H8 1902C

H9 SJ-5303 (CLEAR)
 H10 SJ-5303 (CLEAR)
 H11 SJ-5303 (CLEAR)
 H12 SJ-5303 (CLEAR)

FID1
 FID2
 FID3

PCB Number: TIDA-01434
 PCB Rev: E2

PCB LOGO
 PCB LOGO

Variant/Label Table

Variant	Label Text
001	ChangeMe!
002	ChangeMe!

LBL1
 PCB Label
 Size: 0.65" x 0.20"

ZZ1
 Label Assembly Note
 This Assembly Note is for PCB labels only

ZZ2
 Assembly Note
 This Assembly Note will show in the PcbDoc and associated outputs

ZZ3
 Assembly Note
 This Assembly Note will show in the PcbDoc and associated outputs

ZZ4
 Assembly Note
 This Assembly Note will show in the PcbDoc and associated outputs

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: EVM, orderable	Designed for: Public Release	Mod. Date: 11/27/2017
TID #: TIDA-01434	Project Title: TIDA-01434	
Number: TIDA-01434 Rev: E2	Sheet Title:	
SVN Rev. Version control disabled	Assembly Variant: [No Variations]	Sheet: 3 of 3
Drawn By:	File: TIDA-01434_EVM_Hardware.SchDoc	Size: B
Engineer: Lars Lotzenburger	Contact: http://www.ti.com/support	



© Texas Instruments 2018

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (www.ti.com/legal/termsofsale.html) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2018, Texas Instruments Incorporated