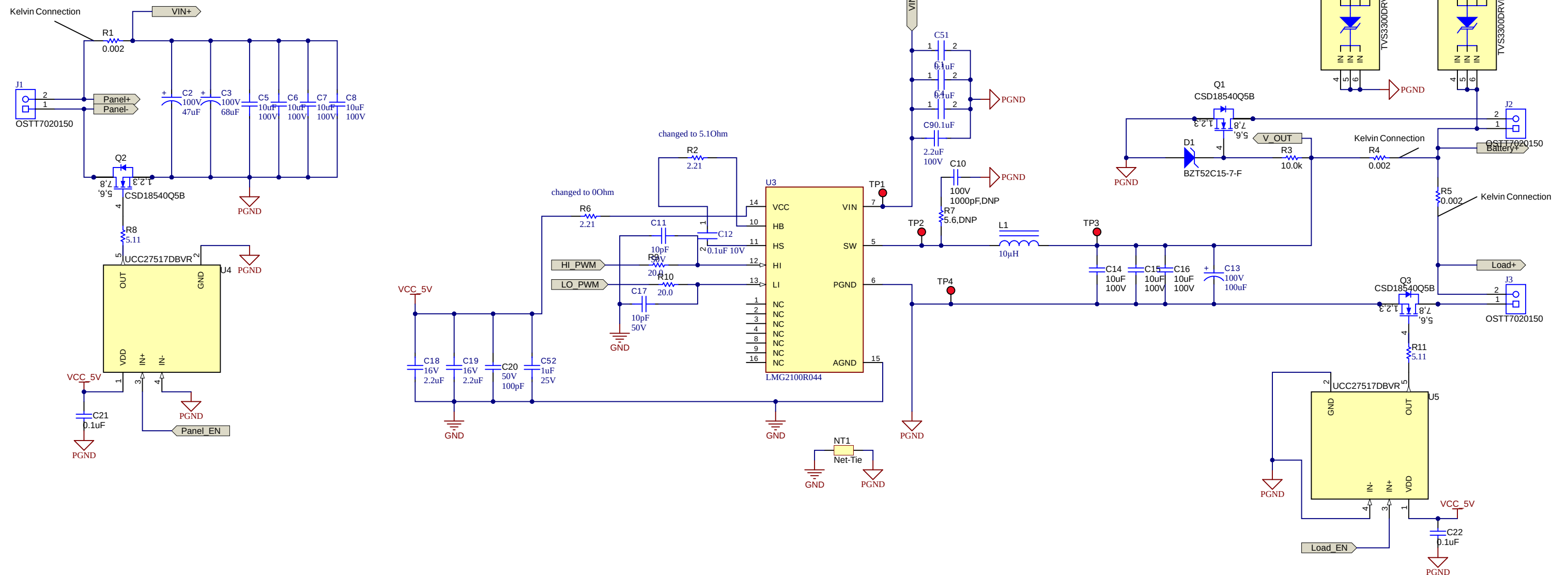
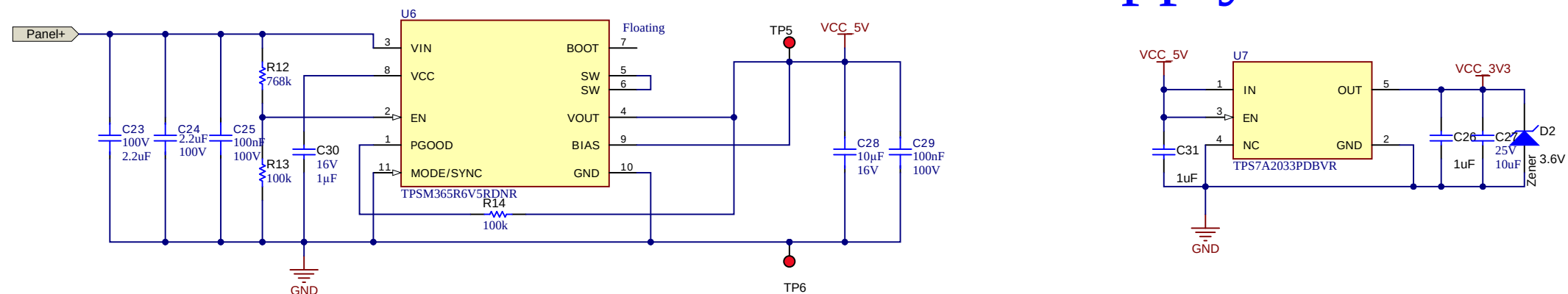


Main Circuit



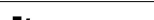
Power Supply



Pin2: Precision enable high level for VOUT: 1.26V typ

MODE connect to GND:Auto Mode

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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 10/14/2023	
TID #: TIDA-010042	Project Title: TIDA-010042		
Number: TIDA-010042 Rev: E3	Sheet Title:		
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 3	
Drawn By: Bowen	File: Power Stage_SchDoc	Size: B	
Engineer: Bowen Ling	Contact: http://www.ti.com/support		http://www.ti.com © Texas Instruments 2023

MSPM0G1506

A

B

C

D

1

2

2

3

3

4

4

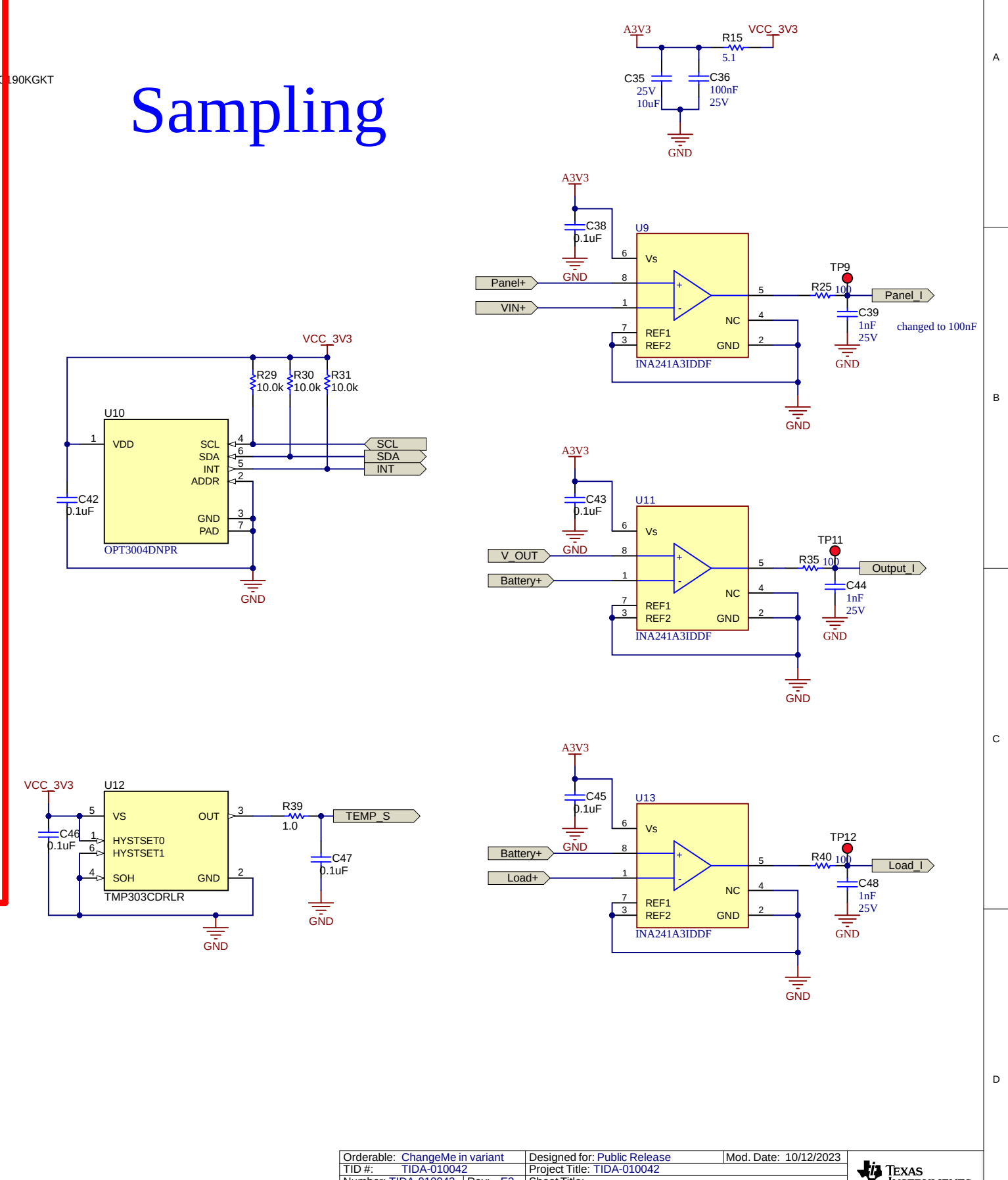
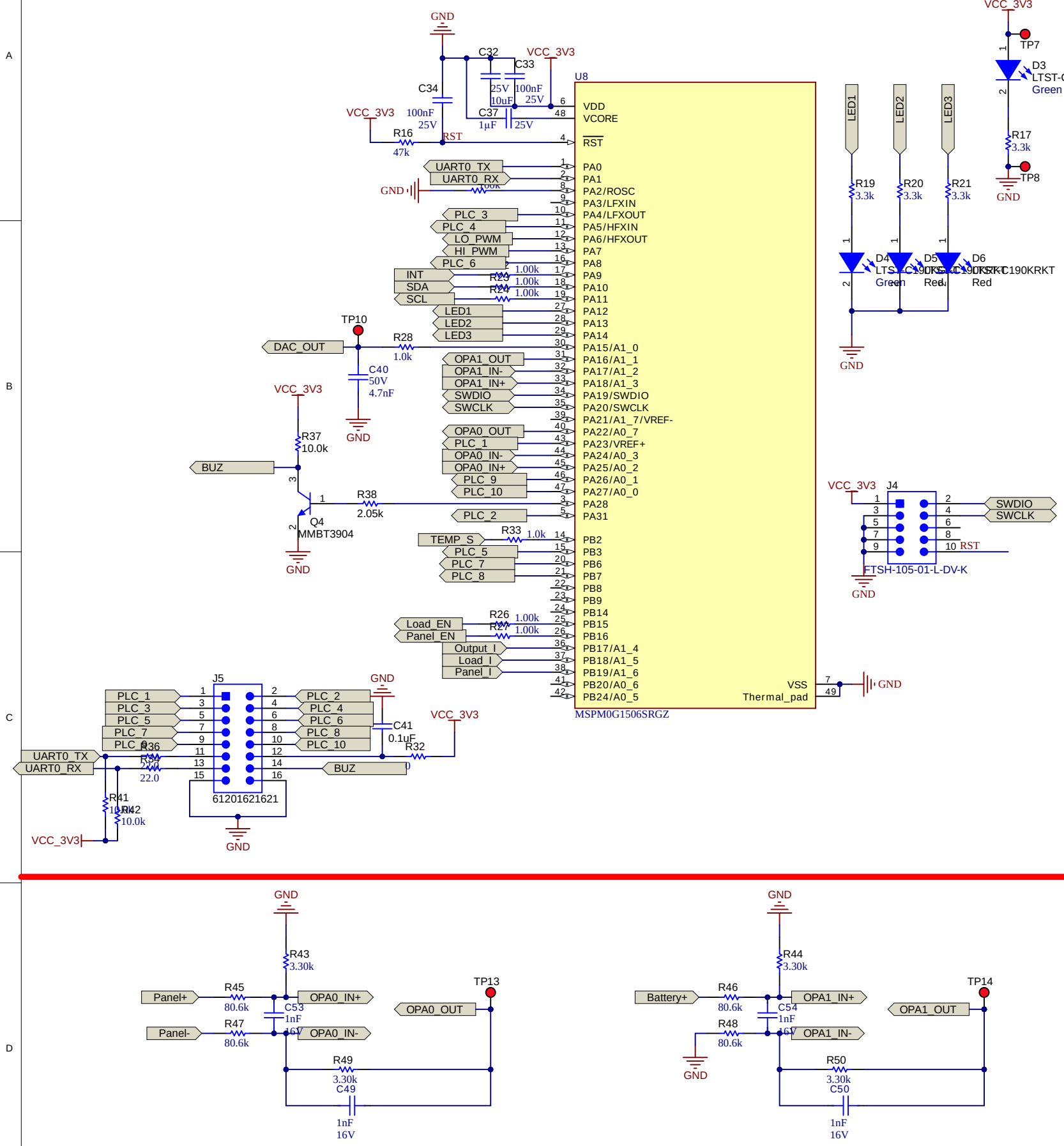
5

5

6

6

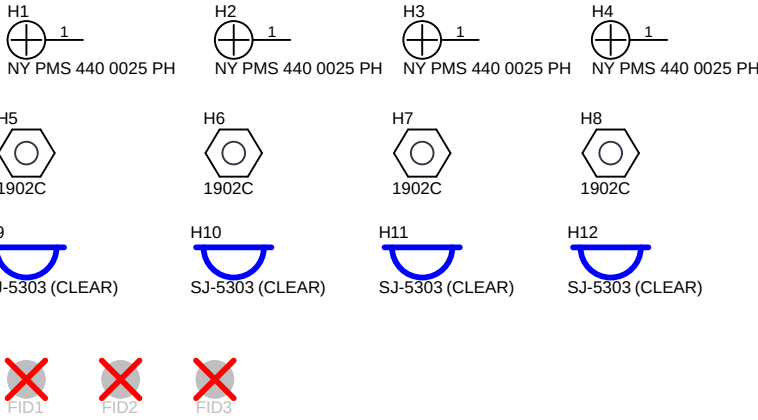
Sampling



Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 10/12/2023
TID #: TIDA-010042	Project Title: TIDA-010042	
Number: TIDA-010042	Rev: E3	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 3
Drawn By: Bowen	File: Control and Sample.SchDoc	Size: B
Engineer: Bowen Ling	Contact: http://www.ti.com/support	

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LBL1

PCB Label

THT-14-423-10
Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

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