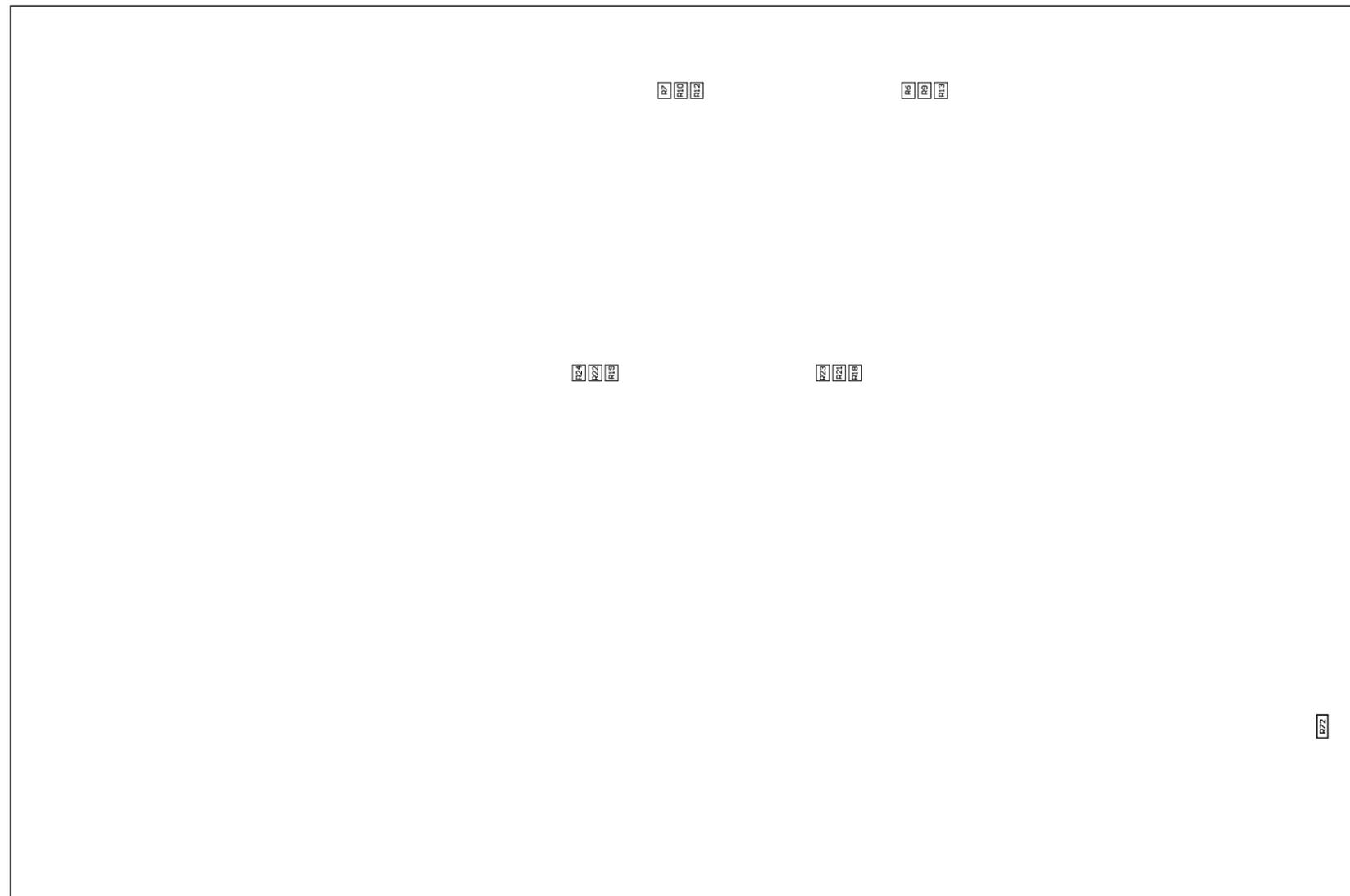


8 7 6 5 4 3 2 1

REVISIONS			
REV.	DESCRIPTION	DATE	APPROVED



Secondary Side

Notes:  
 1) Refer to BOM for stuffing options.  
 2) Observe polarity on diodes and capacitors.

APPLICATION		CONTRACT NO.	DWN: www.WTSCORP.com	10/15	Texas Instruments, Inc.				
USED ON	NEXT ASSY.	UNLESS OTHERWISE SPECIFIED 1. TOLERANCES: LINEAR = : 0.26 (.010) ANGULAR = : 0.50 ° 2. ALL DIMENSIONS ARE IN MM (INCHES). 3. INTERPRET DRAWING IN ACCORDANCE WITH DOD-STD-100.	CHK:		TITLE: ASSEMBLY DRAWING, HV-1PH-DCAC-R3				
			MATERIAL: SEE NOTE FINISH: SEE NOTE	P.E.:		SIZE D	CAGE CODE NO.	DRAWING NO. HV-1PH-DCAC	REV: 3
				DO NOT SCALE DRAWING	DES.ENG.:				
			P.C.:						
			Q.C.:						
			C.M.:						
			REL:						
			MFG:						
			ENG.MGR:						
			OTHER:						
			OTHER:						
			OTHER:						

8 7 6 5 4 3 2 1