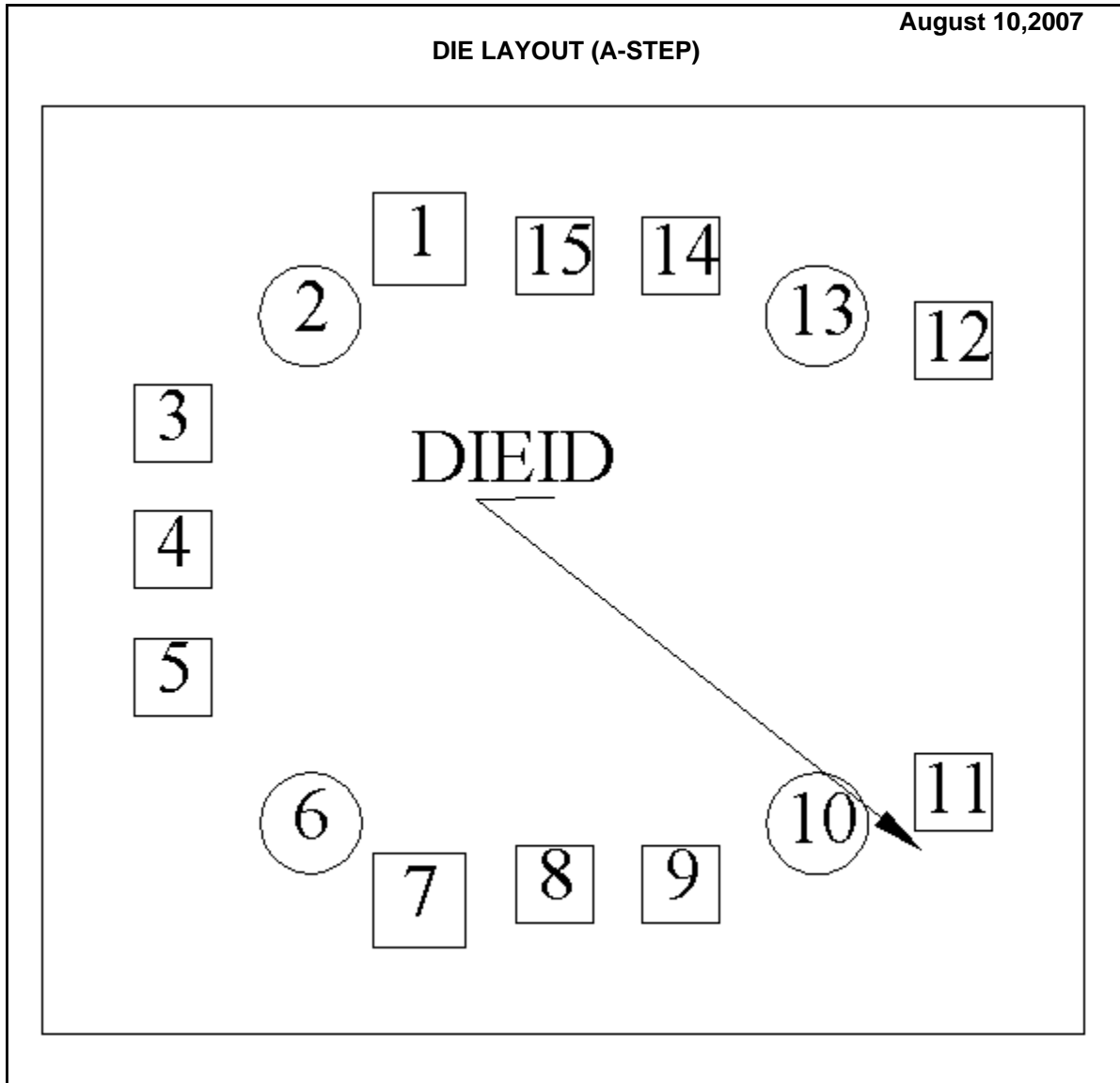


**LP3984I-1.5 MDC MWC  
MICROPOWER, 150MA ULTRA LOW-DROPOUT CMOS VOLTAGE REGULATOR**



**DIE/WAFER CHARACTERISTICS**

<b>Fabrication Attributes</b>		<b>General Die Information</b>	
Physical Die Identification	LP3984A-1.5	Bond Pad Opening Size (min)	92µm x 92µm
Die Step	A	Bond Pad Metalization	0.5% COPPER_BAL. ALUMINUM
<b>Physical Attributes</b>		Passivation	PECVD OX+NITRIDE
Wafer Diameter	200mm	Back Side Metal	BARE BACK
Die Size (Drawn)	1029µm x 914µm 40.5mils x 36.0mils	Back Side Connection	GND
Thickness	254µm Nominal		
Min Pitch	107µm Nominal		

**Special Assembly Requirements:**

**Note: Actual die size is rounded to the nearest micron.**

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Die Bond Pad Coordinate Locations (A -Step)

(Referenced to die center, coordinates in  $\mu\text{m}$ ) **NC** = No Connection, **N.U.** = Not Used

SIGNAL NAME	PAD# NUMBER	XY COORDINATES		PAD SIZE	
		X	Y	X	Y
V <sub>IN</sub>	1	-142	326	92	x 92
V <sub>IN</sub>	2	-250	250	100	x 100
NC	3	-385	144	76	x 76
NC	4	-385	19	76	x 76
NC	5	-385	-106	76	x 76
V <sub>OUT</sub>	6	-250	-250	100	x 100
V <sub>OUT</sub>	7	-142	-326	92	x 92
NC	8	-9	-310	76	x 76
NC	9	116	-310	76	x 76
GND	10	250	-250	100	x 100
NC	11	385	-220	76	x 76
NC	12	385	225	76	x 76
V <sub>EN</sub>	13	250	250	100	x 100
NC	14	116	310	76	x 76
NC	15	-9	310	76	x 76

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