

Effects of External Pullup and Pulldown Resistors on TXS and TXB Devices

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ABSTRACT

The TXS and TXB family of devices belong to the auto-bidirectional translation products from TI. These devices can be sensitive to external pullup or pulldown resistors due to the internal pullup resistors present in the TXS device and the internal serial resistors present in the TXB device. In this application note, the effects of external pullup and pulldown resistors on V_{OL} and V_{OH} levels of the TXS and TXB family of devices are examined. As external pullup or pulldown resistors impact the way the output behaves in a design, a high value of pullup or pulldown resistors greater than 50 k Ω is beneficial to prevent performance degradation.

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1 **External Resistors on TXS and TXB Translators**

1.1 Terminology

V_{OL} – The voltage level at the output when driving logic low. For more detailed information, see the application note on Understanding and interpreting Standard -logic data sheets

 V_{OH} – The voltage level at the output when driving logic high. For more detailed information, see the application note on Understanding and interpreting Standard -logic data sheets

TXS - TI auto bidirectional translation switch type device family. For more detailed information, see the application note on A Guide to Voltage Translation With TXS-Type Translators

TXB - TI auto bidirectional translation buffered type device family. For more detailed information, see the application note on A Guide to Voltage Translation With TXB-Type Translators

R_{PD} - Pulldown resistor

R_{PII}- Pullup resistor

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1.2 TXS Pullup Resistor Analysis

The TXS family of translators incorporates internal pullup resistors designed to hold the output when driving logic high. The internal pullup resistor is fixed at 10 k Ω for the TXS0101, TXS0102, and TXS0104E translators, whereas the TXS0108E translator has dynamic pullup resistors that change value depending on whether the output is driving a high or low. When driving a high, the pullup resistor value is 4 k Ω and when driving a low, the pullup resistor value is 40 k Ω . See Figure 1 for a simplified diagram of the output configuration.

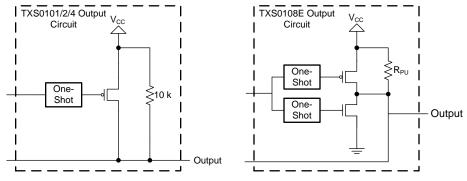


Figure 1. TXS010x Simplified Output Circuit

The one-shot circuit is designed to momentarily increase the drive strength on a transitioning edge. When in a DC steady state, the output is held high by the internal pullup resistor. Due to this, adding external pullup or pulldown resistors can impact the way the output behaves. To demonstrate the resulting changes, the output of the TXS0108E is measured across four different pullup and pulldown resistor values ranging from 4.7 k Ω to 100 k Ω . See Figure 2 for the test setup.



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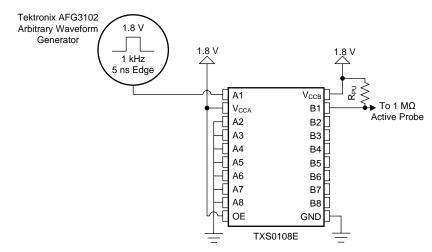


Figure 2. TXS0108E Pullup Test Setup

Input A1 uses a 1-kHz signal at 1.8 V with 5-ns rising and falling edges. The output, B1, is pulled up to 3.3 V through a pullup resistor and measured using a 1-M Ω active probe. All other input channels are grounded.

Figure 3 shows the results. Channel 1 (purple) represents the input signal and Channel 3 (green) represents the output signal. The corresponding pullup resistor value is shown in the upper left corner of each oscilloscope screenshot.



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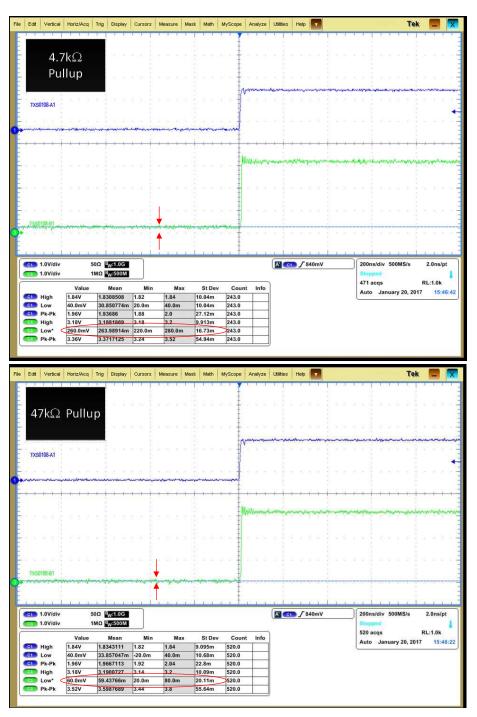


Figure 3. TXS0108E Pullup Resistor Output

The results in Figure 3 illustrate the changes that pullup resistors have on the V_{OL} levels of TXS type translators. The baseline VoL with no pullup resistor is 30 mV compared to the VoL of 264 mV using a 4.7 $k\Omega$ pullup resistor due to the parallel combination of the external pullup resistor with the internal 40 k Ω while driving low. This is attributed to the additional current through the pass transistor, resulting in a larger voltage drop across the pass transistor.

 R_{EQ} = (40 × 4.7) / (40 + 4.7) = 4.2 kΩ; where R_{EQ} is the equivalent resistor due to the parallel combination of the external pullup resistor with the internal 40 k Ω while driving low.



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Adding a strong external pullup resistor increases the current seen at that I/O port because of the reduced parallel resistance of the internal pullup and the external pullup. The result is that the increase in V_{OL} is dependent on both the current sinking capability of the external driving device and the resistance of the internal pass transistor. See Table 1 for a summary of the pullup results.

RESISTOR VALUE (kΩ)	V _{oL} (V)	V _{он} (V)
No external resistor	0.029	3.18
4.7	0.264	3.19
9.8	0.169	3.19
47	0.059	3.19
100	0.038	3.19

Table 1. TXS0108E Pullup Results Summary

1.2.1 TXS Pulldown Resistor Analysis

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The output of the TXS0108E is also measured across four different pulldown resistor values ranging from 4.7 k Ω to 100 k Ω . See Figure 4 for the test setup.

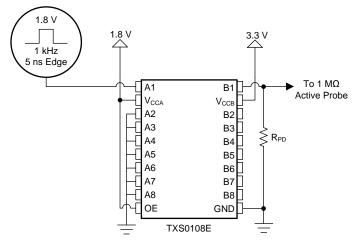


Figure 4. TXS0108E Pulldown Test Setup

Input A1 uses a 1-kHz signal at 1.8 V with 5-ns rising and falling edges. The output, B1, is pulled down to ground through a pulldown resistor and measured using a 1-M Ω active probe. All other input channels were grounded.

Figure 5 shows the results. Channel 1 (purple) represents the input signal and Channel 3 (green) represents the output signal. The pulldown resistor value used is shown in the upper left corner of each oscilloscope screenshot.

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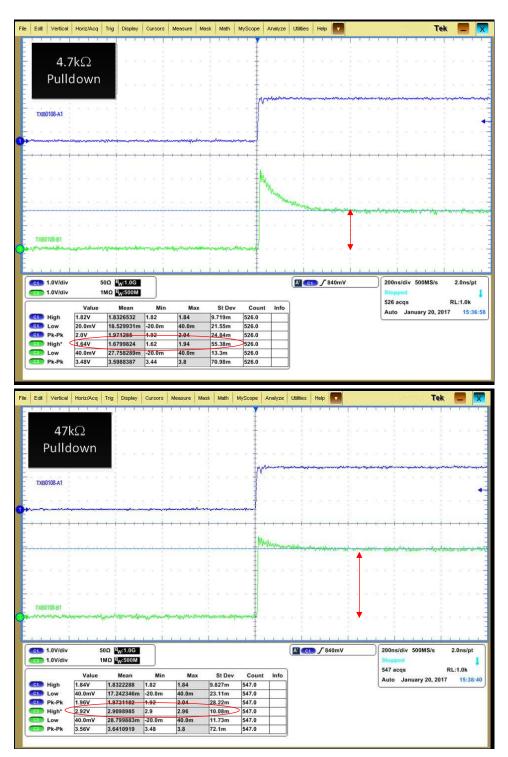


Figure 5. TXS0108E Pulldown Resistor Output

The results in Figure 5 illustrate the changes that pulldown resistors have on the V_{OH} levels of TXS type translators. The baseline V_{OH} with no pullup resistor is 3.18 V compared to the V_{OH} of 1.68 V using a 4.7-k Ω pulldown resistor.



External Resistors on TXS and TXB Translators

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The internal pullup resistor and the external pulldown resistor creates a voltage divider network, which causes a decrease in V_{OH} . The negative impact of pulldown resistors on V_{OH} demonstrates why the TXS family of translators must only be used to drive high-impedance loads. See Table 2 for a summary of the results.

RESISTOR VALUE (kΩ)	V _{oL} (V)	V _{он} (V)
No external resistor	0.029	3.18
4.7	0.028	1.68
9.8	0.027	2.18
47	0.029	2.91
100	0.027	3.04

Table 2. TXS0108E Pulldown Results Summary

1.2.2 Conclusion

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TXS type translators can be used with external pullup resistors without a significant impact on output voltage levels, provided that the driving device is capable of sinking the additional required current to pull the line low and the voltage drop across the internal pass transistor is not significant. Use Equation 1 to estimate the required current sinking capabilities of the driver:

$$I = \frac{V_{CCA}}{R_A} + \frac{V_{CCB}}{R_B}$$

(1)

 R_A and R_B are equal to the equivalent parallel resistance of the external pullup resistor and the internal pullup resistor. For the TXS0101, TXS0102, and TXS0104E devices, the internal pullup resistance is equal to 10 k Ω when driving low. For the TXS0108E, the internal pullup resistance is equal to 40 k Ω when driving low. TI recommends that this current be limited to values under 1 mA for the TXS0108E and 10 mA for the TXS0101, TXS0102, and TXS0104E.

pulldown resistors must be avoided because of decreased V_{OH} levels. If pulldown resistors are necessary they must be limited to values of 50 k Ω or greater. The negative impact of pulldown resistors on V_{OH} levels also demonstrates why the TXS family of translators must only be used to drive high-impedance loads.



External Resistors on TXS and TXB Translators

1.3 TXB Pullup and Pulldown Resistor Analysis

The TXB family of translators is designed to drive high-impedance loads, with the output driven by a 4-k Ω buffer when in a DC steady state. In this section, the impact of external resistors on V_{OH} and V_{OL} levels of TXB translators is examined. Figure 6 shows a simplified diagram of the TXB010x output.

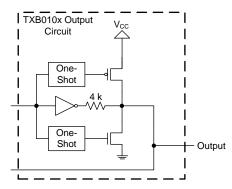


Figure 6. TXB010x Simplified Output Circuit

If strong external pullup or pulldown resistors are added, a resistor divider network is formed with the 4-k Ω buffer resulting in adverse changes in V_{OH} and V_{OL} levels. The output of the TXB0108 is measured across four different pullup and pulldown resistor configurations ranging from 4.7 k Ω to 100 k Ω . Figure 7 shows the test setup for the pullup resistor analysis.

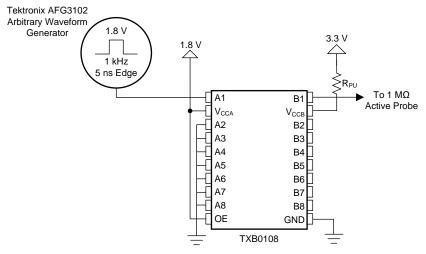


Figure 7. TXB0108 Pullup Test Setup

Input A1 uses a1-kHz signal at 1.8 V with 5-ns rising and falling edges. The output, B1, is pulled up to 3.3 V through a pullup resistor and measured using a 1-M Ω active probe. All other input channels are grounded.

Figure 8 shows the results. Channel 1 (green) represents the input signal and Channel 3 (purple) represents the output signal. The corresponding pullup resistor value is shown in the upper left corner of each oscilloscope screenshot.

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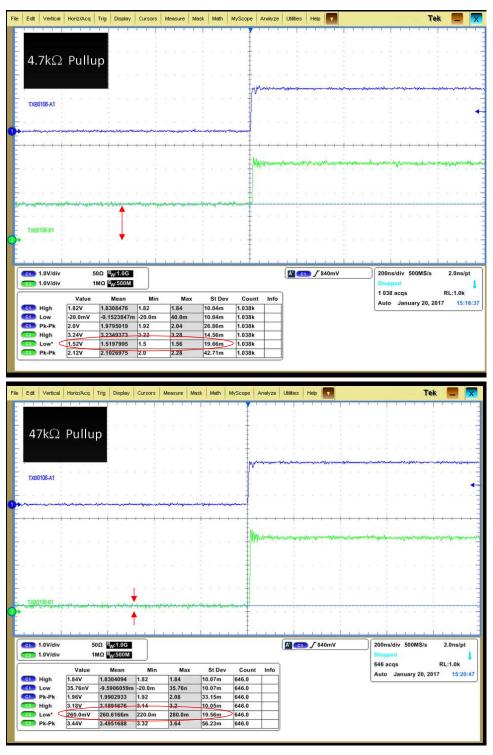


Figure 8. TXB0108 Pullup Resistor Output

The results in Figure 8 illustrate the impact that pullup resistors have on the V_{OL} levels of TXB type translators. The baseline V_{OL} with no pullup resistor is –6.8 mV compared to the V_{OL} of 1.52 V using a 4.7-k Ω pullup resistor. See Table 3 for a summary of the results.

RESISTOR VALUE (k Ω)	V _{oL} (V)	V _{он} (V)	
No External Resistor	-0.007	3.19	
4.7	1.52	3.23	
9.8	0.977	3.21	
47	0.26	3.19	
100	0.11	3.19	

1.3.1 TXB Pulldown Resistor Analysis

The output of the TXB0108 is also measured across four different pulldown resistor values ranging from 4.7 k Ω to 100 k Ω . Figure 9 shows the test setup.

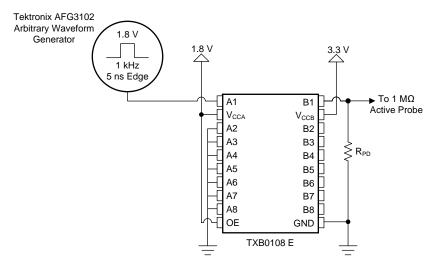


Figure 9. TXB0108 Pulldown Test Setup

Input A1 uses a 1-kHz signal at 1.8 V with 5-ns rising and falling edges. The output, B1, is pulled down to ground through a pulldown resistor and measured using a 1 M Ω active probe. All other input channels are grounded.

Figure 10 shows the results. Channel 1 (green) represents the input signal and Channel 3 (purple) represents the output signal. The pulldown resistor value used is shown in the upper left corner of each oscilloscope screenshot.

File Edit Vertical Horiz/Acq Trig Display Cursors Measure Mask Math MyScope Analyze Utilities Help 🔽 Tek $4.7 \mathrm{k}\Omega$ Pulldown TXB0108-A1 1.0V/div 50Ω By:1.0G 📓 😋 🖌 840mV 200ns/div 500MS/s 2.0ns/pt 1.0V/div 1MΩ ^BW:500M 536 acqs RL:1.0k Value Mean Min Max St Dev Count Info Auto January 20, 2017 15:26:46 High 1.82V 1.8290141 1.82 1.84 10.03m 536.0 CI Low -20.0mV -10.985891m -20.0m 35.76n 10.03m 536.0 CID Pk-Pk 1 9908163 1.92 2.08 536.0 2.0V 31.56m 1.72V 0.0V High* 1.7116021 1.66 1.82 22.61m 536.0 -9.7876011m -20.0m Low 536.0 Pk-Pk 3.8V 3.7415724 3.6 3.92 54.7m 536.0 File Edit Vertical Horiz/Acq Trig Display Cursors Measure Mask Math MyScope Analyze Utilities Help Tek $47 k\Omega$ Pulldown TXB0108-A1 C10 1.0V/div 50Ω Ew:1.0G 📓 😋 🖌 840mV 200ns/div 500MS/s 2.0ns/pt 1MQ 8W:500M 524 acqs RL:1.0k Value Mean Min Max St Dev Count Info Auto January 20, 2017 15:28:20 82968 10.08m 524.0 1.82V 1.84 High C1 Low -20.0mV -10.314179m -20.0m 20.0m 10.08m 524.0 Pk-Pk 1.9851299 1.92 2.12 28.45m 524.0 C1 High* 2.92 2,9115475 2,9 2.96 10.64m 524.0 524.0 -9.0878124m -20.0m 10.04m Low 0.0V 0.0 Pk-Pk 3.56V 3.7866105 3.56 3.96 69.09m 524.0

Figure 10. TXB0108 Pulldown Resistor Output

The results in Figure 10 illustrate the changes that pulldown resistors have on the V_{OH} levels of TXB type translators. The baseline V_{OH} with no pullup resistor is 3.98 V compared to the V_{OH} of 1.71 V using a 4.7-k Ω pulldown resistor. The negative impact of pulldown resistors on V_{OH} levels demonstrates why the TXB family of translators must only be used to drive high-impedance loads. See Table 4 for a summary of the results.

(3)

 Table 4. TXB0108 Pulldown Results Summary

RESISTOR VALUE ($k\Omega$)	V _{oL} (V)	V _{он} (V)	
No External Resistor	-0.007	3.19	
4.7	-0.01	1.71	
9.8	-0.01	2.22	
47	-0.01	2.91	
100	-0.012	3.04	

1.3.2 Conclusion

Pullup and pulldown resistors of less than 50 k Ω must not be used with TXB translators because the internal 4-k Ω buffer and external resistors create a resistor divider network. Equation 2 provides an estimated calculation for the resulting V_{oL} based on the pullup value.

$$V_{OL} = \frac{4 \text{ k}\Omega}{4 \text{ k}\Omega + \text{Rpu}} \times V_{CCO}$$
⁽²⁾

In Equation 2, substitute in the external pullup resistor value for R_{pu} and the V_{CC} voltage at the output port for V_{CCO} to obtain the estimated V_{OL} value.

Similarly, the resulting V_{OH} value based on an external pulldown resistor can be estimated using Equation 3.

$$V_{OH} = \frac{Rpd}{Rpd + 4 \ k\Omega} \times V_{CCC}$$

where

- R_{pd} is the pulldown resistor value
- V_{cco} is the voltage supply of the output port

From these results, only weak pullup or pulldown resistors (> 50 k Ω) must be used with TXB devices. If stronger external pullup resistors are required, refer to the TXS family or LSF family of devices.

2 References and Further Reading

2.1 Relevant Technical Documents

- Basics of Voltage-Level Translation
- Selecting the Right Level-Translation Solution
- Voltage-Level Translation With the LSF Family
- A Guide to Voltage Translation With TXS-Type Translators
- A Guide to Voltage Translation With TXB-Type Translators
- Voltage Translation Between Different Logic Voltage Standards

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