In May 2000 Texas Instruments will release to production the TSB41AB3PFP three-port physical layer (PHY). This part is an upgrade to the current three-port PHYs, the TSB41LV03 (data sheet, literature number SLLS317) and the TSB41LV03A (data sheet, literature number SLLS364).

Texas Instruments recommends that the TSB41LV03 not be considered for new designs, and that all new designs use the TSB41LV03A or the TSB41AB3. Existing designs may continue to use the TSB41LV03/03A, but may want to consider transitioning to the TSB41AB3.

The TSB41AB3 offers advantages over the existing TSB41LV03 and TSB41LV03A:

- The TSB41AB3PFP is pin-compatible with the TSB41LV03PFP and TSB41LV03APFP.
- All items described in the TSB41LV03 errata (literature number SLLS316) have been corrected.
- The TSB41AB3 has lower active and idle power consumption (as much as 90% lower current than the TSB41LV03) and as much as 40% lower current than the TSB41LV03A.
- The TSB41AB3, like the TSB41LV03A, has an ultra low-power mode of less than 150 mA when no ports are active.
- The TSB41AB3, like the TSB41LV03A, is fully 1394a–2000 compliant including the extended TPbias mode to accommodate older model DV camcorder hot-plug connectivity. The extended TPbias is not included in the TSB41LV03 design.
- The TSB41AB3 and the TSB41LV03A both have failsafe circuitry that senses sudden loss of power to the device and disables its ports. This ensures that the device does not load the port of the connected device on the other end of the cable and blocks any leakage path from the port back to the device power plane. The circuit is not present in the TSB41LV03.
- The TSB41AB3 has a 1394a compliant common-mode noise filter on the incoming bias detect circuit to filter out cross-talk noise.
- The TSB41AB3 uses a single, standard value 6.34-kΩ ±1% resistor between R0 and R1 instead of the 6.3-kΩ ±0.5% resistor used in the TSB41LV03/03A device.

The TSB41AB3 and TSB41LV03A are pin-compatible with the TSB41LV03. However, circuit modifications can be made to save components and/or increase functionality with the TSB41AB3 and TSB41LV03A. The changes that can be made are as follows:

- The 128 kΩ resistor on the RESET pin can be removed to reduce power consumption as it is not needed for the TSB41AB3 or the TSB41LV03A. This resistor is required on the TSB41LV03.
- The 1 kΩ resistor to GND on the SE pin is optional. The resistor is recommended on the TSB41AB3 and TSB41LV03A to enable future enhancement.
When connecting the PHY LPS input pin to the link LPS output pin for non-isolated applications, the pullup resistor on the LPS signal is not needed for the TSB41LV03, TSB41LV03A, or the TSB41AB3 devices. The pulldown resistor is required but may be changed from a 1 kΩ resistor for the TSB41LV03 to a resistor ranging from 1 kΩ to 10 kΩ when using the TSB41LV03A or the TSB41AB3. For isolation applications, the TSB41LV03APFP (literature number SLLS364) and the TSB41AB3 (literature number SLLS418) data sheets describe the proper pullup/pulldown combinations.
IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgment, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI’s standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Customers are responsible for their applications using TI components.

In order to minimize risks associated with the customer’s applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI’s publication of information regarding any third party’s products or services does not constitute TI’s approval, warranty or endorsement thereof.

Copyright © 2000, Texas Instruments Incorporated