Audio Tone Control Using The TLC074 Operational Amplifier

Dee Harris

ABSTRACT

This application report describes the design and function of a stereo high-fidelity tone control using a single TLC074 quad operational amplifier. A rail-to-rail operational amplifier is used to provide a midpoint supply voltage and signal ground, allowing the use of a single power supply.

Contents

1 Introduction ................................................................. 2
2 Circuit Description ....................................................... 2
3 Tone Control Frequency Response .................................... 5
4 Summary .................................................................. 7
5 References ................................................................. 7

List of Figures

1 Tone Control BLock Diagram ........................................... 2
2 Tone Control Composite Frequency Response Range ............... 3
3 Audio Tone Control Schematic Diagram ................................ 4
4 Bass and Treble Tone Control Response ................................ 5
5 Maximum Bass and Treble Boost ....................................... 6
6 Bass and Treble Flat ....................................................... 6
7 Maximum Bass and Treble Cut ......................................... 7
1 Introduction

Tone controls allow the frequency response of the audio system to be adjusted to compensate for the response of speakers and their enclosures or the listening room, or to simply provide a more pleasing sound.

In this design, a variation of the classic and very popular Baxandall\(^1\), negative feedback tone control circuit provides the familiar Hi-Fi tone control with cut...flat...boost response for both bass and treble frequencies. A single bass control adjusts both channels simultaneously and a single treble control adjusts both channels.

A pair of slide pots adjusts the volume of each channel independently. The circuit provides a gain of 6 dB at the maximum volume setting when both tone controls are at their midpoints (flat).

2 Circuit Description

Each of the two separate channels of the tone control circuit is basically an active filter built around an IC operational amplifier. An active filter design was chosen over a passive filter circuit because active filters have the frequency-response adjusting components located in the feedback loop of the filter amplifiers, providing much lower THD, little or no insertion loss, and a symmetrical response about the axis in both boost and cut, compared with most passive designs. Each channel also includes an input buffer amplifier to provide some gain and isolation from source impedance variations.

A block diagram of the right channel of the tone control circuit is shown in Figure 1. The left channel is identical.

![Figure 1. Tone Control Block Diagram](image)

One dual-element slide pot adjusts the bass response from approximately –20 dB of cut, to flat, to approximately 20 dB of boost for both channels simultaneously. Another dual-element slide pot adjusts the treble response of both channels through the same range.

Mid-range frequencies are not affected by the tone controls. An overall flat response (no boost or cut at frequency extremes) is obtained when the tone controls are at their mid-point position.

The composite frequency response range curves shown in Figure 2 are provided by the component values indicated in the schematic (Figure 3).
A single TLC074 quad operational amplifier IC contains all the amplifiers required for buffering and filtering both left and right channels. A TLV2461 operational amplifier IC is connected to provide a virtual ground for proper operation of the TLC074 from a single supply voltage.

Figure 3 shows the tone control schematic diagram.
The input buffer amplifier provides a gain of approximately 2 (RF/RIN) with the specified resistor values. Input capacitor C1 blocks dc and sets the minimum low-frequency response of the tone control circuit at approximately 16 Hz (–3 dB) with the value of 2.2 μF. Volume control R1 has an audio taper to provide a perceived linear response in volume, proportional to the physical position of the slider. The adjustment range of the buffer amplifier is from 0 V to approximately 2 times (6 dB) the audio signal input voltage.
The tone adjusting action in each channel of the tone control circuit is provided by an equalizing amplifier (or active filter) created by placing a frequency-dependent negative feedback network around an operational amplifier. Almost any overall gain-versus-frequency characteristic can be defined by the design of the feedback network.

3 Tone Control Frequency Response

The overall tone control circuit frequency response can be shifted up or down by changing the values of capacitors C7, C9, C11, and C12 in the tone adjusting networks.

To shift the frequency response downward, for example, increase the values of the capacitors in the tone adjusting networks. Doubling the values of C7, C9, C11, and C12 shifts the break frequency downward a full octave (Case B, Figure 4). Conversely, halving the values of C7, C9, C11, and C12 shifts the break frequency upward a full octave. (1 octave up = \( \frac{1}{2} C \)) and 1 octave down = \( 2 \frac{1}{2} C \).)

![Figure 4. Bass and Treble Tone Control Response](image)

Note that to keep the boost and cut break frequencies the same, the value of C7 must equal that of C9, and the value of C11 must equal that of C12. In addition, although the bass and treble break frequencies can be adjusted separately if desired, to maintain the overall shape and symmetry of the response, all four capacitors must be increased or decreased by the same factor.
Measured Performance

Figures 5, 6, and 7 are measurements from the TI Tone Control EVM (SLOP109), using the TLC074 IC amplifier. All measurements taken with Vdd = 5 V, Vin = 100 mVrms, RL = 47 kΩ, and f = 3 kHz.

Figure 5. Maximum Bass and Treble Boost

Figure 6. Bass and Treble Flat
4 Summary

The familiar Baxandall Hi-Fi tone control is updated from vacuum tubes with modern high-performance operational amplifiers. Two channels (for stereo) can be implemented using only two very small IC packages and a few small passive components. This design places the frequency determining components in the feedback loop of an operational amplifier, reducing distortion and insertion loss, and providing symmetrical boost and cut responses.

5 References

IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE (“CRITICAL APPLICATIONS”). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER’S RISK.

In order to minimize risks associated with the customer’s applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 2000, Texas Instruments Incorporated