Selecting High-Speed Operational Amplifiers Made Easy

Bruce Carter

ABSTRACT

High-speed op amps come in current- and voltage-feedback versions. Voltage-feedback amplifiers are sometimes de-compensated to allow greater speed, but this technique has been superceded by current-feedback amplifiers. Differential-output op amps are gaining popularity for transformerless A/D interfaces.

Contents

1 Introduction ................................ ................................ ................................ ................................ ... 2
2 Voltage Feedback and Current Feedback................................ ................................ .................... 2
  2.1 Voltage-Feedback Op Amps ................................ ................................ ................................ ..... 2
  2.2 Current-Feedback Op Amps ................................ ................................ ................................ ..... 3
  2.3 Applying Current-Feedback Op Amps ................................ ................................ ....................... 3
3 Compensated vs Uncompensated Op Amps................................ ................................ ............... 6
4 Differential vs Single-Ended Op Amps ................................ ................................ ........................ 9
5 Putting It All Together: Selecting the Right High-Speed Op Amp................................ ............ 10
  5.1 Any Differential Load................................ ................................ ................................ ............... 10
  5.2 High Speed ............................................................................................................................. 10

Figures

1 Ideal Voltage-Feedback Op Amp ..........................................................................................2
2 Ideal Current-Feedback Op Amp .........................................................................................3
3 Gain Stage Topology for Voltage and Current-Feedback Op Amps....................................4
4 Use of a Current-Feedback Op Amp as a Unity Gain Buffer ...........................................4
5 Low Pass Filter With a Current Feedback Op Amp .............................................................5
6 Two-Pole Low Pass Filter With Current Feedback Op Amps ..............................................5
7 Effect of Capacitance at the Inverting Input ................................ ................................ ..........6
8 Load Capacitance Causes Instability ..................................................................................7
9 Load Capacitance Can Be Isolated With a Resistor .............................................................7
10 Lead Compensation for Load Capacitance ........................................................................8
11 Lead Compensation for Inverting-Input Capacitance ..........................................................8
12 Traditional Method of Interfacing to Differential-Input A/D Converters ............................9
13 Improved Differential A/D Interface ................................................................................10

Tables

1 Voltage- and Current-Feedback Characteristics ..................................................................3
2 High-Speed Op Amp Parameters ......................................................................................12
1 Introduction

The availability of high-speed operational amplifiers (op amps) is relatively recent. Most designers are either unaware of the differences between high- and low-speed op amps, or are intimidated by them. This note is written to make the basic concepts of high-speed op amps easy to understand. It is intended as a guide for designers who must specify and use a high-speed op amp.

2 Voltage Feedback and Current Feedback

There are two basic types of high-speed op amp, voltage and current feedback. A brief review of these types of op amps follows.

2.1 Voltage-Feedback Op Amps

The voltage-feedback op amp is familiar to designers. Its ideal form is shown in Figure 1.

![Ideal Voltage-Feedback Op Amp](image)

The gain of an ideal voltage-feedback op amp is given by:

\[ V_o = a \times V_e \]

where \( V_o \) is the output voltage, \( a \) is the open-loop voltage gain, and \( V_e (=V_p - V_n) \) is the error voltage.

Although no op amp is ideal, modern processing techniques yield devices that come close, at least in some parameters. This is by design. In fact, different op amps are optimized to be close to ideal for some parameters, while other parameters for the same op amp may be quite ordinary. It is a trade-off. Some parameters can be improved, but only at the expense of others. It is the designer’s function to select the op amp that is closest to ideal in ways that matter to the application, and to know which parameters can be discounted or ignored.

When dealing with high-speed op amps, the parameters that are of most interest to the designer are bandwidth and slew rate. In voltage-feedback amplifiers, bandwidth is inversely related to gain over the normal operating range of the op amp. This linear relationship is a \(-20\, \text{dB/dec roll-off}\) caused by an internal compensation capacitor. This capacitor was introduced to solve a problem that has plagued voltage-feedback op amps from the time they were first introduced, namely, instability (i.e., the tendency to oscillate). The technique works well, and it has allowed the production of many varieties of voltage-feedback op amps over the years. Unfortunately, the compensation capacitor also limits the response of the op amp at high frequencies.
2.2 Current-Feedback Op Amps

There is much confusion amongst designers as to exactly what a current-feedback op amp is. The term *current feedback* refers to the internal construction of the op amp, not to something external to it. The ideal current-feedback op amp is shown in Figure 2.

![Ideal Current-Feedback Op Amp](image)

**Figure 2. Ideal Current-Feedback Op Amp**

The gain of an ideal current-feedback op amp is given by:

\[ V_o = i_e Z_t \]

where \( V_o \) is output voltage, \( i_e \) is the error current, and \( Z_t \) is the open-loop transimpedance gain.

It can be seen in Table 1 that the slew rate of a current feedback op amp is more than an order of magnitude greater than that of a voltage feedback op amp.

<table>
<thead>
<tr>
<th>CHARACTERISTIC</th>
<th>VOLTAGE FEEDBACK</th>
<th>CURRENT FEEDBACK</th>
</tr>
</thead>
<tbody>
<tr>
<td>Part number</td>
<td>THS4001</td>
<td>THS3001</td>
</tr>
<tr>
<td>Bandwidth (MHz)</td>
<td>300</td>
<td>420</td>
</tr>
<tr>
<td>Slew rate (V/µs)</td>
<td>400</td>
<td>6500</td>
</tr>
<tr>
<td>Settling time (ns)</td>
<td>30</td>
<td>40</td>
</tr>
<tr>
<td>I/O (mA)</td>
<td>100</td>
<td>100</td>
</tr>
</tbody>
</table>

Op amp design is a series of trade-offs and an improvement in one parameter is always accompanied by degradation of others. This is certainly the case with current-feedback amplifiers where, to a degree, dc performance has been sacrificed to gain speed.

2.3 Applying Current-Feedback Op Amps

It is important to resolve misconceptions about how to use current-feedback amplifiers. The average designer does not know how to build a gain stage with a current-feedback op amp. Usually there is the assumption that something esoteric is done with the traditional, familiar, gain topologies. This is not true. In fact, as shown in Figure 3, *there is no difference!*
There are several do's and do not's for current-feedback op amps: first, the do not's:

- Do not use a current-feedback op amp as a traditionally configured unity gain buffer (output connected directly to inverting input)! Correct and incorrect unity gain buffers are shown in Figure 4:

Figure 3.  Gain Stage Topology for Voltage and Current-Feedback Op Amps

Figure 4.  Use of a Current-Feedback Op Amp as a Unity Gain Buffer
• Do not connect a capacitor from the output of a current-feedback op amp to the noninverting input. Designers will often do this when trying to implement a low-pass filter. Correct and incorrect ways to implement a low-pass filter with a current-feedback op amp are shown in Figure 5.

![Figure 5. Low Pass Filter With a Current Feedback Op Amp](image)

The correct way to implement a two-pole filter is to use the Sallen Key topology, as shown in Figure 6, adding the recommended feedback resistor for the current feedback amplifier.

![Figure 6. Two-Pole Low Pass Filter With Current Feedback Op Amps](image)

Along with the do not's, there are some do's:

• Do watch the trace length on the inverting input. Capacitance on this input, formed by parasitic capacitance with other layers and traces, will reduce the phase margin (see Figure 7).
Figure 7. Effect of Capacitance at the Inverting Input

Parasitic capacitance comes primarily from PCB traces. Long or wide traces create capacitance with ground planes and adjacent traces. Keep the inverting input trace short.

- The current-feedback op amp must have a resistor in the feedback loop. This resistor is usually specified in the data sheet for the device. It is chosen to limit overshoot or undershoot in the output amplitude near the maximum operating frequency.

- To change the gain of a current-feedback op-amp circuit, change the input gain resistor, not the feedback resistor.

- Do use correct PCB techniques, including a separate analog ground plane for high-speed op amps.

- Do use correct decoupling techniques. This includes placing a 10 nF or 100 nF ceramic capacitor less than 0.1” from the power supply pin.

3 Compensated vs Uncompensated Op Amps

It is possible to make a high-speed voltage-feedback op amp without a compensation capacitor. Doing so allows the op-amp gain to be semi-independent of bandwidth, but the designer is responsible for externally compensating the op amp.

At present, there are very few uncompensated op amps on the market. Many so-called uncompensated op amps actually have compensation capacitors that are smaller than usual, and thus are decompensated partially, albeit not completely. The user must supply additional compensation, trading bandwidth for stability.
Decompensated op amps have not been widely successful in the market. The newer, current-feedback amplifiers achieve high speeds without sacrificing stability. Voltage-feedback amplifiers with compensation have also become faster than those decompensated devices that are on the market. Consequently, this note covers decompensated op amps in only general terms. In the event that a decompensated op amp is needed, exact component values can be found in the data book\cite{4} and techniques to derive such values have been discussed by Mancini\cite{2}.

All voltage-feedback amplifiers can be made unstable under the right conditions. The techniques described here can usually solve stability problems.

The worst enemy of stability is load capacitance (Figure 8).

![Figure 8. Load Capacitance Causes Instability](image)

Load capacitance is most commonly encountered when interfacing an op amp to a data converter, which can have relatively large variable capacitance at its input. The problem is especially severe in uncompensated (or decompensated) op amps.

![Figure 9. Load Capacitance Can Be Isolated With a Resistor](image)

The usual method of isolating load capacitance is with a small series resistor (Figure 13). However, this also forms an RC low-pass filter with the cutoff frequency of $f_0 = 1/(2\pi RC)$ and, as a consequence, it will limit the frequency response of the circuit and load the amplifier at frequencies above $1/(2\pi RC)$. Many times, the A/D manufacturer will specify the component values for this RC circuit. Follow the manufacturer’s recommendations.

Unless connection points are supplied to the internal op-amp compensation points, there are very few places at which the designer can apply compensation. The output terminal, as shown in Figure 9, is one such place. Another way to compensate an unstable voltage-feedback op amp is through lead compensation, as shown in Figure 10.
The concept is to balance the phase lag caused by the load capacitance with a phase lead from the feedback capacitance so that the two capacitors effectively cancel each other. Frequency response can actually increase with the correct value of the feedback capacitor. Direct calculation is beyond the scope of this note and, indeed, laboratory experimentation may produce the quickest results. Note that the lead capacitor also works in a noninverting gain circuit.

Inverting-input capacitance can cause instability in both voltage- and current-feedback op amps. The problem is especially severe in op-amp circuits with gains of less than one (inverting attenuator circuits). The lead-capacitor technique can also be used to compensate for input capacitance on the inverting input, as shown in Figure 11.

Both load and inverting-input capacitance can contribute to instability, so the lead capacitor should be selected to compensate for both.

Note that current-feedback amplifiers are much less tolerant of capacitors in the feedback loop. Place a resistor in series with the feedback capacitor.
4 Differential vs Single-Ended Op Amps

Differential op amps have been increasing in popularity in recent years. Differential-output op amps actually revive a design concept almost 50 years old. The original vacuum-tube-based op amp, the K2-W, was a differential-output device. The primary application for differential-output op amps is interfacing to differential-input A/D converters.

![THS3201 schematic](image)

**Figure 12. Traditional Method of Interfacing to Differential-Input A/D Converters**

Figure 12 shows how differential A/D converters have been interfaced in the past. The differential inputs of the A/D converter are connected through a transformer to a single-ended op amp. There is nothing inherently wrong with this approach, but it does have some disadvantages:

- It does not take advantage of the noise rejection possible with differential circuitry.
- The circuit does not include dc in the frequency response. By definition, transformer T1 isolates dc and limits the ac response of the circuit.
Figure 13. Improved Differential A/D Interface

Figure 13 shows an improved A/D Interface. A differential-output op amp, the THS 4140, has been substituted for the single-ended op amp. This allows the transformer to be eliminated. The main advantage of this circuit is that it includes dc in the frequency range; in other words, it is dc accurate.

5 Putting It All Together: Selecting the Right High-Speed Op Amp

With this background, a designer now should be in a better position to make a good decision about which high-speed op amp is right for a given application. In this section, some suggestions are made about which particular op amp to select. The selections are not intended to be exclusive because (a) any application may contain additional constraints that would lead to a better choice, and (b) new devices will become available after publication of this note.

5.1 Any Differential Load

For differential loads, use THS413X, THS414X, or THS415X.

5.2 High Speed

Table 2 is a guide for the application of high-speed op amps. The best parameters are in bold face type. If your application needs:

- A current-feedback op amp: Use 3001
- A dual op amp: All op amps except for the 3001 and 4001 are available in dual packages.
• Offset trimming: The op amps listed that are available as single devices have nulling inputs, except the THS3001.

• To operate from a single 5-V supply: Use 4001.

• To drive capacitive loads: Use the 404X. Any op amp can do this if compensated correctly, however.

• For video applications: Use 4011.

• Low noise: Use 3001, 402X, or 403X.

• Fast settling time: Use 4011.

• Low THD: Use 3001.

• High load drive: Use 6002, 6012, 6022, or 6032.

• Low quiescent power consumption: Use 408X.
### Table 2. High-Speed Op Amp Parameters

<table>
<thead>
<tr>
<th>Parameter</th>
<th>THS3001</th>
<th>THS4001</th>
<th>THS4011/2</th>
<th>THS4021/2</th>
<th>THS4031/2</th>
<th>THS4041/2</th>
<th>THS4051/2</th>
<th>THS4061/2</th>
<th>THS4081/2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Speed (MHz)</td>
<td>420</td>
<td>270</td>
<td>290</td>
<td>350</td>
<td>100</td>
<td>165</td>
<td>70</td>
<td>180</td>
<td>175</td>
</tr>
<tr>
<td>Slew rate V/µs</td>
<td>6500</td>
<td>400</td>
<td>310</td>
<td>470</td>
<td>100</td>
<td>400</td>
<td>240</td>
<td>400</td>
<td>230</td>
</tr>
<tr>
<td>Settling time (ns)</td>
<td>40</td>
<td>40</td>
<td>37</td>
<td>40</td>
<td>60</td>
<td>120</td>
<td>60</td>
<td>40</td>
<td>43</td>
</tr>
<tr>
<td>Differential gain (%)</td>
<td>0.01</td>
<td>0.04</td>
<td>0.006</td>
<td>0.02</td>
<td>0.02</td>
<td>0.01</td>
<td>0.01</td>
<td>0.02</td>
<td>0.01</td>
</tr>
<tr>
<td>Differential phase angle (Deg.)</td>
<td>0.02</td>
<td>0.15</td>
<td>0.01</td>
<td>0.08</td>
<td>0.03</td>
<td>0.01</td>
<td>0.01</td>
<td>0.02</td>
<td>0.05</td>
</tr>
<tr>
<td>Output drive (mA)</td>
<td>100</td>
<td>100</td>
<td>110</td>
<td>100</td>
<td>90</td>
<td>100</td>
<td>100</td>
<td>115</td>
<td>85</td>
</tr>
<tr>
<td>Distortion dBC 1 MHz, 150</td>
<td>−96</td>
<td>−72</td>
<td>−80</td>
<td>−68</td>
<td>−72</td>
<td>−75</td>
<td>−82</td>
<td>−72</td>
<td>−64</td>
</tr>
<tr>
<td>Noise nV/rtHz</td>
<td>1.6</td>
<td>12.5</td>
<td>7.5</td>
<td>1.5</td>
<td>1.6</td>
<td>14</td>
<td>14</td>
<td>14.5</td>
<td>10</td>
</tr>
<tr>
<td>Quiescent Current per amplifier (mA)</td>
<td>6.6</td>
<td>7.8</td>
<td>7.8</td>
<td>7.8</td>
<td>8.5</td>
<td>8</td>
<td>8.5</td>
<td>7.8</td>
<td>3.4</td>
</tr>
</tbody>
</table>
References:


3. THS1050/1060 EVM User’s Guide, Texas Instruments Literature Number SLAU044A.

4. High-Speed Amplifier Data Book, Texas Instruments Literature Number SLOD005.
IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgment, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI’s standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Customers are responsible for their applications using TI components.

In order to minimize risks associated with the customer’s applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI’s publication of information regarding any third party’s products or services does not constitute TI’s approval, warranty or endorsement thereof.

Copyright © 2000, Texas Instruments Incorporated