## PMP4079_REVC BOM

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<th>COUNT</th>
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<th>Value</th>
<th>Description</th>
<th>Size</th>
<th>Part Number</th>
<th>MFR</th>
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<tr>
<td>1</td>
<td>C1</td>
<td>1.0uF</td>
<td>Capacitor, Ceramic, 25V, X5R, 20%</td>
<td>0603</td>
<td>C1608X5R1E105M</td>
<td>TDK</td>
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<td>C2, C5</td>
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<td>Std Std</td>
<td>Std</td>
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<td>Header, 2 pin, 100mil spacing, (36-pin strip)</td>
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<td>PTC36SAAN</td>
<td>Sullins</td>
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<td>IC, 1.5A LDO Regulator with Soft-Start</td>
<td>SON-10</td>
<td>TPS74801DRC</td>
<td>TI</td>
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<td>TPS73633DRB</td>
<td>IC, Cap-Free, NMOS, 400mA LDO Regulator With Reverse Current Protection</td>
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<td>IC, 250mA, Low Iq, Wide Bandwidth, LDO Linear Regulators</td>
<td>SOT23-5</td>
<td>TPS73218DBV</td>
<td>TI</td>
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The following test report includes measurements for the following output voltage rails using a 5V input.

This design meets the power sequencing requirements required by OMAP-L137 / C6747 / C6745 / C6743.

Contents

Start-Up Waveform
  o Unloaded
  o Fully Loaded

TPS 74801 – LDO (1.2V@0.66A)
  o Output Ripple
  o Load Transient (50 to 100% Step)

TPS73633 – LDO (3.3V@0.165A)
  o Output Ripple
  o Load Transient (50 to 100% Step)
Start-Up Waveform

Ch1: 1.2V – no load
Ch2: 3.3V – no load
Ch3: 1.8V - no load

Fig 1a: Start Up Waveform with no load on outputs
Ch1: 1.2V @ 0.66A  
Ch2: 3.3V @ 0.165A  
Ch3: 1.8V @ 0.05A  

Fig 1b: Start Up Waveform with outputs fully loaded
1.2V@0.66A (TPS 74801)

Output Ripple

Ch 1: 1.2V Output (ac coupled)
Ch 4: Load Current

Fig 2: Output Ripple for 1.2V @ 0.66A
1.2V@0.66A (TPS 74801)

Load Transient (50 to 100% Step)

Ch 1: 1.2V Output (ac coupled)
Ch4: Load Current

Fig 3: Transient response on 1.2V output. Step from 0.33A to 0.66A
3.3V@0.165A (TPS73633)

Output Ripple

Ch 1: 3.3V Output (ac coupled)
Ch 4: Load Current

Fig 4: Output ripple 3.3V @ 0.165A
3.3V@0.165A (TPS73633)

Load Transient (50 to 100% Step)

Ch 1: 3.3V Output (ac coupled)
Ch 4: Load Current

Fig 5: Load Transient on 3.3V output. Step from 0.083A to 0.165A
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