AN-1849 An Audio Amplifier Power Supply Design

ABSTRACT

This application report provides design information for a power supply for use with TI's newest offering of high-performance, ultra high-fidelity audio amplifier input stage ICs.

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1 Introduction

Analog audio circuit power supplies can have an audible effect in listening tests and a quantifiable effect in bench measurement results. Power supply designs that operate from the power mains are of three common types: Switch mode (SMPS), regulated, and unregulated power supplies.

Switch mode power supplies have become very popular, common, inexpensive, and readily available. SMPS are used extensively in computer hardware. They are well-suited for such use providing good regulation with high efficiency in a small physical size. A drawback to SMPS is the switching nature of the design which creates EMI and RFI plus electrical noise on the supply rails. Small signal analog circuits are more susceptible to noise in the form of EMI or electrical noise on the supply lines. Certain classes of amplifiers, namely Class G and Class H, may be more easily realized with SMPS that are fast responding for full audio bandwidth signals. Using SMPS for audio circuits presents additional design challenges than when using a SMPS for non-audio circuits.

A regulated supply can be a simple linear regulator IC with the rectified voltage from the transformer as input and a handful of external components or any number of more complicated and often higher performance designs. There are the tradeoffs of complexity, cost, space, thermal design, reliability, and protection with any regulated design. It is common for regulated supplies to be used for the analog small signal portions and other sensitive circuits for the best performance. For an audio power amplifier, regulated supplies need high bandwidth for good audio performance. The complexity and cost for such a power supply design may not be acceptable. Most linear regulator ICs do not have high bandwidth and are slow compared to audio signals that can result in reduced audio performance.

For simplicity, good performance, and reasonable cost, an unregulated supply is the most common for an audio power amplifier. An unregulated supply uses a transformer, a bridge rectifier, and various rail capacitors. A drawback to the unregulated supply is the voltage fluctuations with load and power mains fluctuations. A design should allow for a minimum 10% high line condition on the power mains. Unregulated supplies may have only a fuse in the power mains input to protect against excessive current unlike more sophisticated regulated designs. Additionally, the power supply voltage rails may have inline fuses to add some additional protection.

The circuit and solution presented in this application note have not been tested to any industry standards. It is the responsibility of the reader to perform standard industry testing to assure safety when using the solution in part or in whole in any form. Texas Instruments does not provide any guarantees, written or implied, about the safety of the solution.

2 Overview

This application note covers the design of a ±72-V unregulated power supply specifically for audio amplifiers.

The power supply is an unregulated design with an option to allow connection to either 120-V or 240-V mains. The design uses toroidal transformers, a fully integrated bridge, and various rail capacitors for ripple voltage reduction, noise suppression, and to act as high current reservoirs. Additional circuitry to control inrush current on power up and power down Mute control are also included. A complete schematic, PCB views, and Bill of Materials are provided for the power supply design.

3 Schematic and Design

3.1 Power Supply

Figure 1 shows the complete schematic of the power supply design. The heart of the design is the basic power supply consisting of the transformers, the bridge, and various capacitors. Many of the capacitors used may not be commercially necessary or may have a minimal effect on performance. Because the design is not a commercial design where tight cost constraints must be taken into account, additional capacitors are freely used. For a commercial design, bench and listening test, or some other test, criteria is recommended to determine the exact number, size, and type of external components required. A short explanation of the purpose of each capacitor at the primary side of the transformers, around the bridge and on the supply rails follows. Some capacitors are doubled up on the PCB for flexibility or to achieve the desired total capacitance.

- $C_1$, $C_2$, $C_4$ protect against turn on and off spikes caused when the power switch changes positions. $C_3$
• $C_{S1}$ and $C_{S2}$ are low value, ceramic capacitors to filter higher frequency noise right at the DC output of the diode bridge.

• $C_{S3}$ and $C_{S4}$ are the large reservoir capacitors to supply large current demands and stabilize the supply rails to minimize low frequency fluctuations. These are very large value electrolytic capacitors. Two capacitors are used to achieve the desired 40 000 $\mu$F capacitance per rail.

• $C_{S5}$ and $C_{S6}$ are high quality film capacitors to filter higher frequency noise. Two footprints are used on the PCB for flexibility.

• $C_{S7}$ and $C_{S8}$ act in conjunction with $R_{S1}$ and $R_{S2}$ to decouple the large electrolytic capacitors and reduce impedance.

• $C_{S9}$ and $C_{S10}$ are low value, ceramic capacitors to filter higher frequency noise from the transformer secondary AC lines at the diode bridge.

• $C_{S11}$–$C_{S14}$ are in parallel with the bridge diodes to reduce high frequency noise and ringing of the diode. An additional RC snubber in parallel with each diode of the rectifier will further reduce noise and ringing.

The values for the different capacitors were not chosen based on extensive bench work or research. The values were chosen based on general guidelines and commonly used values. Additional performance may be obtained through refinement of the capacitor values. The equations and methods to determine optimal values are beyond the scope of this application note.

Additionally, the supply rails have bleeder resistors, $R_{BL1}$ and $R_{BL2}$, to drain the large reservoir capacitors ($C_{S3}$ and $C_{S4}$). Two footprints per rail were placed on the PCB to allow for lower power resistors to be used and a wide range of bleeder current. More sophistication can be added by including an additional DPDT relay and controls to only connect the bleeder resistors below a set voltage and remain unconnected during normal operation.

The fully integrated bridge has a peel and stick heat sink attached. See Table 1 for robustness in use and higher ambient temperature conditions.
Figure 1. Complete Power Supply Circuit

4 Bill Of Materials

Table 1. Bill Of Materials

<table>
<thead>
<tr>
<th>Reference</th>
<th>Value</th>
<th>Tolerance</th>
<th>Description</th>
<th>Manufacturer</th>
<th>Part Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1, C2, C4</td>
<td>0.01 µF</td>
<td>10%</td>
<td>400 V, metalized polyester film, 7.5-mm lead spacing</td>
<td>Panasonic</td>
<td>ECQ-E4103KF</td>
</tr>
<tr>
<td>CS5A, CS5B, CS6A, CS6B</td>
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<td>10%</td>
<td>100 V, metalized polyester film, 10-mm lead spacing</td>
<td>Panasonic</td>
<td>ECQ-E1105KF</td>
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<td>CS1, CS2, CS7, CS8, CS9, CS10</td>
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<td>10%</td>
<td>100 V ceramic, X7R type, 200-mil lead spacing</td>
<td>AVX Corporation</td>
<td>SR211C104KAR</td>
</tr>
<tr>
<td>CS11, CS12, CS13, CS14</td>
<td>0.1 µF</td>
<td>10%</td>
<td>250 V, metalized polyester film, 7.5-mm lead spacing</td>
<td>Panasonic</td>
<td>ECQ-E2104KF</td>
</tr>
<tr>
<td>CS3A, CS3B, CS4A, CS4B</td>
<td>20 000 µF</td>
<td>20%</td>
<td>100-V electrolytic can</td>
<td>CDE Cornell Dubilier</td>
<td>DCMC203U100BC2B</td>
</tr>
<tr>
<td>Reference</td>
<td>Value</td>
<td>Tolerance</td>
<td>Description</td>
<td>Manufacturer</td>
<td>Part Number</td>
</tr>
<tr>
<td>-------------</td>
<td>-------</td>
<td>-----------</td>
<td>-------------------------------------------------------</td>
<td>-----------------------</td>
<td>---------------</td>
</tr>
<tr>
<td>CSR1, CSR2</td>
<td>1 µF</td>
<td>20%</td>
<td>63-V electrolytic radial, 2-mm lead spacing</td>
<td>Panasonic</td>
<td>EEU-EB1J1R0S</td>
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<td>D1</td>
<td>1 A</td>
<td></td>
<td>400-V diode, DO-41</td>
<td>Vishay Semiconductor</td>
<td>1N4004-E3/54</td>
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<td>DZ1</td>
<td>51 V</td>
<td>5%</td>
<td>2-W Zener diode, DO-41</td>
<td>Microsemi Corporation</td>
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<td>DZ2</td>
<td>43 V</td>
<td>5%</td>
<td>2-W Zener diode, DO-41</td>
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<td>2EZ43D5DO41</td>
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<td>DZM</td>
<td>3.9 V</td>
<td>5%</td>
<td>500-mW Zener diode, DO-35</td>
<td>Diodes Inc.</td>
<td>1N5228B-T</td>
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<td>RBLD1, RBLD2, RBLD3, RBLD4</td>
<td>2 kΩ</td>
<td>5%</td>
<td>5-W metal oxide</td>
<td>International Yageo Corporation</td>
<td>SQP500JB-2K0</td>
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<td>RFAN</td>
<td>1.2 kΩ</td>
<td>5%</td>
<td>5-W metal oxide</td>
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<td>SQP500JB-1K2</td>
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<td>RIR1, RIR2, RIR3</td>
<td>68 Ω</td>
<td>1%</td>
<td>5-W wire-wound silicone</td>
<td>Huntington Electric, Inc.</td>
<td>ALSR-5-68-1%</td>
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<td>RS1, RS2</td>
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<td>5%</td>
<td>¼ Watt carbon film</td>
<td>Panasonic</td>
<td>ERD-S2TJ1R0V</td>
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<td>RG</td>
<td>100 Ω</td>
<td>1%</td>
<td>¼ Watt metal film</td>
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<td>MFR-25FBF-100R</td>
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<td>RZ1</td>
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<td>1 Watt metal oxide film</td>
<td>Panasonic</td>
<td>ERG-1SJ561</td>
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<td>RZ2</td>
<td>390 Ω</td>
<td>5%</td>
<td>½ Watt carbon film</td>
<td>Panasonic</td>
<td>ERD-S1TJ391V</td>
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<tr>
<td>RPD</td>
<td>10 kΩ</td>
<td>5%</td>
<td>¼ Watt carbon film</td>
<td>Panasonic</td>
<td>ERD-S2TJ103V</td>
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<td>RL1</td>
<td>16 A</td>
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<td>48 V, 400-mW SPST, N.O., relay</td>
<td>Panasonic Electric Works</td>
<td>ALE15B48</td>
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<tr>
<td>U1</td>
<td>35 A</td>
<td></td>
<td>700-V bridge rectifier</td>
<td>Fairchild Semiconductor</td>
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<td>S1</td>
<td>6 A</td>
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<td>DPDT PCB mount, mini slide switch</td>
<td>C&amp;K Components</td>
<td>1201M2S1CQE2</td>
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<td>J1, J5</td>
<td></td>
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<td>3 pin 156-mil header, right angle, tin plating</td>
<td>Molex/Waldom Electronics Corp.</td>
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<td>J2, J9, J4A, J4B</td>
<td>2 pin 156-mil header, right angle, tin plating</td>
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<td>J3A, J3B</td>
<td></td>
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<td>4 pin 156-mil header, right angle, tin plating</td>
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<td>J7, J8, J11, J12, J13, J14, J15</td>
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<td>Transformer1, Transformer2</td>
<td>24 V, 300 VA</td>
<td>Dual primary, dual secondary, torrid transformer</td>
<td>Plitron Manufacturing Inc.</td>
<td>77060201</td>
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<td></td>
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<td>θCA = 16.5°C/W</td>
<td>CTS Electronic Components, Inc.</td>
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<td>RZ3, RZ4, DZ3, DZ4, CSF1, CSF2, CSF3</td>
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<td>Option unused circuits</td>
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**Note:** The design options for RZ3, RZ4, DZ3, DZ4, CSF1, CSF2, CSF3 are option unused circuits.
5 Additional Circuit

5.1 120 V/240 V Selection Option

For multi-country operation, a switch is included to select between 120-V or 240-V input at the primary side of the transformers. The transformers are dual primary with the switch allowing the option to put the primaries into series or parallel. The primary side of each transformer is connected in parallel for 120-V operation with series connection used for 240-V operation. The schematics, Figure 2 and Figure 3, show the different connections with the switch set for either 120-V or 240-V input from the power lines.

![Figure 2. 120V Transformer Connections, Primaries in Parallel](image)

![Figure 3. 240V Transformer Connections, Primaries in Series](image)
5.2 Inrush Current Control

A simple inrush circuit is used to limit the high current that occurs at power up. The portion of the schematic that controls inrush current is shown in Figure 4.

The inrush circuit consists of three 68-Ω/5-W resistors (R_{IR1}–R_{IR3}, labeled just R_{IR} in Figure 1 and Figure 4) in parallel, a relay and the relay controls. The R_{IR} resistors limit transformer primary current flow and the resulting secondary current flow when the transformer is powered for a softer turn on. Once the V_{CC} rail voltage exceeds 33 V, the relay is activated, shorting out the resistors. The relay is deactivated when the V_{CC} voltage falls below 10 V, resetting the circuit. The circuit is very simple and does not limit inrush current if the mains power is switched on before the V_{CC} rail drops below 10 V. The relay control consists of the R_{Z1} and R_{Z2} resistors to limit current through the voltage clamping D_{Z2} Zener diode. D_{Z2} limits the relay voltage below the maximum 48 V rating. The D_{1} diode is for the relay coil EMF and C_{SR2} is to remove ripple and stabilize the relay voltage. The oscilloscope view in Figure 5 shows how the positive rail charges up with the increase in charge rate once the relay is closed shorting out the inrush current limiting resistors. The R_{IR} resistors will get warm but they are only conducting for 500 ms each time the amplifier is powered on keeping the power dissipation well within the 5-W rating.

Figure 5. Supply Ramp at Power On
5.3 **Power Up/Down Mute Control**

The Mute function of the audio amplifier input stage IC is used for a completely quiet turn on and turn off. The amplifier is held in Mute mode until the voltage supplies are nearly stable and also goes into Mute mode once the supplies have collapsed below a determined voltage. With 40 000 μF of supply reservoir capacitance per rail, the amplifier can continue operation for some time after the mains power has been removed. The mute control circuit removes the drive signal for a quicker turn off well before the supplies have collapsed down below the minimal operating voltages. The amplifier turns off quietly and smoothly without any undesired noise. The Mute control circuit portion is shown in *Figure 6*.

![Figure 6. Mute Control](image)

The voltage threshold is set by the value of the D_{Z1} Zener diode, the current limiting R_{Z1} resistor and the forward voltage on the LED. The circuit works by simply requiring a certain positive supply rail voltage before the LED turns on and the amplifier switches out of Mute mode. The D_{Z1} Zener diode begins to conduct once the positive supply rail exceeds the rated voltage. At this point, the LED begins to develop voltage across it. The forward voltage of the LED (typically 2 V ~ 4 V) is used as the Mute voltage of the amplifier. Setting the Mute resistor on the amplifier PCB module correctly allows the amplifier to go out of Mute mode once the forward voltage of the LED is high enough to supply the needed Mute current. The LED is also used as an indicator, lighting when the amplifier is in Play mode. The values shown set the Mute voltage threshold to 57 V on power up and 58 V on power down. Because of component tolerances, the threshold voltages varies. At power down, the forward voltage of the LED collapses quickly, putting the amplifier into Mute mode well before the supplies are discharged for a quiet and relatively quick power off. *Figure 7* and *Figure 8* show the Mute signal with supply voltage at power on and power off. There is additional delay from when the Mute signal reaches the Mute threshold (~1.80 V for the amplifier PCB) and when the amplifier enters PLAY mode as a result of the mute delay capacitor on the amplifier PCB.
The $R_{ZM}$ Zener diode is for protection in the event of LED failure locking the Mute voltage so it does not exceed 4 V. The Mute resistor of the amplifier PCB module is sized for a maximum of 4 V safely limiting Mute current. $R_{PD}$ is needed so $D_{Z1}$ conducts and $C_{SR1}$ is for a steady LED/Mute voltage.

A shortcoming of the simple Mute control circuit is that the brightness of the LED varies under a heavy amplifier load with the circuit values shown in Figure 6. Either the threshold of the Mute circuit can be lowered by changing the value of $D_{Z1}$ for more consistent brightness in operation or a constant current circuit may be used. Figure 9 shows a basic constant current (LED brightness) circuit with similar threshold voltages as the Mute control circuit.
The LED first begins to light when the positive supply rail voltage exceeds 45 V. Once the positive rail reaches 60 V, the LED have 6.5 mA of current and only increase to 6.7 mA at 80 V with indiscernible change in brightness. Zener diode D\textsubscript{ZA} sets the minimum threshold for first light of the LED. Combining the values of D\textsubscript{ZA} and D\textsubscript{ZB}, along with voltage drop across R\textsubscript{1}, sets the voltage when the LED current reaches a constant value and constant brightness. R\textsubscript{3} and D\textsubscript{ZC} set the LED current and R\textsubscript{3} is used to bias Q\textsubscript{LED} and limit current through D\textsubscript{ZC}. By using a 10-V Zener diode (D\textsubscript{ZB}), the power dissipation in Q\textsubscript{LED} is kept very low so that a small transistor can be used without power dissipation concerns. The trade-off is that the D\textsubscript{ZA} Zener diode is required to dissipation about 1 W when the supply reaches 80 V. Figure 9 does not give both constant LED current and the Mute signal control as Figure 6, although the Mute control could be taken at the emitter of Q\textsubscript{LED}. An alternate circuit to combine both Figure 6 and Figure 9 is shown in Figure 10.

**Figure 9. Constant Brightness LED Circuit**

![Constant Brightness LED Circuit Diagram]

**Figure 10. Constant Brightness LED and Mute Control Circuit**

![Constant Brightness LED and Mute Control Circuit Diagram]
The circuit in Figure 10 has the same threshold voltages as in Figure 9 and similar Mute control thresholds as in Figure 6, but can also be used to control the Mute signal to the audio amplifier module. For a reduced supply voltage window from LED first light to constant brightness, $D_{ZA}$ should be increased while $D_{ZB}$ is reduced. This increases the LED first light threshold while reducing the additional voltage needed to reach the constant brightness threshold. The value of $D_{ZC}$ may also be adjusted to achieve the designed circuit response.

6 Summary

The unregulated power supply presented gives very good performance while powering an audio amplifier. While circuit modifications and additions can improve performance, the solution presented has a relatively low part count and simplicity is maintained with all circuits. The power supply provides a ±70 V to ±73 V supply under quiescent conditions with full load voltage dropping to ±59 V to ±62 V.
Figure 11. PCB Composite View From Top
Figure 12. PCB Top Silkscreen View
Figure 13. PCB Bottom Silkscreen View
Figure 15. PCB Bottom Layer View

8 Revision History

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<th>Description</th>
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<td>Initial release.</td>
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<td>1.01</td>
<td>03/15/10</td>
<td>Deleted all references to AN-1625.</td>
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<tr>
<td>2.0</td>
<td>06/27/19</td>
<td>Edited the Overview section. EDITED APPLICATION REPORT FOR CLARITY.</td>
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