AN-1572 Quick Start Guide for a 3 Amp Buck Regulator Using the LM5576 and LM25576

ABSTRACT
This application note provides an easy to use process to design a 3 Amp buck regulator using the LM5576 and LM25576 switching regulators.

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1 Introduction

The LM5576 and LM25576 switching regulators feature all of the functions necessary to implement an efficient high voltage buck regulator using a minimum of external components. These easy to use regulators include either a 42V (LM25576) or a 75V (LM5576) N-Channel buck switch with an output current capability of 3 Amps. The operating frequency is programmable from 50kHz up to 1MHz. Protection features include: current limit, thermal shutdown and remote shutdown capability. The device is available in a power enhanced TSSOP-20 package featuring an exposed die attach pad to aid thermal dissipation.

This step-by-step guide provides an easy to use process to quickly select the external components necessary to complete a design. More detailed information including theory of operation, design trade-offs and additional application guidance is available in the device datasheet. Shown below in Figure 1 is a complete schematic for a 3 Amp step-down DC-DC converter. Several external component values can be standardized for most applications. The input voltage range, output voltage and desired operating frequency dictate the remaining component values.

An Excel based spreadsheet derived from the guide is available on www.ti.com.

![Figure 1. Application Schematic](image)
2 Quick Guide Design Worksheet

Step 1. List the basic requirements:
Output Current: 0 to 3 Amp

Output Voltage: \( V_1 \) [1]
Input Voltage Min Spec: \( V_2 \) [2]
Input Voltage Max Spec: \( V_3 \) [3]

Step 2. If the Input Voltage Max Spec \( V_3 \) is less than 42V use the LM25576. If the Input Voltage Max Spec \( V_3 \) is greater than 42V but less than 75V use the LM5576. Both devices require the Input Voltage Min Spec \( V_2 \) to be greater than 6V.

Selected Regulator: \( \text{[4]} \)

Step 3. Selection of the operating frequency is a trade-off between the conversion efficiency and solution size. Operating at a high frequency, with a relatively high input voltage will severely impact the efficiency and consequently generate a lot of heat. In some applications, the selection of a high operating frequency will limit the input voltage range. The recommended maximum operating frequency for applications using LM5576 is 500 kHz. When using the LM25576 the operating frequency may be set as high as 1MHz. Select a target operating frequency from 50 kHz to 1MHz (500kHz for the LM5576). Check to see if the \( V_{in(min)} \) limits the selected operating frequency:

\[
F_{sw(max)} = \frac{V_{in(min)[2]} - (V_{out}[1] + 0.6)}{V_{in(min)[2]} \times 5.5 \times 10^{-7}}
\]  

(1)

The selected operating frequency must be less than \( F_{sw(max)} \) calculated above, if not reduce the operating frequency. Check to see if the \( V_{in(max)} \) limits the selected operating frequency:

\[
F_{sw(max)} = \frac{(V_{out}[1] + 0.6)}{V_{in(max)[3]} \times 8 \times 10^{-9}}
\]  

(2)

The selected operating frequency must be less than \( F_{sw(max)} \) calculated above, if not reduce the operating frequency.

Selected operating frequency \( F_{sw} \): \( \text{[5]} \)

Step 4. Calculate the value of Rt for the selected operating frequency.

\[
Rt = \frac{1}{F_{sw}[5]} \times 580 \times 10^{-9}
\]

\[\times 135 \times 10^{12}\]  

(3)

Selected value for R1: \( \text{[6]} \)

Step 5. Calculate the value of L1.

\[
L1 = \frac{V_{out}[1] \times (V_{in(max)[3]} - V_{out}[1])}{0.8 \times F_{sw}[6] \times V_{in(max)[3]}}
\]  

(4)

Select the nearest standard inductor value. During an overload condition the peak inductor current is limited to 4.5A nominal (5.1A maximum). The selected inductor must be rated for peak current of at least 5.1 Amps.

Selected value of L1: \( \text{[7]} \)
Step 6. Calculate the value of $C_{\text{RAMP}}$:

$$C_{\text{RAMP}} = L_1 \times 10^{-5}$$  \hspace{1cm} (5)

Selected value of $C_{\text{RAMP}}$: ___________ Farads [8]

Step 7. Set $R_{fb2}$ to 5kOhms if $V_{out}[1]$ is less than or equal to 5 Volts. If $V_{out}[1]$ is greater than 5V set $R_{fb2}$ to 10K Ohms.

Selected value of $R_{fb2}$: ___________ Ohms [9]

Calculate the value of $R_{fb1}$:

$$R_{fb1} = \frac{1.225 \times R_{fb2}[9]}{(V_{out}[1] - 1.225)}$$  \hspace{1cm} (6)

Selected value of $R_{fb1}$: ___________ Ohms [10]

Step 8. Select the re-circulating diode, D1. A Schottky type diode is required for all applications. Ultra-fast diodes are not recommended and may result in damage to the IC due to reverse recovery current transients. The reverse breakdown rating should be greater than the Input Voltage Max Spec[3], plus some safety margin. For worst case design, assume a short circuit load condition. In this case the diode will carry the output current almost continuously. This current can be as high as 5.1A. Assuming a 0.6V drop across the diode, the maximum diode power dissipation can be as high as 3W. A DPAK or SMC case is recommended.

Selected diode part number: ___________ [11]

Step 9. A good quality input capacitor(s) is necessary to limit the ripple voltage at the VIN pin while supplying most of the switch current during the on-time. The minimum RMS ripple current rating for the input capacitor(s) is 1.5Amp. A quality ceramic capacitor(s) with a low ESR is recommended. The input capacitor voltage rating should be greater than the Input Voltage Max Spec [3], plus some safety margin. A guide to select the input capacitor(s) value in proportion to the operating frequency is:

$$C_{in} = \frac{1.5}{F_{sw}[5]}$$  \hspace{1cm} (7)

Selected value for $C_{in}$: ___________ [12] Farads:

Step 10. The output capacitor(s) smooth the inductor ripple current and provide a source of charge for transient loading conditions. A good starting point for the output capacitance is to parallel a ceramic capacitor (10 µF to 47 µF) and a low ESR organic or tantalum capacitor (22 µF to 220 µF). The ceramic capacitor provides ultra low ESR to reduce the output ripple voltage and noise spikes, while the larger bulk capacitor provides a source of charge for transient loading conditions. The output capacitor voltage rating should be greater than the Output Voltage Spec [1], plus some safety margin. An approximation for the output ripple voltage is:

$$\Delta V_{out} = 0.8 \times \left( ESR + \frac{1}{8 \times F_{sw}[5] \times C_{out}} \right)$$  \hspace{1cm} (8)

Selected value for $C_{out}$: ___________ Farads [13]
Step 11. Ccomp and Rcomp configure the error amplifier gain characteristics to accomplish a stable overall loop gain. One advantage of current mode control is the ability to close the loop with only two feedback components. Calculate the value of Rcomp:

\[
R\text{comp} = 6 \times 10^4 \times R\text{fb1}[9] \times C\text{out}[13] + \frac{R\text{fb1}[9]}{V\text{out}[1]}
\]  

(9)

Selected value of Rcomp: ___________ Ohms [14]

Calculate the value of Ccomp

\[
C\text{comp} = \frac{1}{8 \times 10^3 \times R\text{comp}[14]}
\]

(10)

Selected value of Ccomp: ___________ Farads [15]

Step 12. Shown in Table 1 is the Bill of Materials for your design. Transcribe each value [#] from worksheet above into the following table. Congratulations, you're done.

### 2.1 Bill of Materials

<table>
<thead>
<tr>
<th>SCH REF</th>
<th>PART NUMBER</th>
<th>DESCRIPTION</th>
<th>VALUE</th>
<th>WORK SHEET REF</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1 (Cin)</td>
<td>C2012X7R2A103K</td>
<td>INPUT CAPACITOR</td>
<td>0.01 µ, 100V</td>
<td>[12]</td>
</tr>
<tr>
<td>C2 (Cin)</td>
<td>C2012X7R2A223K</td>
<td>OPTIONAL INPUT CAPACITOR</td>
<td>0.047 µ, 100V</td>
<td>[12]</td>
</tr>
<tr>
<td>C3 (CRAMP)</td>
<td>C2012X7R1C474M</td>
<td>RAMP CAPACITOR</td>
<td>0.47 µ, 16V</td>
<td>[8]</td>
</tr>
<tr>
<td>C4 (Css)</td>
<td>C2012X7R2A103K</td>
<td>CAPACITOR, TDK</td>
<td>0.01 µ, 100V</td>
<td>--</td>
</tr>
<tr>
<td>C5 (Ccomp)</td>
<td>C2012X7R2A223K</td>
<td>COMPENSATION CAP</td>
<td>0.047 µ, 100V</td>
<td>[15]</td>
</tr>
<tr>
<td>C6 (Cboot)</td>
<td>C2012X7R1C474M</td>
<td>CAPACITOR, TDK</td>
<td>0.47 µ, 16V</td>
<td>--</td>
</tr>
<tr>
<td>C7 (Cbyp)</td>
<td>C2012X7R2A103K</td>
<td>OUTPUT CAPACITOR</td>
<td>0.01 µ, 100V</td>
<td>--</td>
</tr>
<tr>
<td>C8 (Cout)</td>
<td>C2012X7R2A223K</td>
<td>OPTIONAL OUTPUT CAPACITOR</td>
<td>0.047 µ, 100V</td>
<td>[13]</td>
</tr>
<tr>
<td>L1</td>
<td>C2012X7R2A223K</td>
<td>INDUCTOR</td>
<td>[7]</td>
<td></td>
</tr>
<tr>
<td>R1 (Rfb1)</td>
<td>C2012X7R2A223K</td>
<td>Feedback RESISTOR</td>
<td>[10]</td>
<td></td>
</tr>
<tr>
<td>R2 (Rfb2)</td>
<td>C2012X7R2A223K</td>
<td>Feedback RESISTOR</td>
<td>[9]</td>
<td></td>
</tr>
<tr>
<td>R3 (Rt)</td>
<td>C2012X7R2A223K</td>
<td>TIMING RESISTOR</td>
<td>[6]</td>
<td></td>
</tr>
<tr>
<td>R4 (Rcomp)</td>
<td>C2012X7R2A223K</td>
<td>COMPENSATION RESISTOR</td>
<td>[14]</td>
<td></td>
</tr>
<tr>
<td>U1</td>
<td>C2012X7R2A223K</td>
<td>REGULATOR, TI</td>
<td>[4]</td>
<td></td>
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