The PMBus Address selection circuit selects between one of four address resistors on the ADDR1 pin. It is not required in an end application.
PMBus Address Selection

for MGTAVTT and MGTVAUX

FUNCTION TABLE

<table>
<thead>
<tr>
<th>Rn2</th>
<th>Rn1</th>
<th>0 TO 6, 8 TO 9</th>
</tr>
</thead>
<tbody>
<tr>
<td>L</td>
<td>L</td>
<td>D = S1</td>
</tr>
<tr>
<td>L</td>
<td>H</td>
<td>D = S2</td>
</tr>
<tr>
<td>H</td>
<td>L</td>
<td>D = S3</td>
</tr>
<tr>
<td>H</td>
<td>H</td>
<td>D = S4</td>
</tr>
</tbody>
</table>

High Current Path

PMBus Address Selection Circuit:

The PMBus Address selection circuit selects between one of four address resistors on the ADDR1 pin. It is not required in an end application.

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Iripple = ~9.1A

AGND internally connected to GND on TPS544C20

Provides address selection for MGTAVTT rail

Provides address selection for MGTVCCAUX rail

Page2.SchDoc

Sheet Title:

Size:

Mod.

 isInImage = True
Pins 13 and 17: AVCC_CS+/− not used

Pins 21 and 25: AVTT_CS+/− not used

Pins 68, 70, 76, and 78 for I2C not used

MGTAVTT on Ultrascale. Previously called MGTAVT on Gen 6 and MGTAVTT on 7-series

Pins 29, 30, 33, and 34 not used for Ultrascale. Previously called MGTAVVTT on Gen 6.

MGTVCCAUX on Ultrascale. Previously called MGTAVCCPLL on Gen 6 and MGTHVCCAUX on 7-series

MGTAVCC on Ultrascale. Previously called MGTHAVCC on Gen 6 and MGTHAVCC on 7-series

MGTVCCAUX on Ultrascale. Previously called MGTVCCAUX on Gen 6 and MGTHVCCAUX on 7-series

Pins 68, 70, 76, and 78 for I2C not used
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