Main Controller, Driver and Power Stage.

12V Input

VCCP Rail

VCCP Rail

Evaluation Schematic, TPS53625, VR12

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VCCP Rail

12V Input

VID=0.9V
VIDAdd=0(R152=150k)
ZeroLoadLine(R16=Open)
Vbus=0.9V(R18=34.67K,R4=39K)
SlewRate=12mV/us(R6=39k)
Imax=21A(R14=413k)

FSW=600kHz(R2=39k)
OCL=35A(R5=24k)
USR Level8(LeastSensitive)(R8=9.37k)
OSRLevel8(LeastSensitive)(R1=150k)
OSRDisabled(R45=open)
RAMP=100mV(R4=39k)
Main Controller, Driver and Power Stage.

12V Input

VID=0.9V  
SVIDAdd=1(R9=127k)  
ZeroLoadLine(R19=Open)  
Vth=0.9V(R24=88.89k,R39=100k)  
SlewRate=12mV/us(R42=39k)  
Imax=2.3A(R23=2.94Meg)

FSW=600kHz(R38=39k)  
OCL=10A(R41=100k)  
USR Level8(LeastSensitive)(R21=9.37k)  
OSRLevel8(LeastSensitive)(R37=150k)  
OSRDisabled(R29=open)  
RAMP=160mV(R39=100k)

Evaluation Schematic, TPS53625, VR12

VNN Rail

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