Input Voltage = 18Vin to 20Vin

*NOTE2: Applying a voltage between 0V and 3V at the "Vinj_In" node/input (TPx2) adjusts the output voltage of the converter from 30V to 54V, respectively.

The relationship between the "Vout Adj." voltage and the output voltage of the converter are also linearly scaled.

A 12.2V auxiliary supply is needed for this Output Vadj circuit.

Make sure to have the GNDs of the 12.2V auxiliary supply and that of the Output Vadj circuit (including the GND node of the source providing the 0V to 3V signal) be connected to the GND of the LM5122 circuit.

Vout = 30V to 54V @ 1.7A Max. Continuous
(or 0A to 3.4A Max. Step at 120Hz and 50% Duty Cycle)

NOTE1: All component designators with "x" suffix were added in the circuit as a modification to the original design and the PCB does not have placeholders for these components.

PROJECT IS BUILT ON PMP7950 REV1B PCB BOARD.
### Label Table

<table>
<thead>
<tr>
<th>Variant</th>
<th>Label Text</th>
</tr>
</thead>
<tbody>
<tr>
<td>001</td>
<td>LOGO</td>
</tr>
<tr>
<td></td>
<td>PCB</td>
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<tr>
<td></td>
<td>Pb-Free Symbol</td>
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</tbody>
</table>

**Assembly Note ZZ2**
These assemblies are ESD sensitive. ESD precautions shall be observed.

**Assembly Note ZZ3**
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

**Assembly Note ZZ4**
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
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