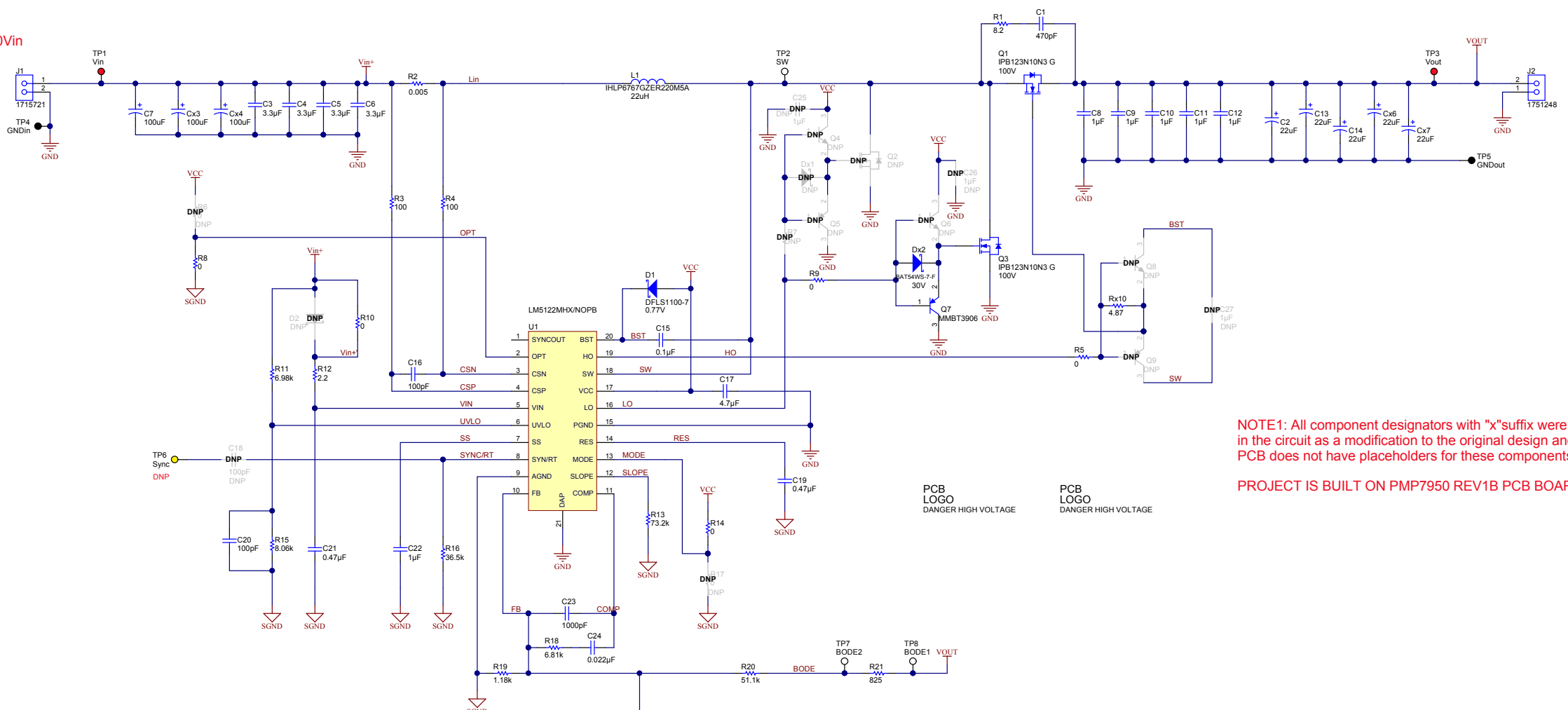


Input Voltage = 18Vin to 20Vin

Vout = 30V to 54V @ 1.7A Max. Continuous
(or 0A to 3.4A Max. Step at 120Hz and 50%
Duty Cycle)



NOTE1: All component designators with "x" suffix were added in the circuit as a modification to the original design and the PCB does not have placeholders for these components.
PROJECT IS BUILT ON PMP7950 REV1B PCB BOARD.

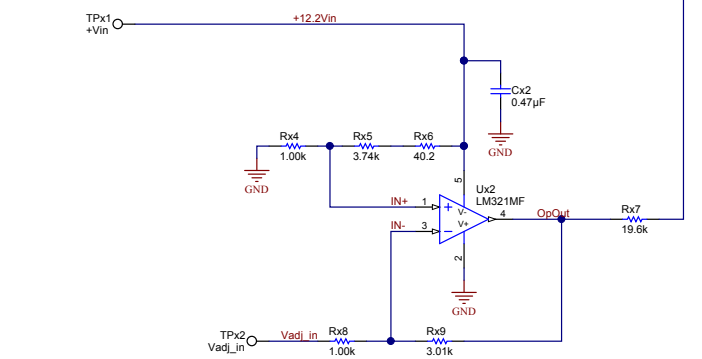
*NOTE2: Applying a voltage between 0V and 3V at the "Vadj_In" node/input (TPx2) adjusts the output voltage of the converter from 30V to 54V, respectively.

The relationship between the "Vout Adj." voltage and the output voltage of the converter are also linearly scaled.

A 12.2V auxiliary supply is needed for this Output Vadj. circuit.

Make sure to have the GNDs of the 12.2V auxiliary supply and that of the Output Vadj circuit (including the GND node of the source providing the 0V to 3V signal) be connected to the SGND of the LM5122 circuit.

+12.2V Auxiliary Supply



0Vin to 3Vin Control Voltage

H1 NY PMS 440 0025 PH H2 NY PMS 440 0025 PH H3 NY PMS 440 0025 PH H4 NY PMS 440 0025 PH

H5 1902C H6 1902C H7 1902C H8 1902C

DNP FID1 DNP FID2 DNP FID3

PCB: PMP7950 REV1B
PMP7950 REV1B

PCB LOGO
Texas Instruments

PCB LOGO
Pb-Free Symbol

Label Table	
Variant	Label Text
001	

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Designed for: Public Release		Mod. Date: 4/16/2015	
Project Title: LM5122 Adj Vout Boost			
Number: PMP10538	Rev: 1	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet 2 of 2	
Drawn By:	File: PMP10538_Hardware_ANSI-B_SchDoc	Size: B	
Engineer: Hrag Kasparian	Contact: http://www.ti.com/support		



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