ZZ1  These assemblies are ESD sensitive. ESD precautions shall be observed.
ZZ2  These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
ZZ3  These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
ZZ4  Place SH-J8 on J6, SH-J7 on J7, and SH-J9 on J9.

**COMPONENTS MARKED ’DNP’ SHOULD NOT BE POPULATED.**
**ASSEMBLY VARIANT: 001**

<table>
<thead>
<tr>
<th>PCB VIEWED FROM TOP SIDE</th>
<th>BOARD #: ISE4033</th>
<th>REV: E1</th>
<th>SUN REV: Not In VersionControl</th>
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<tr>
<td>TID #: TIDA-00486</td>
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**PLOT NAME = Top Assembly Drawing**
**TEXAS INSTRUMENTS**
COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.
ASSEMBLY VARIANT: 001

PCB VIEWED FROM BOTTOM SIDE | BOARD #: ISE4033 | REV: E1 | SUN REV: Not In VersionControl
---|---|---|---
| TID #: TIDA-00486
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