<table>
<thead>
<tr>
<th>ALL ARTWORK VIEWED FROM TOP SIDE</th>
<th>BOARD #: TIDA-00652</th>
<th>REV: E2</th>
<th>SUN REV: Not In VersionControl</th>
</tr>
</thead>
<tbody>
<tr>
<td>LAYER NAME = Bottom Paste</td>
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</tbody>
</table>
These assemblies are ESD sensitive, ESD precautions shall be observed.

These assemblies must be clean and free from flux and all contaminants. Use of no-clean flux is not acceptable.

These assemblies must comply with workmanship standards IPC-H-610 Class 2, unless otherwise specified.

Components marked "SMD" should not be populated.

Assembly Variant: [No Variations]

Layer Name: Assembly Top

Plot Name: TID0-00652.01S5

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