NOTES:
1) DNI = DO NOT INSTALL
MATCHED LENGTH TO EACH OTHER.

FPGA_CLK1 & FPGA_CLK2* MUST BE MATCHED LENGTH TO EACH OTHER.

+3.3V_DAC
+1.8V_DAC

DAC3XJ8X EVM

MAIN POWER

LEADER TECH RF SHIELD P/N SL-11356

SH1
SH2
SH3
SH4
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