Place close to LMK

Y1 CMOS version OUTPUT on PIN 4

Place close to LMK

Place near Vc pin of Y1

R20 and C31 form voltage divider
set the center frequency voltage for PLL2 reference

Shared Pad
Make sure this line does not hold LMK high before LMK can be programmed for SDO
ADC POWER SUPPLIES

Title: ADC32RFxxEVM

Date: Thursday, October 08, 2015

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