TP : 0.7 mm plated drill holes

U2 : Switch to select if VDD is supplied from DevPack or Remote control batteries. DP_PG = logical signal from DevPack
DP_P3V = Power supplied from DevPack

Default :
- PG=0
- VDD=P3V
Key Matrix:
- 31 keys, 3 columns, 11 Rows
- Shift registers used to cycle through the rows to reduce number of required IOs
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