Smart-Lock Block Diagram
BLE/MCU: CC2640R2

LPRF Bypass Capacitors & DC-DC Passives
Place L2 and C20 as close to Pin 33

Reset Switch and User Buttons

JTAG Programming Interface

I2C Pullup Resistors

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<table>
<thead>
<tr>
<th>Variant</th>
<th>Label Text</th>
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<tr>
<td>001</td>
<td>Smart-Lock</td>
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**Assembly Notes:**

- **ZZ1:** This Assembly Note is for PCB labels only.
- **ZZ2:** These assemblies are ESD sensitive, ESD precautions shall be observed.
- **ZZ3:** These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
- **ZZ4:** These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
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