Install label in silkscreened box after final wash. Text shall be 8 pt font. Text shall be per the Label Table in the PDF schematic.

Three different wire assemblies to be supplied with each board

Place the short on J4 position A

Components marked 'DNP' should not be populated.

Assembly Variant: DOI

<table>
<thead>
<tr>
<th>PCB VIEWED FROM TOP SIDE</th>
<th>BOARD #: TIDA-01182</th>
<th>REV #: A</th>
<th>SUN REV: Not In VersionControl</th>
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<tbody>
<tr>
<td></td>
<td>TID #: TIDA-01182</td>
<td></td>
<td></td>
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<tr>
<td>PLOT NAME = Top Assembly Drawing</td>
<td>GENERATED: 10-28-2016 12:16:20 PM</td>
<td></td>
<td>TEXAS INSTRUMENTS</td>
</tr>
</tbody>
</table>
COMPONENTS MARKED ‘DNP’ SHOULD NOT BE POPULATED.
ASSEMBLY VARIANT: 001

<table>
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<tr>
<th>PCB VIEWED FROM BOTTOM SIDE</th>
<th>BOARD #: TIDA-01182</th>
<th>REV: A</th>
<th>SUN REV: Not In VersionControl</th>
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<tr>
<td></td>
<td>TID #: TIDA-01182</td>
<td></td>
<td></td>
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</table>

PLOT NAME = Bottom Assembly Drawing  
GENERATED: 10/28/2016 12:16:22 PM  
TEXAS INSTRUMENTS
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