alignment use case

The overall duty cycle should be below 0.1% and pulse length below 100 ns - see datasheet.

If you operate below threshold, the optical output will be very very low. I recommend to do some low duty cycle pulsing with e.g. 2 Amps for alignment (threshold <

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PCB
LOGO
Pb-Free Symbol
FCC disclaimer

Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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