Block Diagram

V_{CNTLP} & V_{CNTLN}
Control Signals to set the virtual ground and output voltages
(Can come from DAC or external power rails)

SMPS

Positive Vin
(10V to 120V)

CSD19533
CSD19533
CSD19533

Cascaded current sharing stages

+2.5V to +100V

TPS7A4701

TLV171

TIDA-01371

V_{CNTLP}

V_{CNTLN}

Negative Vin
(-10V to -120V)

CSD19533
CSD19533
CSD19533

Cascaded current sharing stages

-2.5V to -100V

TPS7A3301

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Floating Positive Regulator

Multiple Stages can be cascaded

Stage-1

Stage-2

Stage-3

Control Voltage (Positive)

Vcc for Op-amps

Floating Positive Regulator

Your for Regulator is set at 3.3V w.r.t. Virtual-GND-Pos (Internal Vref = 1.4V)
Multiple Stages can be cascaded

Floating Negative Regulator

Control Voltage (Negative)

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Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265

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