POWER SUPPLY

Rfb = Rfbb * (Vout/Vref - 1)
(Rfb/Rfbb) * Vref + 1 = \left(\frac{261k}{100k}\right) * 0.8 + 1 = 2.888V = Vout

Mode pin pulled high, forced PWM mode

Contact:
http://www.ti.com/support

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Project Title:
Designed for:
Public Release
Assembly Variant:
[No Variations]
Aptina Image Sensor

Filters are designed for 100mV drop at full load (see simulations)
These assemblies are ESD sensitive. ESD precautions shall be observed.

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
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