Gain_In

4

2

3

6

D

C

B

A

2 5

9/7/2017

TIDA-01519_Analog_FE.SchDoc

Sheet...

reference voltage of ADC (CC1330) when the voltage scaling is disabled. VREF set to half of set maximum, 0.65V

5Ft Proximity

3

5

Gain_Out

C

E1

A

http://www.ti.com/support

Public Release

GND

B

20Hz High Pass Filter

Variable Gain Stage

Anti-Aliasing Filter (8.5kHz Cutoff)

Power Supply Filter

Reference Voltage Resistor Networks

Gain Select

Minimum dB Level at 1/10 = 85dB => 2 mV RMS

Minimum dB Level at 1/100 = 81.1dB => 4 mV RMS

Minimum dB Level at 1/1000 = 76.2dB => 8 mV RMS

* Voltages based on 10th degree polyfit prediction

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Engineer:

SVN Rev:

Number:

TID #:

Mod. Date:

Size:

File:

Designed for:

Version control disabled

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http://www.ti.com
2.14V required on inverting input to switch from Wall to Batt. This is equivalent to 4.28V from the battery. This ensures that the battery is only used once the wall supply is absent.
Alarm detector

PCB Label

<table>
<thead>
<tr>
<th>Variant</th>
<th>Label Text</th>
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<tbody>
<tr>
<td>001</td>
<td>Default Build</td>
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<tr>
<td>002</td>
<td>Minimum BOM</td>
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</table>

**Variant/Label Table**

**Variant**

- 001 Default Build
- 002 Minimum BOM

**Assembly Notes**

- **ZZ1** This Assembly Note is for PCB labels only
- **ZZ2** These assemblies are ESD sensitive, ESD precautions shall be observed.
- **ZZ3** These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
- **ZZ4** These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
- **ZZ5** Microphone U2 requires special precautions for mounting and board cleaning. See datasheet for full details.

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