Ultrasonic Signal Processor and Transducer Driver

- **AVDD**: Analog VDD
- **DOUT**: Data Out
- **VPWR**: Power Source
- **DIN**: Data In
- **SCLK**: Serial Clock
- **RXD**: Receive Data
- **TXD**: Transmit Data
- **ID**: Identification
- **IOREG**: I/O Register
- **VPWR**: Power Source
- **AVDD**: Analog VDD

**Components:***
- **C1**: 0.033 µF
- **C2**: 100 µF
- **C3**: 0.1 µF
- **C4**: 0.1 µF
- **C5**: 0.1 µF
- **C6**: 100 µF
- **R1**: 2.7k
- **R2**: 390pF
- **R3**: 100
- **C7**: 0.033 µF
- **C8**: 390pF
- **C9**: 180pF
- **C10**: 180pF
- **L1**: 3mH
- **T1**: Three-winding transformer
- **J1**: TSW-102-07-G-S
- **J2**: 300 ohm

**Notes:**
- RND and TXD route to pads for connecting external devices.

**Contact:**
http://www.ti.com/support

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**Assembly Variant:**
[No Variations]
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Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
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