I_{DEVICE} = I_{ADC} + I_{DAC1} + I_{DAC2}
915 \mu A = 415 \mu A + 250 \mu A + 250 \mu A

V_{RTD} = 0-25 mA Loop

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PCB Number: TIDM-01000
PCB Rev: A

Variant/Label Table

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<thead>
<tr>
<th>Variant</th>
<th>Label Text</th>
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<td>001</td>
<td>TIDM-01000</td>
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Variant ZZ1

These assemblies are ESD sensitive, ESD precautions shall be observed.

Variant ZZ2

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

Variant ZZ3

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
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