Differential to Single Ended Conversion

Low Pass / High Pass / Band Reject Filter

Polarity Correction

Gain Stage (G=100)

Mid Supply Reference Generation

Clock Generation

Reset Generation

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### Variant/Label Table

<table>
<thead>
<tr>
<th>Variant</th>
<th>Label Text</th>
</tr>
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<tbody>
<tr>
<td>001</td>
<td>ChangeMe!</td>
</tr>
<tr>
<td>002</td>
<td>ChangeMe!</td>
</tr>
</tbody>
</table>

- **PCB Label**  
  Size: 0.65" x 0.20"  
  This Assembly Note is for PCB labels only.

- **Assembly Note**  
  These assemblies are ESD sensitive, ESD precautions shall be observed.

- **Assembly Note**  
  These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

- **Assembly Note**  
  These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
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