xWR1642BOOST-ODS

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Revision History

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<th>ECN #</th>
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<tr>
<td>B</td>
<td>1</td>
<td>22/01/2018</td>
<td>Vivek Dham</td>
<td>Added switch control to move between SPI and CAN interface.</td>
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<tr>
<td>B</td>
<td>2</td>
<td>22/01/2018</td>
<td>Vivek Dham</td>
<td>Enabled by default the 5V supply from the 60pin HD connector.</td>
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<tr>
<td>B</td>
<td>3</td>
<td>22/01/2018</td>
<td>Vivek Dham</td>
<td>Enabled by default the SPI and CAN signal connection to J1 connector.</td>
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<tr>
<td>B</td>
<td>4</td>
<td>22/01/2018</td>
<td>Vivek Dham</td>
<td>Serial falt detection updated to M35012052/F2N0.</td>
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<tr>
<td>B</td>
<td>5</td>
<td>22/01/2018</td>
<td>Vivek Dham</td>
<td>Added series resistors on I2C lines.</td>
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<tr>
<td>B</td>
<td>6</td>
<td>13/02/2018</td>
<td>Vivek Dham</td>
<td>Removed the series diode on the NRST signal.</td>
</tr>
<tr>
<td>B</td>
<td>7</td>
<td>23/02/2018</td>
<td>J Quintal</td>
<td>Added Variant 002, U2, PCB Label, revised AWR1642 to xWR1642.</td>
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OPTIONS FOR INTERNAL DEBUG ONLY

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Sheet Title: VPP SUPPLY LDO

Sheet: 1 of 1

File: http://www.ti.com/support

Design Title: PIC8501

Orderable: AWR1642BOOST-ODS

Drawing By: Vivek Dham

Engineer: Vivek Dham

TID #: N/A

Description: PIC8501

Specifications: PIC8501

Revision: B

Revision Date: 4/17/2018

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TID #: N/A

Description: PIC8501

Specifications: PIC8501

Revision: B

Revision Date: 4/17/2018

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http://www.ti.com
CONTROLS FOR THE PMIC

THE 3V3 OUTPUT FROM PMIC IS USED AS PGOOD.

SNUBBER ON SWITCHING NODES

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DXS110(1/2)

BY DEFAULT THE XDS SUPPLY IS DISABLED.
GETS ENABLED ONLY ONCE THE PMIC IS POWERED UP.
CAN INTERFACE

MUX BETWEEN SPI AND CAN INTERFACE
SOP HEADERS

- SOP_MODE1 = "000" SCAN/ATPG
- SOP_MODE2 = "011" DEV/FLED/DRB
- SOP_MODE3 = "000" TED
- SOP_MODE4 = "000" CAN_article "DEFAULT VALUE FOR OUTPUTS"
- SOP_MODE5 = "000" DEV MANAGEMENT -> FOR FLASHING

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- This schematic and its associated text is not in version control.
- SVN Rev: PROC049B_SOP...
ONBOARD TEMP SENSORS

DEFAULT I2C ADDRESS 0X49
AND MMWAVE DEVICE
TEMP SENSOR AWAY FROM PMIC

DEFAULT I2C ADDRESS 0X48
TEMP SENSOR CLOSE TO PMIC
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